











CERTIFICATE OF PARTICIPATION

This Is To Certify That

S Sanjay Ramasamy

has participated in Mastering FPGA Design: A Hands-On Journey with Quartus Software held during graVITas'24 from 27th to 29th September at Vellore Institute of Technology, Vellore

Dr. Sharmila N

Convenor, graVITas'24