SANJAY RAMASAMY

SECOND YEAR STUDENT

CONTACT

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- 📍 Vellore
- 🌐 https://ssanjayramasamy.github.io/WEB-PORTFOLIO/

AREAS OF SPECIALIZATION

Digital design • Verilog • Problem Solving • Debbuging • Testing

LANGUAGES

English ● ● ● ● ●
Tamil ● ● ● ● ●
French ● ● ● ● ●

INTREST

- Movies and Series
- Esports
- Sports

PROFILE

I am a second-year B.Tech student at Vellore Institute of Technology, specializing in Electrical and Electronics Engineering (EEE). With a strong passion for the VLSI domain, I have developed proficiency in digital design and Verilog, which has fueled my interest in working on cutting-edge technologies in chip design and semiconductor systems.

Eager to apply my knowledge in real-world projects, I am constantly learning and improving my skills in hardware description languages and integrated circuit design. I am excited to explore opportunities where I can contribute to innovative digital systems while expanding my expertise in the field.

EDUCATION

2027 — VELLORE INSTITUTE OF TECHNOLOGY
Engineering B.Tech EEE
● 8.85 CGPA 🏛️

2023 — VIDHYA MANDHIR SENIOR SECONDARY SCHOOL
12TH-BIO-MATHS
● 84% 🏛️

2021 — DON BOSCO SCHOOL OF EXCELLENCE
10TH ● 92% 🏛️

PROGRAMMING

- VERILOG
- JAVA
- PHYTHON

TOOLS AND SOFTWARE PROFICIENCY

CADENCE VIRTUOSO
Proficient in custom IC design, layout, and schematic generation for analog and digital circuits

XILINX VIVADO
Proficient in FPGA design, synthesis, and implementation, focusing on RTL design and functional verification.

MATLAB SIMULINK
Basic proficiency in modeling and simulating dynamic systems, with foundational knowledge in control systems and power electronics applications.