

Assignment-RSA implementation in C

(Cryptography & Security Implementation)

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1.Solutions:

Two programmes have been attached with it. Once the two primes are of 512 bits, each. After that one prime is of 768 bit, another is of 1024 bit.

The following steps have been followed-

- i) Prime Generation : Generate two primes of specific bits. The bits are choen uniformly at random using **mpz_urandomb**.
- ii) Finding Totient function : Computes the produvt of two primes as ' N ', the Eulers' phi function is $\phi(N)=(p-1)(q-1)$.
- iii) Key generation: We have used one of the commonly known public exponent $e=65537$. Compute the private key ' d ' (the multiplicative inverse of e) using **mpz_invert**.
- iv) Enc & Dec : Encryption & Decryption have been done & verified that dycrypted message is exactly same as the original plain text.
- v) Clock cycle calculation : We have used **rdtsc** for measuring the clock cycle fo each operation.
- vi) Result(output) : Minimum, maximum, average clock cycles for each operation.

2. The clock cycle measurement table:

A) When the two primes are of 512 bit each :

(For 10000 trials)

<u>Operation</u>	<u>Min clk cycle</u>	<u>Max clk cycle</u>	<u>Avg clk cycle</u>
First prime generation	<i>1133328</i>	<i>40244957</i>	<i>3264389.88</i>
Second prime generation	<i>1131816</i>	<i>43562618</i>	<i>3265428.54</i>
Computing ' N '	<i>168</i>	<i>23436</i>	<i>473.21</i>
Computing ϕ	<i>252</i>	<i>177436</i>	<i>567.90</i>

Clock cycles for computing ' d ' : *4396*

Clock cycle for Encryption: 19068

Clock cycle for Decryption: 242928

(For 50000 trials)

<u>Operation</u>	<u>Min clk cycle</u>	<u>Max clk cycle</u>	<u>Avg clk cycle</u>
First prime generation	<i>1139628</i>	<i>47239976</i>	<i>3184736.67</i>
Second prime generation	<i>1133888</i>	<i>39302508</i>	<i>3183564.31</i>
Computing ' N '	<i>168</i>	<i>261716</i>	<i>459</i>
Computing ϕ	<i>252</i>	<i>156800</i>	<i>511.69</i>

Clock cycles for computing ' d ' : *3976*

Clock cycle for Encryption: 17164

Clock cycle for Decryption: 239232

(For 100000 trials)

<u>Operation</u>	<u>Min clk cycle</u>	<u>Max clk cycle</u>	<u>Avg clk cycle</u>
First prime generation	<i>1128288</i>	<i>22509760</i>	<i>3122441.76</i>
Second prime generation	<i>1130780</i>	<i>32031131</i>	<i>3115301.68</i>
Computing ' <i>N</i> '	<i>168</i>	<i>207088</i>	<i>381.02</i>
Computing <i>phi</i>	<i>252</i>	<i>145320</i>	<i>437.77</i>

Clock cycles for computing '*d*': *2352*

Clock cycle for Encryption: 18004

Clock cycle for Decryption: *238700*

(For 1000000 trials)

<u>Operation</u>	<u>Min clk cycle</u>	<u>Max clk cycle</u>	<u>Avg clk cycle</u>
First prime generation	<i>1129184</i>	<i>47305804</i>	<i>3145238.24</i>
Second prime generation	<i>1126888</i>	<i>44003540</i>	<i>3143621.52</i>
Computing ' <i>N</i> '	<i>168</i>	<i>2151800</i>	<i>420.48</i>
Computing <i>phi</i>	<i>252</i>	<i>1720852</i>	<i>487.55</i>

Clock cycles for computing '*d*': *7588*

Clock cycle for Encryption: 18116

Clock cycle for Decryption: *262864*

B) When the first prime is of 768 bit & the second one is of 1024 bit:

(For 1000 trials)

<u>Operation</u>	<u>Min clk cycle</u>	<u>Max clk cycle</u>	<u>Avg clk cycle</u>
First prime generation	<i>3117072</i>	<i>51576868</i>	<i>10695239.32</i>
Second prime generation	<i>6514928</i>	<i>156810136</i>	<i>28512964.29</i>
Computing ' <i>N</i> '	<i>476</i>	<i>10248</i>	<i>1313.42</i>
Computing <i>phi</i>	<i>560</i>	<i>5740</i>	<i>1123.72</i>

Clock cycles for computing '*d*' : *8176*

Clock cycle for Encryption: *109872*

Clock cycle for Decryption: *1238272*

(For 10000 trials)

<u>Operation</u>	<u>Min clk cycle</u>	<u>Max clk cycle</u>	<u>Avg clk cycle</u>
First prime generation	<i>3118136</i>	<i>80518838</i>	<i>10935252.79</i>
Second prime generation	<i>6460832</i>	<i>230366529</i>	<i>28203228.76</i>
Computing ' <i>N</i> '	<i>448</i>	<i>54404</i>	<i>1194.88</i>
Computing <i>phi</i>	<i>532</i>	<i>127988</i>	<i>1264.87</i>

Clock cycles for computing '*d*' : *10248*

Clock cycle for Encryption: *108836*

Clock cycle for Decryption: *1203160*

(For 50000 trials)

<u>Operation</u>	<u>Min clk cycle</u>	<u>Max clk cycle</u>	<u>Avg clk cycle</u>
First prime generation	<i>3114832</i>	<i>285149844</i>	<i>11488944.51</i>
Second prime generation	<i>6463996</i>	<i>776917538</i>	<i>29629981.53</i>
Computing ' <i>N</i> '	<i>392</i>	<i>715596</i>	<i>1431.40</i>
Computing <i>phi</i>	<i>504</i>	<i>472136</i>	<i>1531.95</i>

Clock cycles for computing '*d*' : *12180*

Clock cycle for Encryption: *448224*

Clock cycle for Decryption: *1459360*

(For 100000 trials)

<u>Operation</u>	<u>Min clk cycle</u>	<u>Max clk cycle</u>	<u>Avg clk cycle</u>
First prime generation	<i>3136280</i>	<i>115129080</i>	<i>110773197.14</i>
Second prime generation	<i>6497512</i>	<i>297725904</i>	<i>28580636.04</i>
Computing ' <i>N</i> '	<i>420</i>	<i>151900</i>	<i>1476.86</i>
Computing <i>phi</i>	<i>504</i>	<i>200312</i>	<i>1587.26</i>

Clock cycles for computing '*d*' : *18284*

Clock cycle for Encryption: *303268*

Clock cycle for Decryption: *1440012*

3.) System Specification:

CPU: AMD Ryzen 5 7520U with Radeon Graphics(Cores: 4 | Threads: 8 | Arch: x86_64)

RAM Size: 2075 GB

Operating System : Ubuntu 24.04.3 LTS

GCC Version: gcc (Ubuntu 13.3.0-6ubuntu2~24.04) 13.3.0

Library used: GNU Multiple Precision Arithmetic Library(GMP)

4) Observation about the results(outputs):

- i) Prime generation: The average clock cycles increase with the key size. The large gap between minimum clock cycle & maximum clock cycles indicates the variability in the number of iterations needed for finding primes using `mpz_nextprime`.
- ii) Phi function: the clock cycle for calculation Phi function is very very fast compare to the clock cycles for finding primes. Although, when the primes get bigger, naturally the clock cycles is increasing.
- iii) Key generation('d') : The clock cycles increases when the prime size increses. In my observation, while performing RSA taking 512bit primes, the key generation clock cycles decreases when number of trials increases(from 10k to 1lakh). But for 1 million trials, this clock cycles is a little bit high compare to the other. When, we have took 768 bit & 1024 bit primes, the clock cycles for findind 'd' is much high than the previos case.
- iv)Encryption & Decryption: Always we have less clock cycle for encryption than the clock cycle for decryption. We, are getting success for every decryption scenario, by checking that the original plain text is same as decrypted message.
- v) Dependency on CPU(system): The output i.e. the number clock cycles for every case & for each operation, it depends on the system characteristics. The updated gcc version may affect in the optimizing performance.

5)References :

- i) Open ChatGPT : taken suggestion for appropriate code & its modification, usage of some functions(that is used in programm).
- ii)Website: www.geeksforgeeks.org
- iii)The paper, " RSA Cryptography Algorithm & its Applications to Security System by Using Linear Congruence: by Souad Mugassabi"