

I/O-efficient Manipulation of Binary Decision Diagrams

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Adiar: Binary Decision Diagrams in External Memory. 2022



Contents

What are Binary Decision Diagrams?

Why do they break?

How can we fix it?

- CountPaths

- Apply

- Equality Checking

Contents

What are Binary Decision Diagrams?

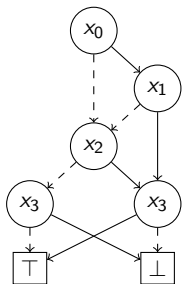
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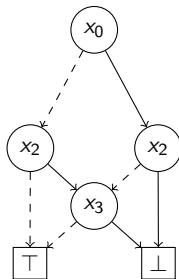
CountPaths

Apply

Equality Checking



(a) $(x_0 \wedge x_1 \wedge x_3) \vee (x_2 \oplus x_3)$

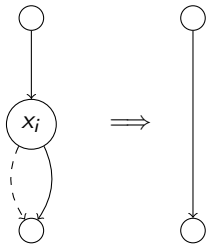


(b) $\neg(x_0 \text{ ? } x_2 \wedge x_3 : x_2 \wedge x_3)$

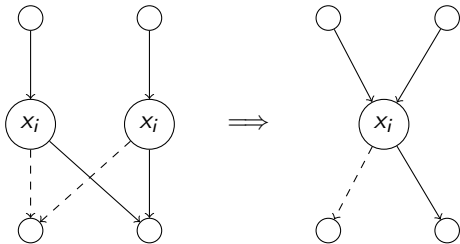
Examples of (Reduced Ordered) Binary Decision Diagrams.

Theorem (Bryant '86)

For a fixed variable order, if one exhaustively applies the two rules below, then one obtains the Reduced OBDD, which is a unique canonical form of the function.



(1) Remove redundant nodes



(2) Merge duplicate nodes

```

bdd_apply( $f$ ,  $g$ ,  $\otimes$ ):
  if  $f, g \in \{\perp, \top\}$ 
  then  $f \otimes g$ 
  else let  $i = \text{top}(f.\text{var}), g.\text{var}$ 
          $t = \text{bdd\_apply}(f[x_i := \top], g[x_i := \top], \otimes)$ 
          $e = \text{bdd\_apply}(f[x_i := \perp], g[x_i := \perp], \otimes)$ 
  in make_node( $i, t, e$ )

```

```

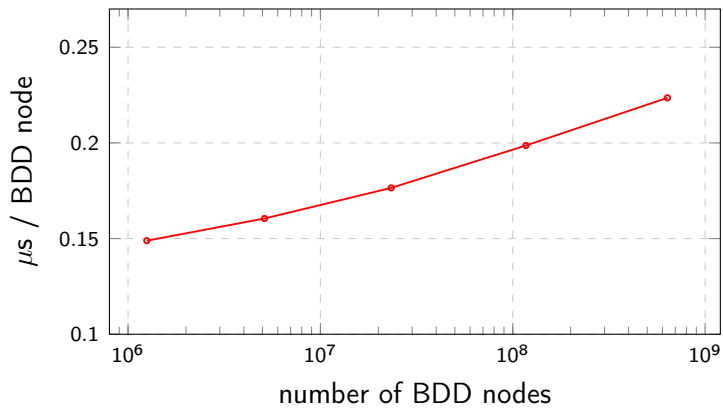
bdd_apply( $f$ ,  $g$ ,  $\otimes$ ):
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  in make_node( $i, t, e$ )

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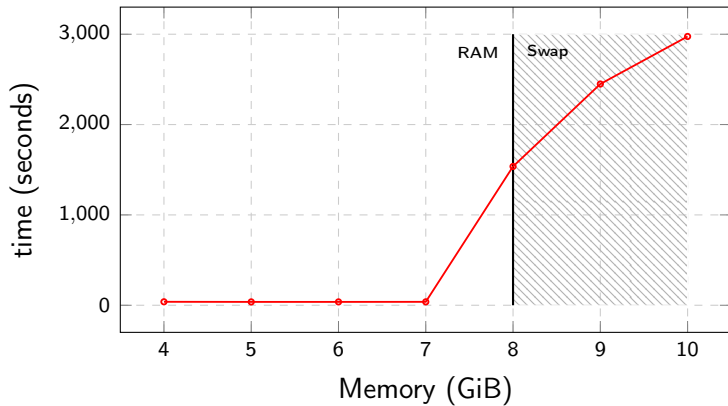
Theorem

`bdd_apply` runs in $O(N_f \cdot N_g)$ time.

- Memoisation (*Computation Cache*) ensures each recursion is computed only once.
- Reduction Rules can be maintained within `make_node(i, t, e)` in $O(1)$ time.
 - 1 Redundancy is resolved with an if-statement.
 - 2 Duplication is avoided with a hash table (*Unique Node Table*).



Running time of *BuDDy* for the *N*-Queens problem.



Running time of *BuDDy* for *Tic-Tac-Toe* with $N = 21$.

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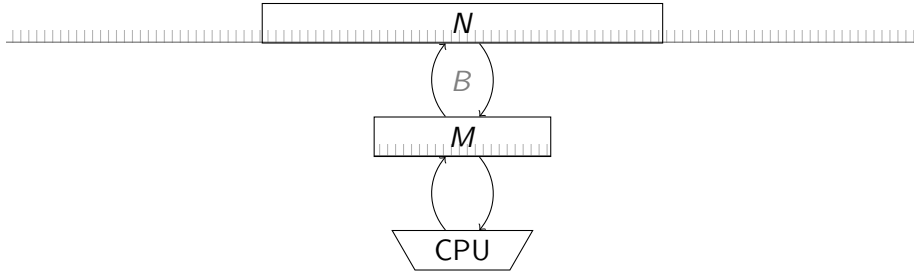
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How can we fix it?

CountPaths

Apply

Equality Checking



The I/O model by Aggarwal and Vitter '87

For any realistic values of N , M , and B we have that

$$N/B < \text{sort}(N) \triangleq N/B \cdot \log_{M/B} N/B \ll N ,$$

Theorem (Aggarwal and Vitter '87)

N elements can be sorted in $\Theta(\text{sort}(N))$ I/Os.

Theorem (Arge '95)

N elements can be inserted in and extracted from a Priority Queue in $\Theta(\text{sort}(N))$ I/Os.

CountPaths : *Example*

Algorithm	Time Complexity
bdd_pathcount	$O(N_f)$
bdd_not	$O(N_f)$
bdd_restrict	$O(N_f)$
bdd_apply	$O(N_f \cdot N_g)$
bdd_equal	$O(1)$

Algorithm	I/O-Complexity
bdd_pathcount	$O(N_f)$
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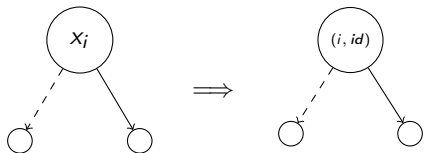
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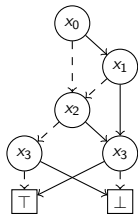
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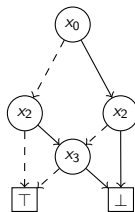


$$(i_1, id_1) < (i_2, id_2) \equiv i_1 < i_2 \vee (i_1 = i_2 \wedge id_i < id_j)$$



[((0, 0), (2, 0), (1, 0)) ,
 ((1, 0), (2, 0), (3, 1)) ,
 ((2, 0), (3, 0), (3, 1)) ,
 ((3, 0), T, F) ,
 ((3, 1), F, T)]

(a) $(x_0 \wedge x_1 \wedge x_3) \vee (x_2 \oplus x_3)$



[((0, 0), (2, 0), (2, 1)) ,
 ((2, 0), F, (3, 0)) ,
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 ((3, 0), T, F)]

(b) $\neg(x_0 ? x_2 \wedge x_3 : x_2 \wedge x_3)$

Node-based representation of prior shown BDDs

CountPaths : *Example*

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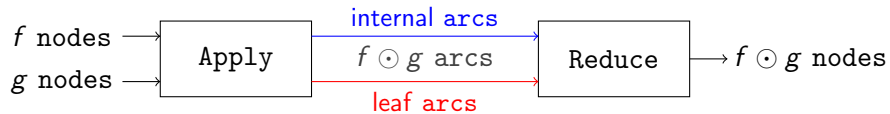
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CountPaths

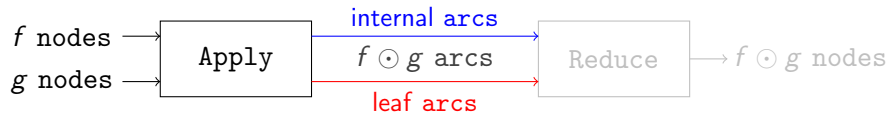
Apply

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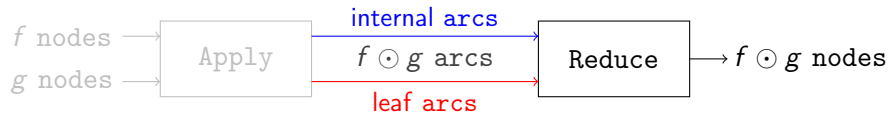


Apply



Apply : *Example*

Apply



Apply : *Example* (Continued)

Algorithm	I/O-Complexity
bdd_pathcount	$O(\text{sort}(N_f))$
bdd_not	$O(N_f/B)$
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$$f \leftrightarrow g \equiv \top$$

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$$f \leftrightarrow g \equiv \top$$

$$\underbrace{O(\text{sort}(N^2))}_{\text{Apply}} + \underbrace{O(\text{sort}(N^2))}_{\text{Reduce}} + \underbrace{O(1)}_{\text{check is } \top} = O(\text{sort}(N^2))$$

Equality Checking

Theorem (Bryant '86)

Let π be a variable order and $f : \mathbb{B}^n \rightarrow \mathbb{B}$ then there exists a unique (up to isomorphism) Reduced Ordered Binary Decision Diagram representing f with ordering π .

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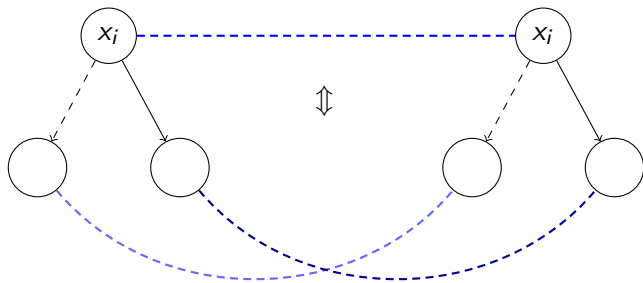
Trivial cases: $f \neq g$ if there is a mismatch in

- $N_f \neq N_g$ Number of nodes $O(1)$ I/Os
- $L_f \neq L_g$ Number of levels $O(1)$ I/Os
- $N_{f,i} \neq N_{g,i}$ Number of nodes on a level $O(L/B)$ I/Os
- $L_{f,i} \neq L_{g,i}$ Label of an i th level $O(L/B)$ I/Os

Equality Checking

Theorem (Bryant '86)

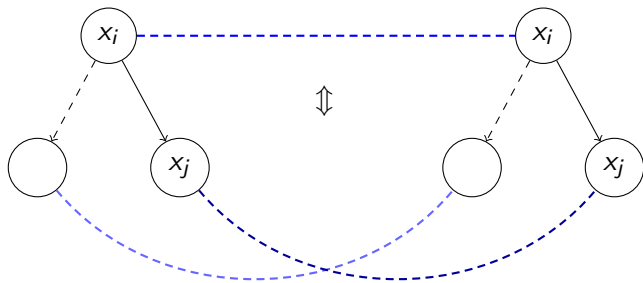
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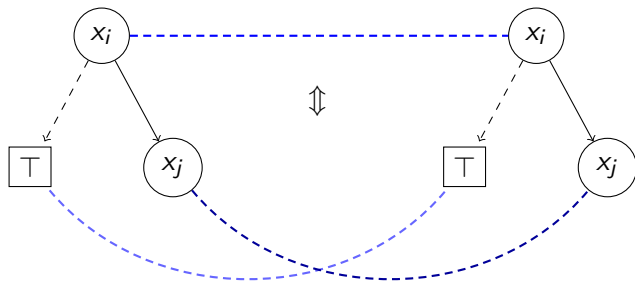
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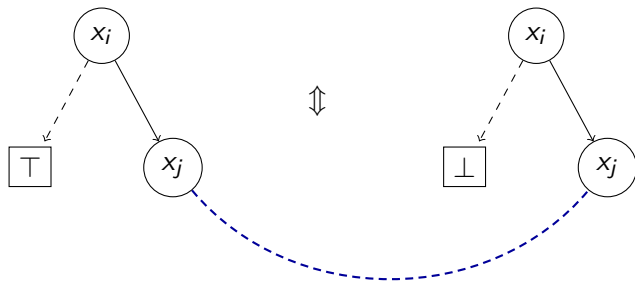
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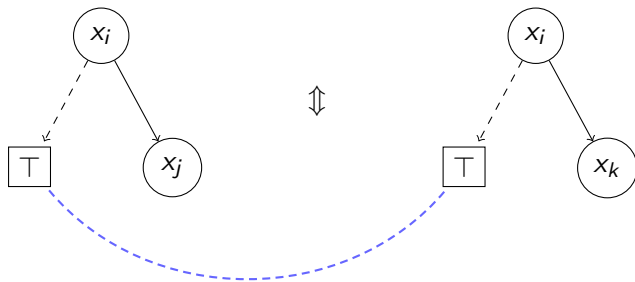
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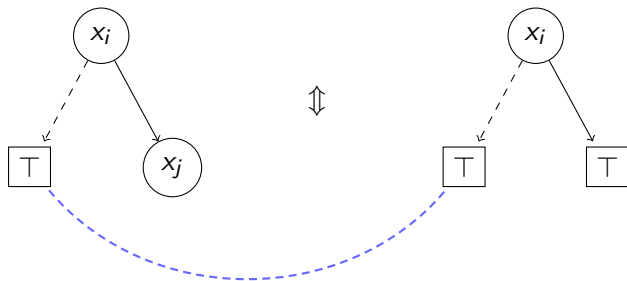
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Theorem (Bryant '86)

Let π be a variable order and $f : \mathbb{B}^n \rightarrow \mathbb{B}$ then there exists a unique (up to isomorphism) Reduced Ordered Binary Decision Diagram representing f with ordering π .

`IsIsomorphic(f , g)`

- Check whether root v_f of f and root v_g of g have a local violation.
- Check $low(v_f) \sim low(v_g)$ and $high(v_f) \sim high(v_g)$ “recursively”.

Return false on first violation. If there are no violations then return true.

Equality Checking

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$\text{IsIsomorphic}(f, g)$

- Check whether root v_f of f and root v_g of g have a local violation.
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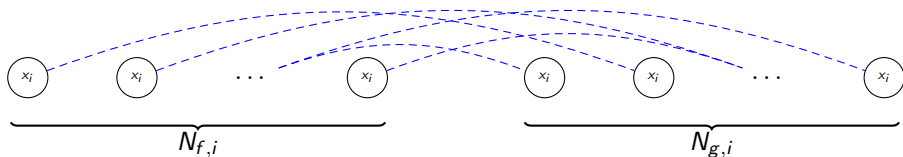
Return false on first violation. If there are no violations then return true.

$$\underbrace{O(\text{sort}(N^2))}_{\text{Apply'}} + \underbrace{O(\text{sort}(N^2))}_{\text{Reduce}} + \underbrace{O(1)}_{\text{check is } \top} = O(\text{sort}(N^2))$$

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Theorem (Bryant '86)

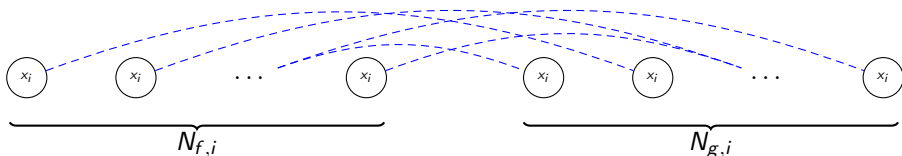
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Theorem (Bryant '86)

Let π be a variable order and $f : \mathbb{B}^n \rightarrow \mathbb{B}$ then there exists a unique (up to isomorphism) Reduced Ordered Binary Decision Diagram representing f with ordering π .



Return false if more than $N_{f,i} = N_{g,i}$ pairs of nodes are checked on level i .

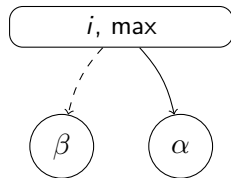
$$\underbrace{O(\text{sort}(\sum_i N_{f,i}))}_{\text{Apply}''} = O(\text{sort}(N))$$

Equality Checking

Observation

Each level output by the Reduce algorithm has the following properties:

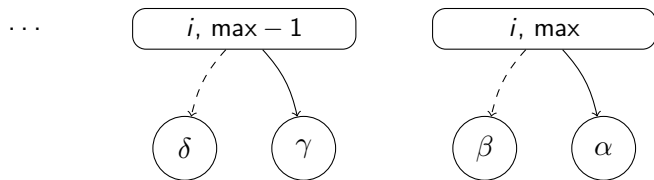
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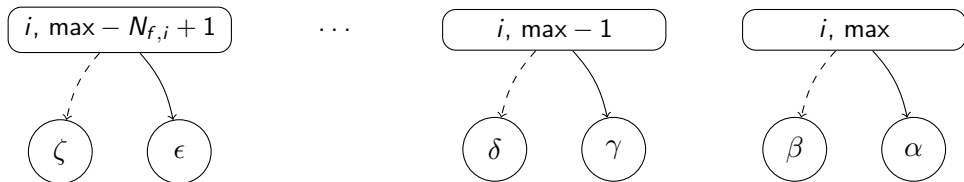
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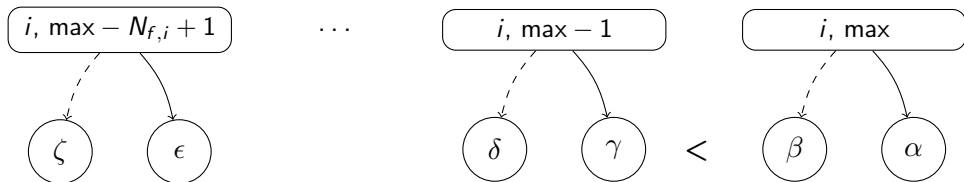


Observation

Each level output by the Reduce algorithm has the following properties:

- Nodes on level i have their identifiers *consecutively* numbered.

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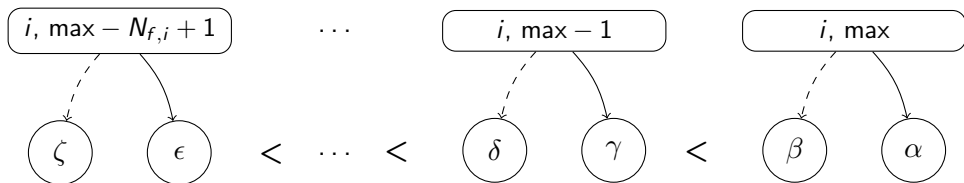


Observation

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Equality Checking



Observation

Each level output by the Reduce algorithm has the following properties:

- Nodes on level i have their identifiers *consecutively* numbered.
- Nodes on level i are output sorted by their children.

Equality Checking

Theorem

If G_f and G_g are outputs of Reduce.

$G_f \sim G_g \iff \text{For all } i \in [0; N_f) \text{ the node } G_f[i] \text{ matches } G_g[i] \text{ numerically.}$

Proof.

\Leftarrow : Must describe the exact same graph.

\Rightarrow : Strong induction on BDD levels bottom-up.



Equality Checking

Theorem

If G_f and G_g are outputs of Reduce.

$G_f \sim G_g \iff$ For all $i \in [0; N_f)$ the node $G_f[i]$ matches $G_g[i]$ numerically.

Proof.

\Leftarrow : Must describe the exact same graph.

\Rightarrow : Strong induction on BDD levels bottom-up. □

Corollary

If G_f and G_g are outputs of Reduce then $f \equiv g$ is computable using $2 \cdot N/B$ I/Os.

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Depth-First

Time-Forwarded

$O(N_f)$

$O(\text{sort}(N_f))$

$O(N_f \cdot N_g)$

$O(\text{sort}(N_f))$

$O(1)$

$2N/B$