Random Access on Narrow Decision Diagrams in External Memory

Steffan Christ Sølvsten, Casper Moldrup Rysgaard, and Jaco van de Pol SPIN 2024



Adiar

I/O-efficient Decision Diagrams

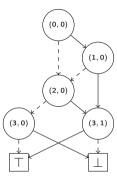
github.com/ssoelvsten/adiar

£ Features

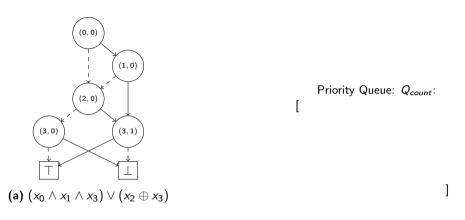


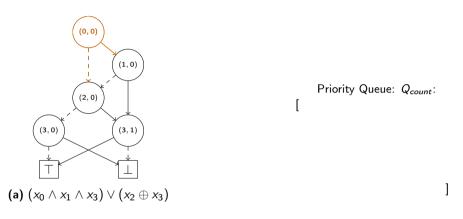


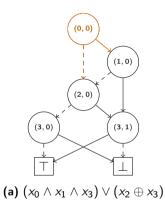


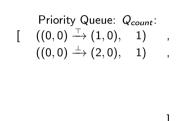


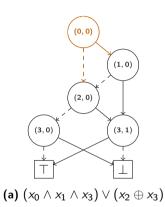
(a) $(x_0 \land x_1 \land x_3) \lor (x_2 \oplus x_3)$







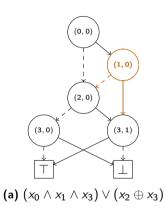




Seek	Sum	Result
(1,0)	0	0

Priority Queue:
$$Q_{count}$$
:
$$[((0,0) \xrightarrow{\top} (1,0), 1), ((0,0) \xrightarrow{\perp} (2,0), 1),$$

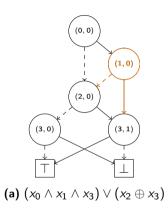
]



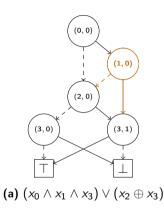
Seek	Sum	Result
(1,0)	0	0

Priority Queue:
$$Q_{count}$$
:
$$[((0,0) \xrightarrow{\top} (1,0), 1), ((0,0) \xrightarrow{\perp} (2,0), 1),$$

]



See (1,0		um 1	Re	sul 0
[Priority Q $((0,0) \xrightarrow{\perp}$,
]

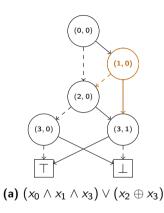


Seek	Sum	Result
(1,0)	1	0

Priority Queue:
$$Q_{count}$$
:

[

((0,0) $\xrightarrow{\perp}$ (2,0), 1), ((1,0) $\xrightarrow{\perp}$ (2,0), 1), ((1,0) $\xrightarrow{\top}$ (3,1), 1),



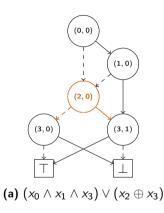
Seek	Sum	Result
(2,0)	0	0

Priority Queue:
$$Q_{count}$$
:

$$((0,0) \xrightarrow{\perp} (2,0), \quad 1) \quad ,$$

$$((1,0) \xrightarrow{\perp} (2,0), \quad 1) \quad ,$$

$$((1,0) \xrightarrow{\top} (3,1), \quad 1) \quad ,$$

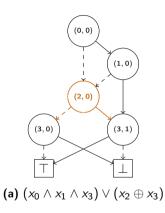


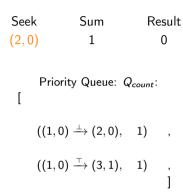
Seek Sum Result
$$(2,0)$$
 0 0

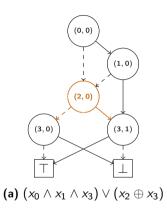
Priority Queue:
$$Q_{count}$$
:
$$((0,0) \xrightarrow{\perp} (2,0), \quad 1) \quad ,$$

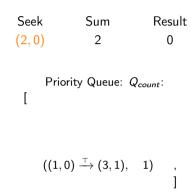
$$((1,0) \xrightarrow{\top} (2,0), \quad 1) \quad ,$$

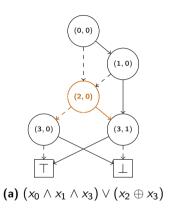
$$((1,0) \xrightarrow{\top} (3,1), \quad 1) \quad ,$$



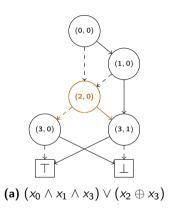




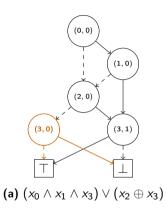




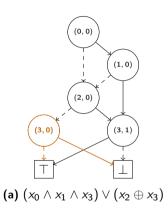
Priority Queue: Q_{count} :

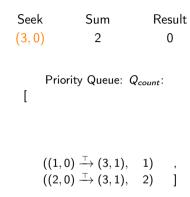


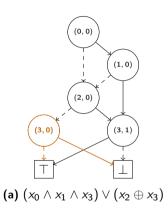
Seek (3,0)	Sum 0	Re	sul 0
Pric [ority Queue: G) _{count} :	
((1,	$0) \xrightarrow{\perp} (3,0),$ $0) \xrightarrow{\top} (3,1),$ $0) \xrightarrow{\top} (3,1),$	2) 1) 2)	, ,]

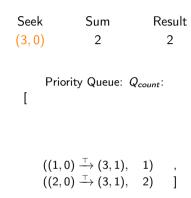


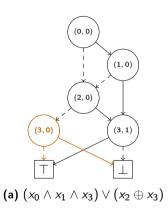
Seek	Sum	Result
(3,0)	0	0
Pric [ority Queue: (Q _{count} :

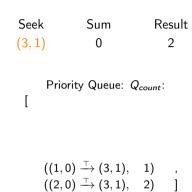


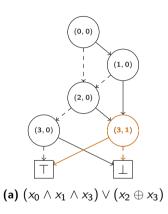


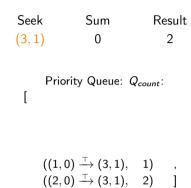


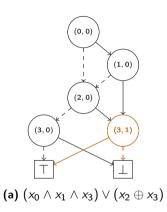








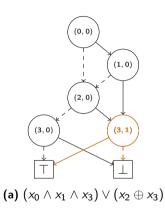


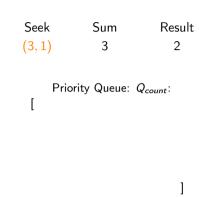


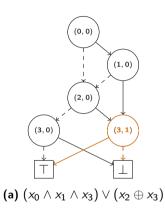


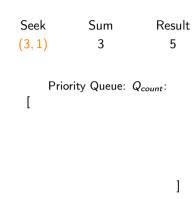
Priority Queue: Q_{count}:

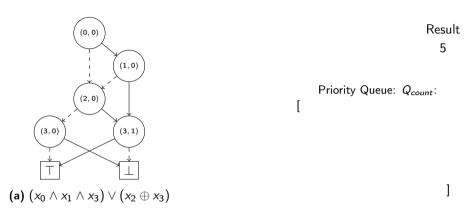
$$((2,0) \xrightarrow{\top} (3,1), \quad 2) \qquad]$$



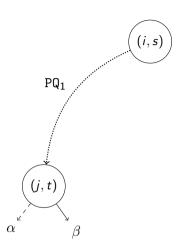


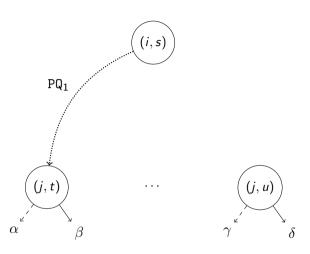


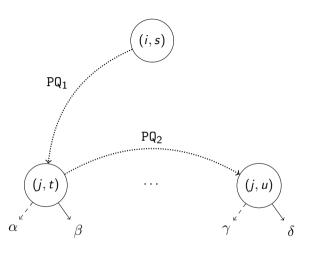


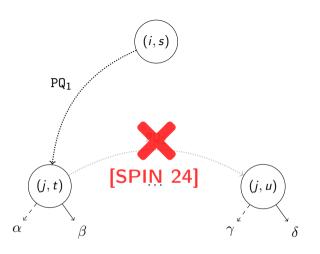


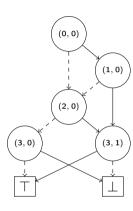


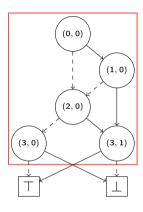


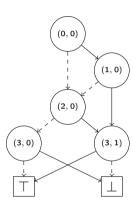


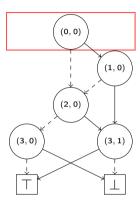


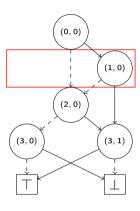


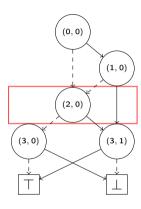


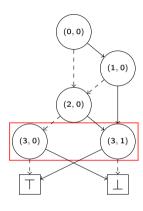


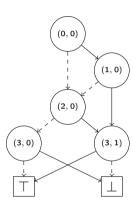


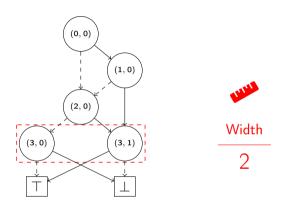


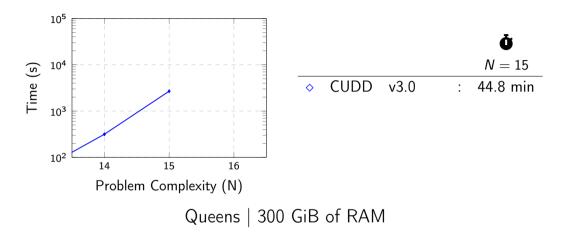


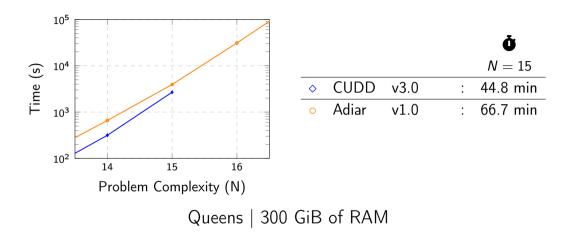


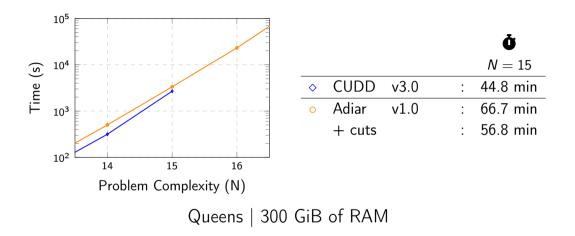


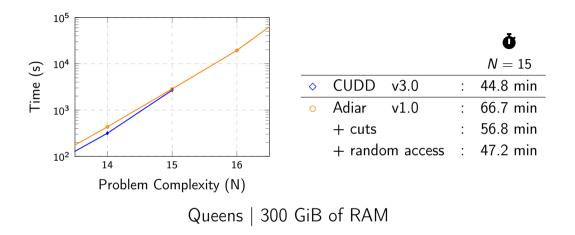


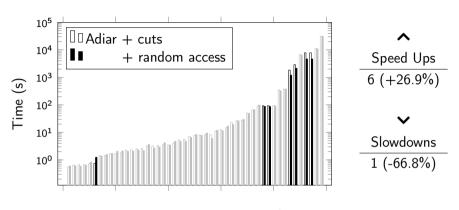












EPFL Circuit Verification | 300 GiB of RAM

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Adiar

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