Random Access on Narrow Decision Diagrams in External Memory

Steffan Christ Sølvsten, Casper Moldrup Rysgaard, and Jaco van de Pol

SPIN 2024

Adiar

I/O-efficient Decision Diagrams

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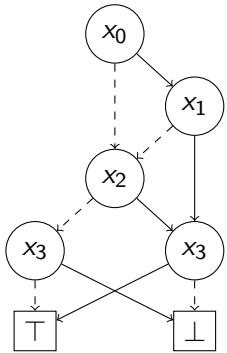
github.com/ssoelvsten/adiar

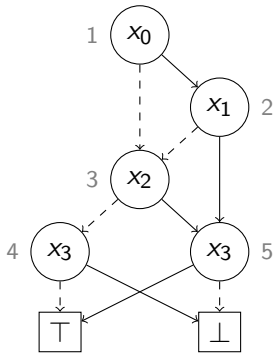
Features

Optimisations

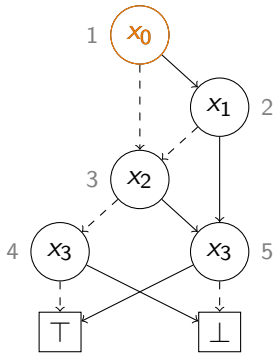
Features

Optimisations

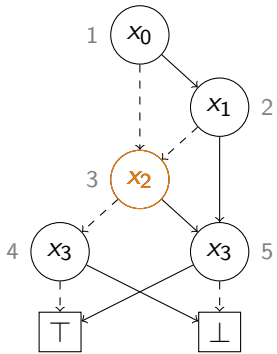

x_0
x_1
x_2
x_3
x_3
$\top$
$\bot$



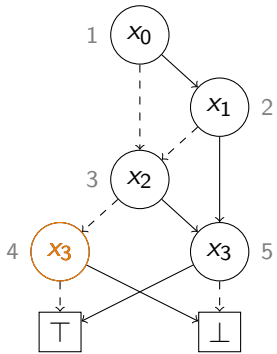
Stack



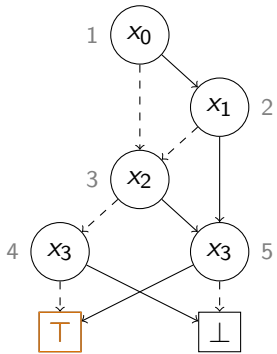
$$\frac{(\circ \to 1)}{\text{Stack}}$$



$$\frac{\begin{array}{c}(1 \to 3)\\(\circ \to 1)\end{array}}{\text{Stack}}$$

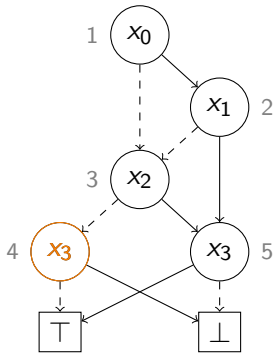


$$(3 \to 4)$$
$$(1 \to 3)$$
$$(\circ \to 1)$$
Stack

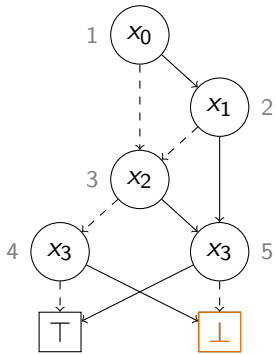


$$(4 \to \top)$$
$$(3 \to 4)$$
$$(1 \to 3)$$
$$(\circ \to 1)$$

Stack

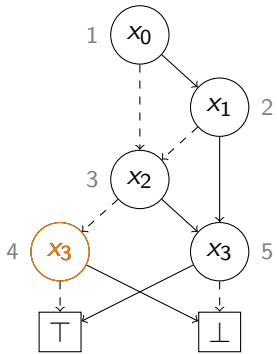


$$(3 \to 4)$$
$$(1 \to 3)$$
$$(\circ \to 1)$$
Stack

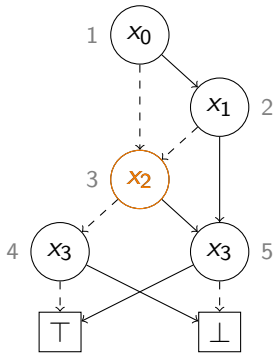


$$(4 \to \bot)$$
$$(3 \to 4)$$
$$(1 \to 3)$$
$$(\circ \to 1)$$

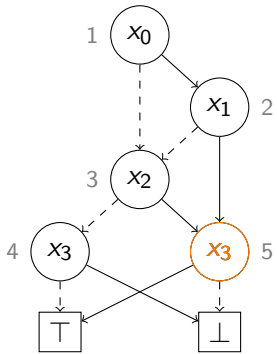
Stack



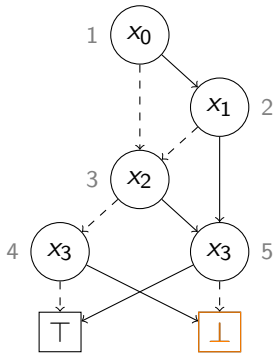
$$(3 \to 4)$$
$$(1 \to 3)$$
$$(\circ \to 1)$$
Stack



$$\frac{\begin{array}{c}(1 \to 3)\\(\circ \to 1)\end{array}}{\text{Stack}}$$

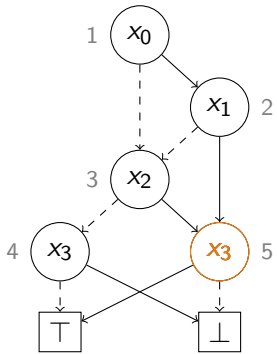


$$(3 \to 5)$$
$$(1 \to 3)$$
$$(\circ \to 1)$$
Stack

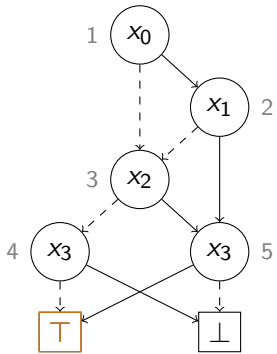


$$(5 \to \bot)$$
$$(3 \to 5)$$
$$(1 \to 3)$$
$$(\circ \to 1)$$

Stack

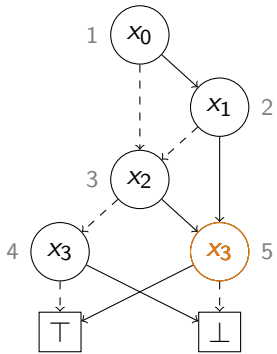


$$(3 \to 5)$$
$$(1 \to 3)$$
$$(\circ \to 1)$$
Stack

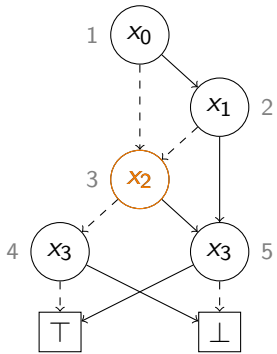


$$(5 \to \top)$$
$$(3 \to 5)$$
$$(1 \to 3)$$
$$(\circ \to 1)$$

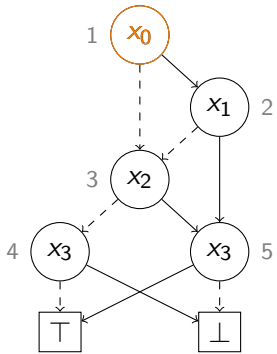
Stack



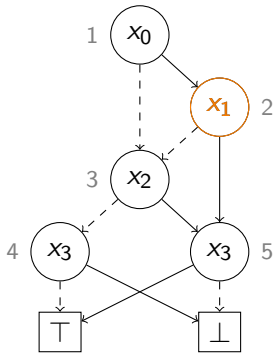
$$(3 \to 5)$$
$$(1 \to 3)$$
$$(\circ \to 1)$$
Stack



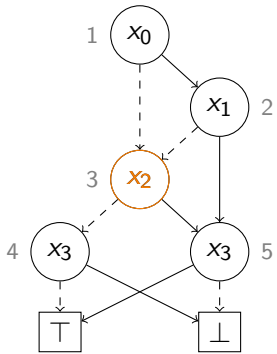
$$\frac{\begin{array}{c}(1 \to 3)\\(\circ \to 1)\end{array}}{\text{Stack}}$$



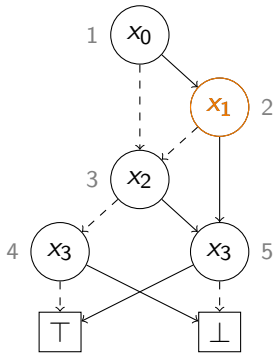
$$\frac{(\circ \to 1)}{\text{Stack}}$$



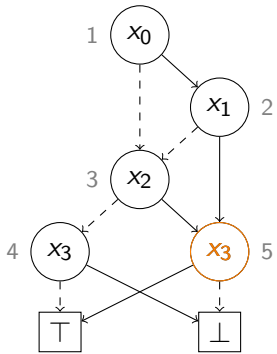
$$\frac{\begin{array}{c}(1 \to 2)\\ (\circ \to 1)\end{array}}{\text{Stack}}$$



$$(2 \to 3)$$
$$(1 \to 2)$$
$$(\circ \to 1)$$
Stack

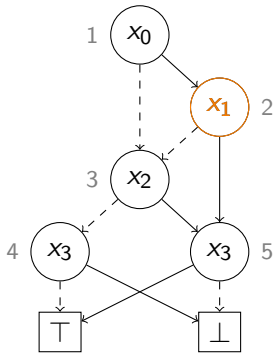


$$\frac{\begin{array}{c}(1 \to 2)\\ (\circ \to 1)\end{array}}{\text{Stack}}$$

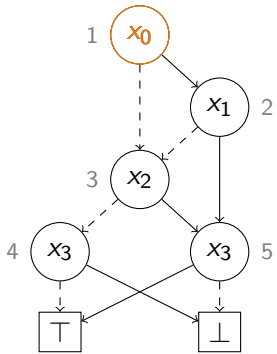


$$(2 \to 5)$$
$$(1 \to 2)$$
$$(\circ \to 1)$$

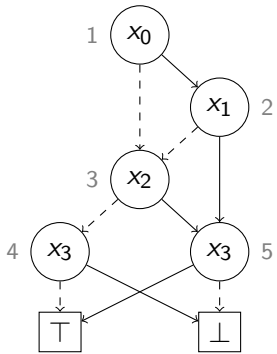
Stack



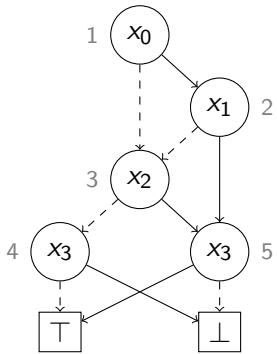
$$\frac{\begin{array}{c}(1 \to 2)\\ (\circ \to 1)\end{array}}{\text{Stack}}$$



$$\frac{(\circ \to 1)}{\text{Stack}}$$

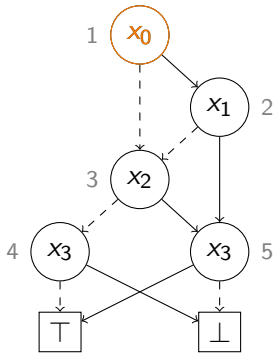


Priority Queue



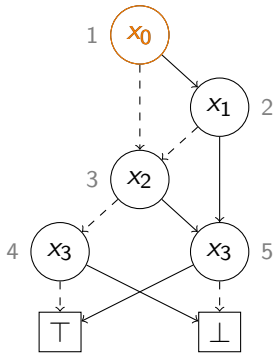
Priority Queue

$(\circ \to 1)$

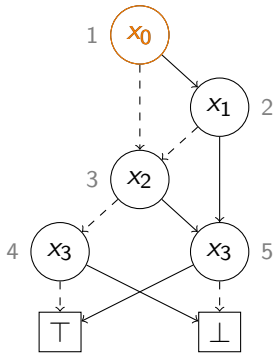


Priority Queue

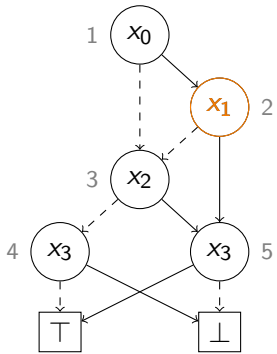
$(\circ \to 1)$



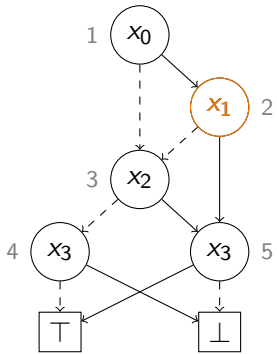
Priority Queue



Priority Queue
$(1 \to 2)$
$(1 \to 3)$

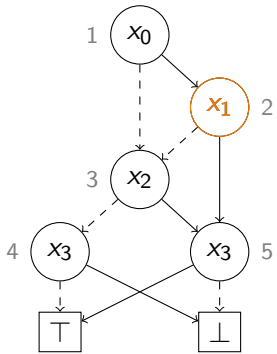


Priority Queue
$(1 \to 2)$
$(1 \to 3)$

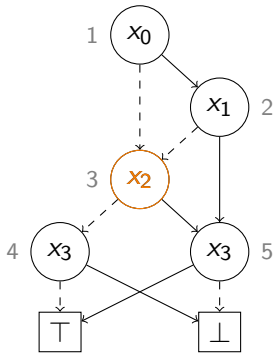


Priority Queue

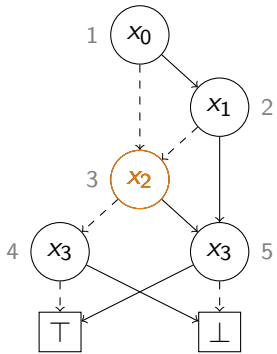
$(1 \to 3)$



Priority Queue
$(1 \to 3)$
$(2 \to 3)$
$(2 \to 5)$

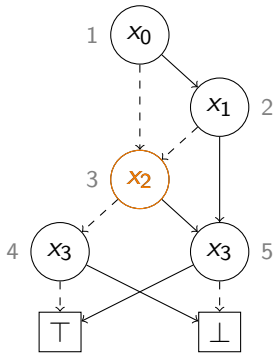


Priority Queue
$(1 \to 3)$
$(2 \to 3)$
$(2 \to 5)$

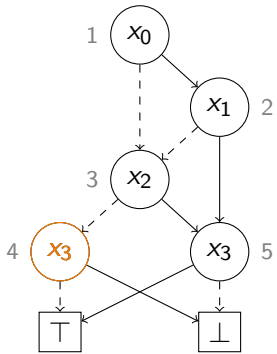


Priority Queue

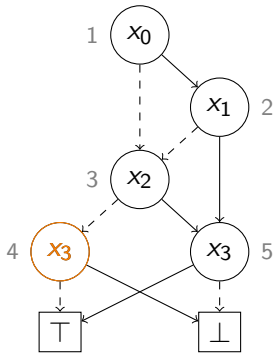
$(2 \to 5)$



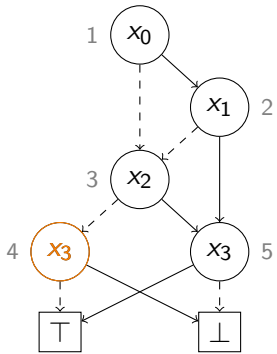
Priority Queue
$(3 \to 4)$
$(2 \to 5)$
$(3 \to 5)$



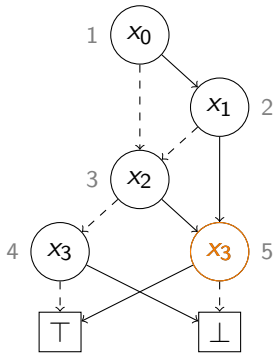
Priority Queue
$(3 \to 4)$
$(2 \to 5)$
$(3 \to 5)$



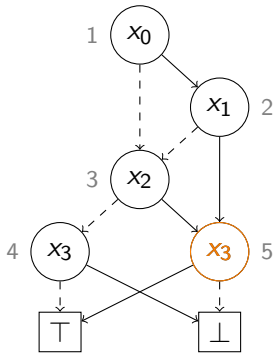
Priority Queue
$(2 \to 5)$
$(3 \to 5)$



Priority Queue
$(2 \to 5)$
$(3 \to 5)$
$(4 \to \top)$
$(4 \to \bot)$



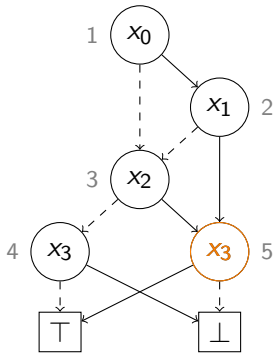
Priority Queue
$(2 \to 5)$
$(3 \to 5)$
$(4 \to \top)$
$(4 \to \bot)$



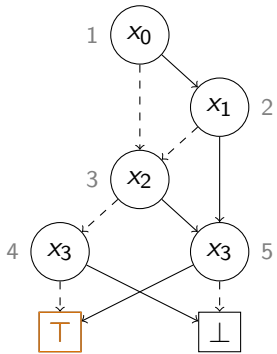
Priority Queue

$(4 \to \top)$

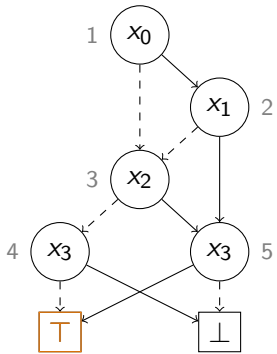
$(4 \to \bot)$



Priority Queue
$(4 \to \top)$
$(5 \to \top)$
$(4 \to \bot)$
$(5 \to \bot)$



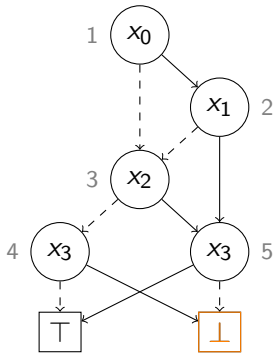
Priority Queue
$(4 \to \top)$
$(5 \to \top)$
$(4 \to \bot)$
$(5 \to \bot)$



Priority Queue

$(4 \to \bot)$

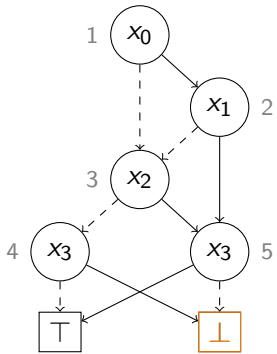
$(5 \to \bot)$



Priority Queue

$(4 \to \bot)$

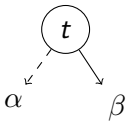
$(5 \to \bot)$



Priority Queue


S


s


t
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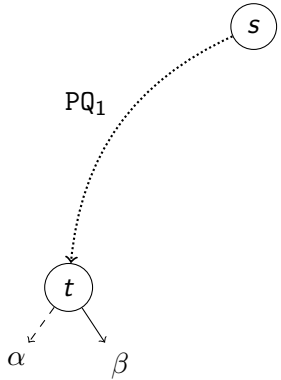
s
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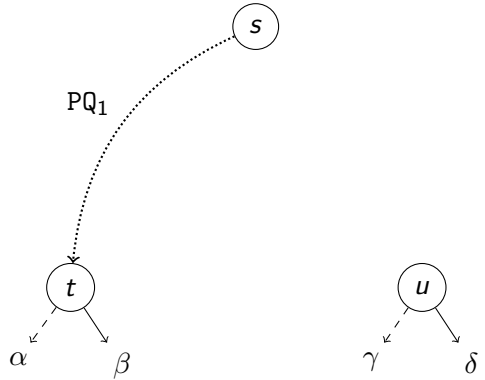
s
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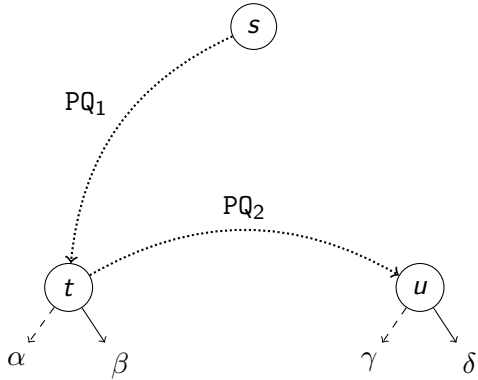
s
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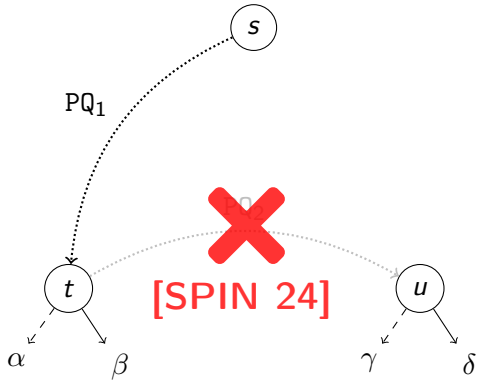
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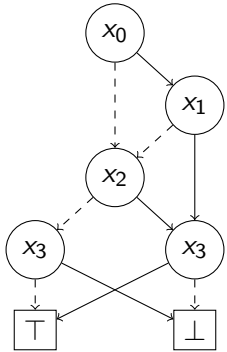
x_0
x_1
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$\top$
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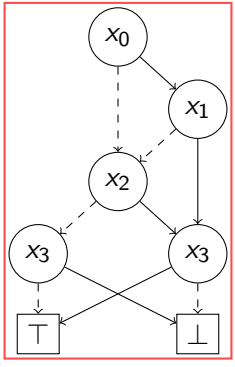
x_0
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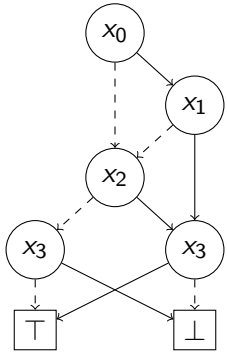
x_0
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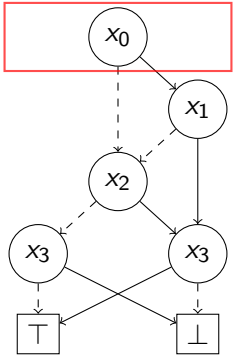
x_0
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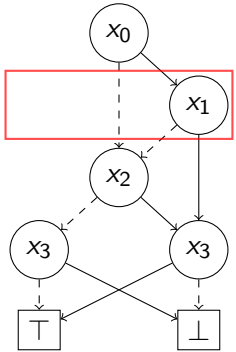
x_0
x_1
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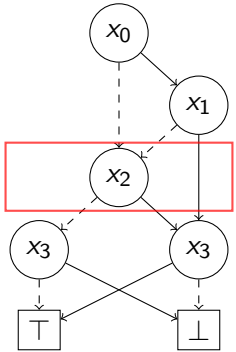
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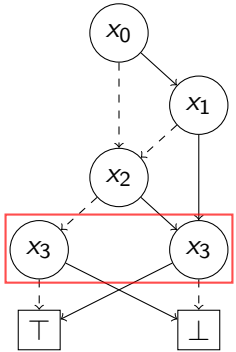
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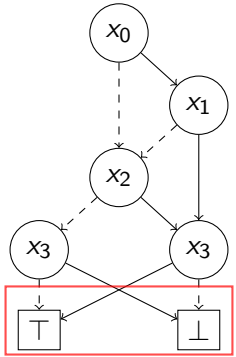
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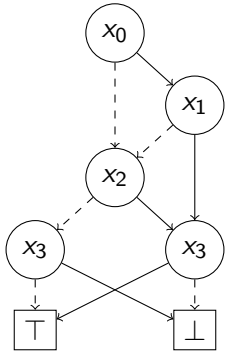
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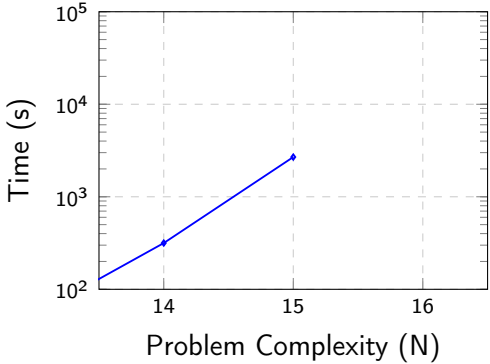
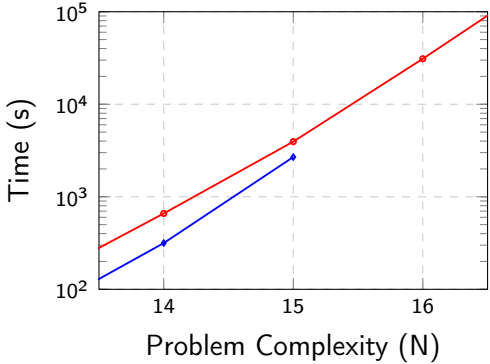


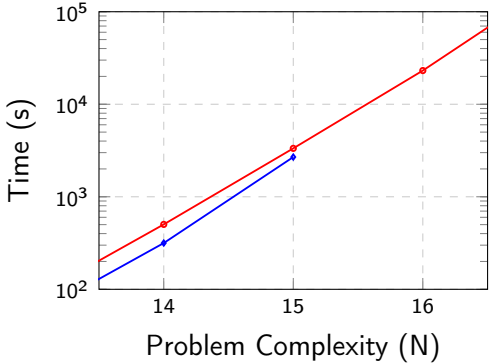
			$N = 15$
◇	CUDD v3.0	:	44.8 min

Queens | 300 GiB of RAM



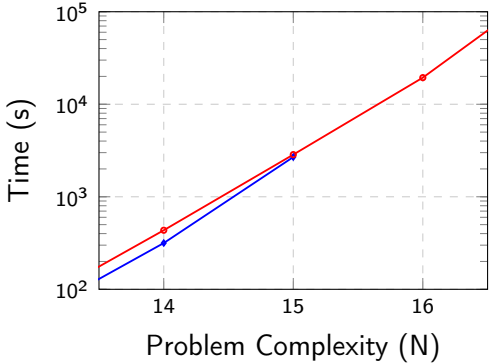
				$N = 15$
◇	CUDD	v3.0	:	44.8 min
○	Adiar	v1.0	:	66.7 min

Queens | 300 GiB of RAM



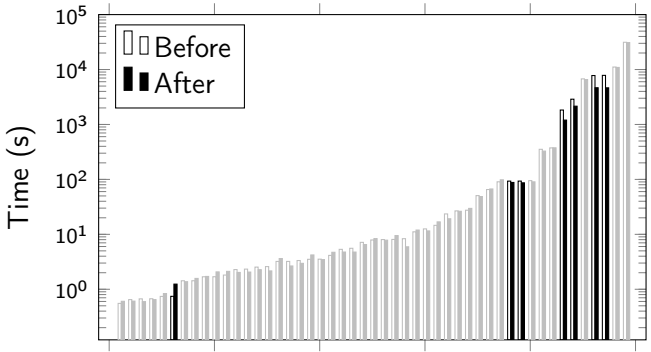
				$N = 15$
◇	CUDD	v3.0	:	44.8 min
○	Adiar	v1.0	:	66.7 min
	+ cuts		:	56.8 min

Queens | 300 GiB of RAM



				$N = 15$
◇	CUDD	v3.0	:	44.8 min
○	Adiar	v1.0	:	66.7 min
	+ cuts		:	56.8 min
	+ random access		:	47.2 min

Queens | 300 GiB of RAM



Speed Ups

6 (+26.9%)

Slowdowns

1 (-66.8%)

EPFL Circuit Verification | 300 GiB of RAM

Steffan Christ Sølvsten

soelvsten@cs.au.dk

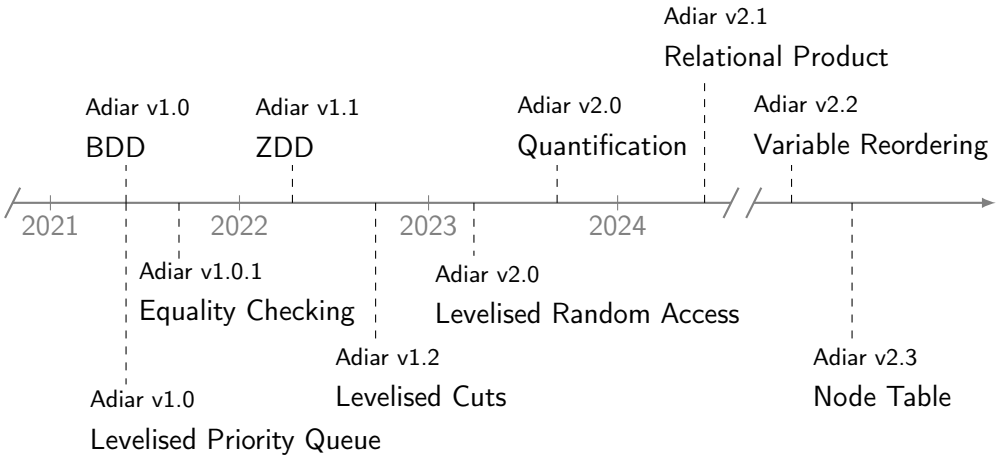
ssoelvsten.github.io

Adiar

github.com/ssoelvsten/adiar

ssoelvsten.github.io/adiar

AARHUS
UNIVERSITY


Adiar v2.1
Relational Product
Adiar v1.0
BDD
Adiar v1.1
ZDD
Adiar v2.0
Quantification
Adiar v2.2
Variable Reordering
2021
2022
2023
2024
Adiar v1.0.1
Equality Checking
Adiar v2.0
Levelised Random Access
Adiar v1.2
Levelised Cuts
Adiar v2.3
Node Table
Adiar v1.0
Levelised Priority Queue