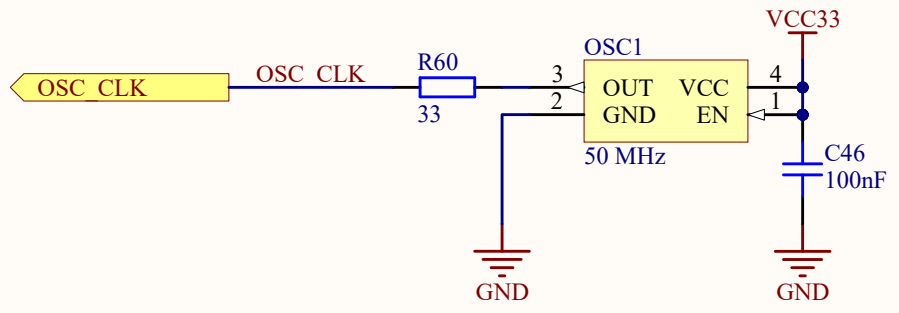
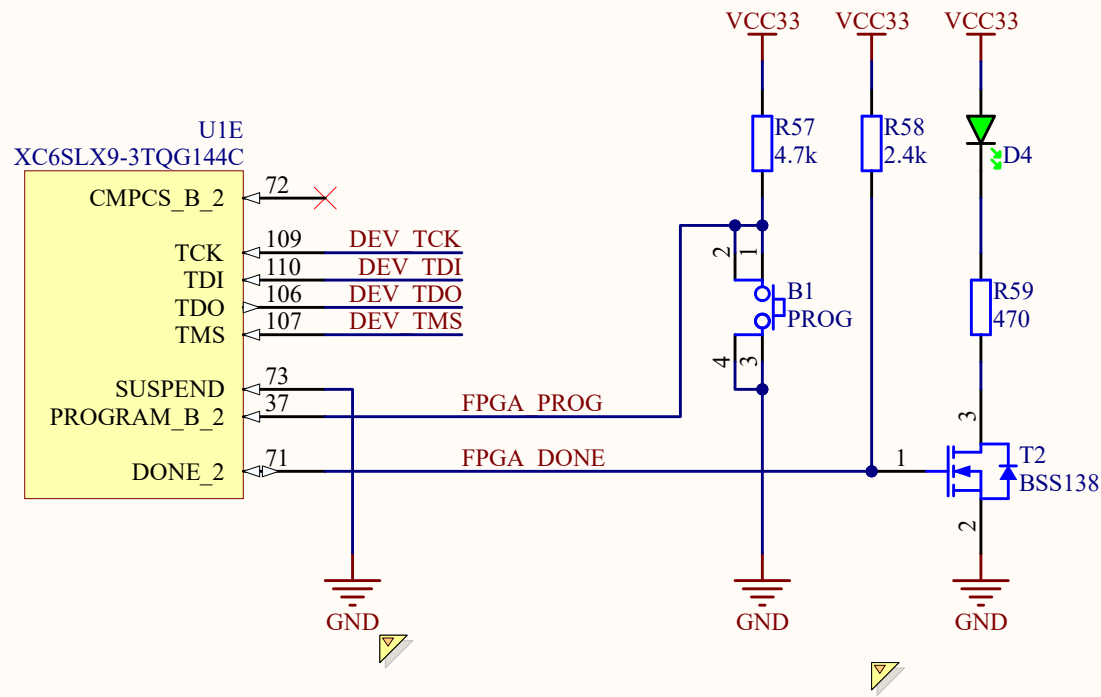
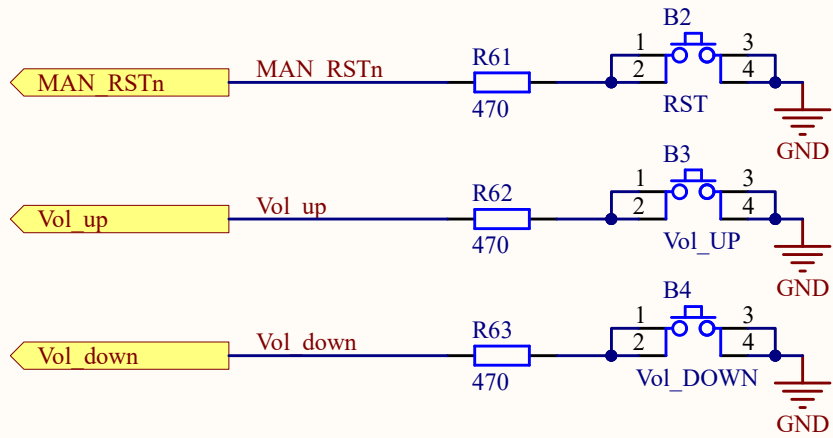
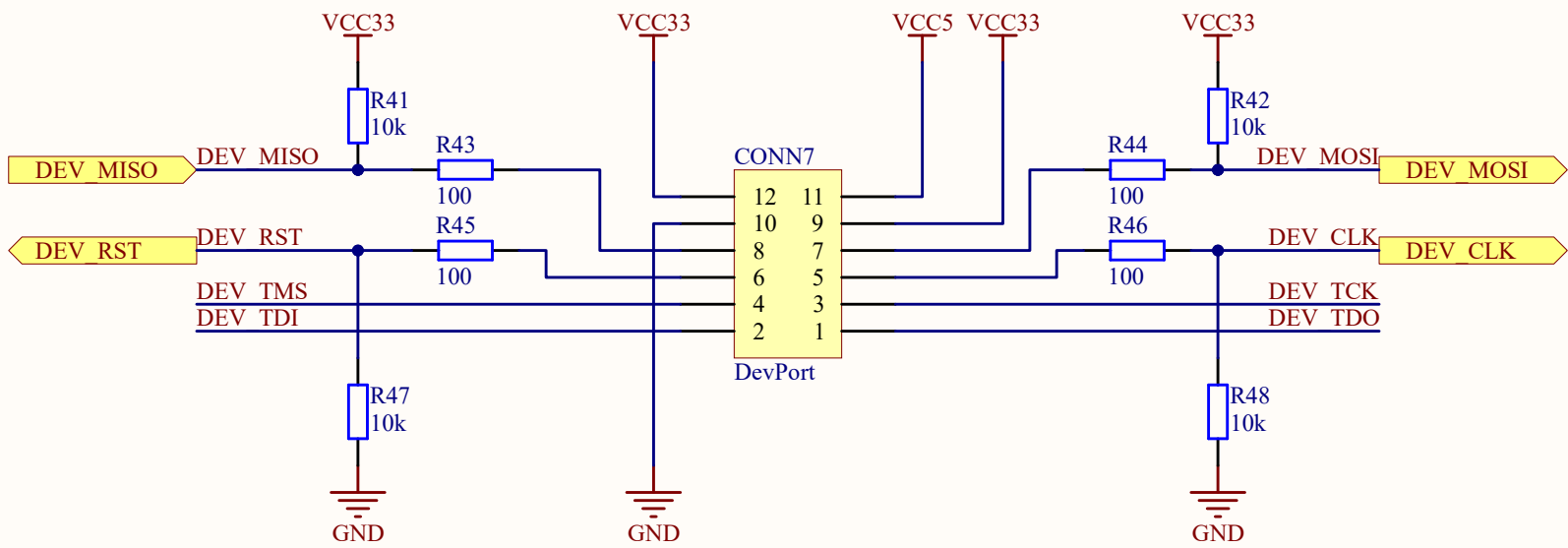


# FPGA-JTAG



# Developement port



Title			FPGA OSC, and JTAG	
Size	Number		Revision	
A4				
Date:	11. 15. 2023		Sheet of	
File:	C:\Users\...\FPGA-OSC-JTAG.SchDoc		Drawn By: Dominik Varga	