

## CONN2

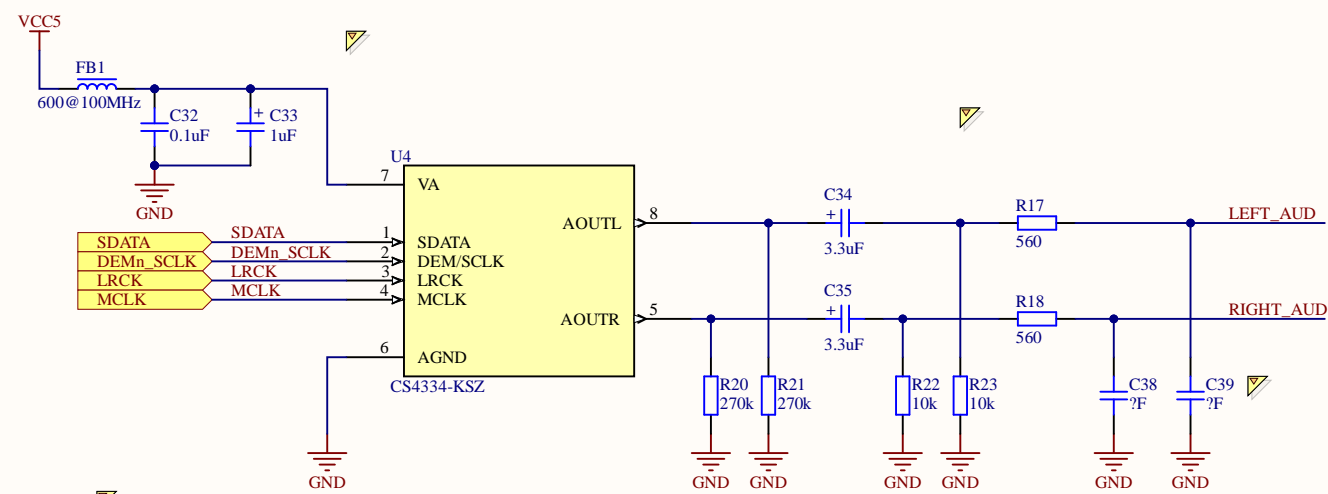


felhúzó ellenállások és soros 100 Ohmosok chekolása

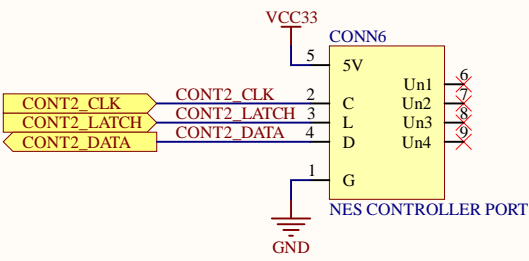
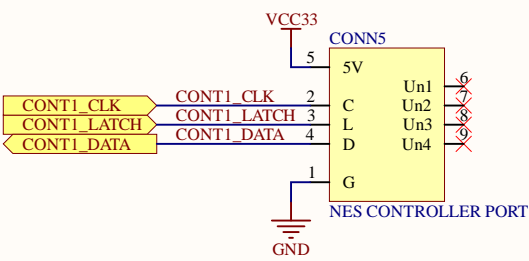
33 OHm minnél közelebb az fpgahoz

Title <b>Connectors: HMI and MicroSD card</b>		
Size A4	Number	Revision
Date: 5. 18. 2023	Sheet of	Drawn By: Dominik Varga
File: C:\Users\...Connectors-HDMI-MicroSD		

DAC, Amplifier and 3.5mm Jack



Game controller ports

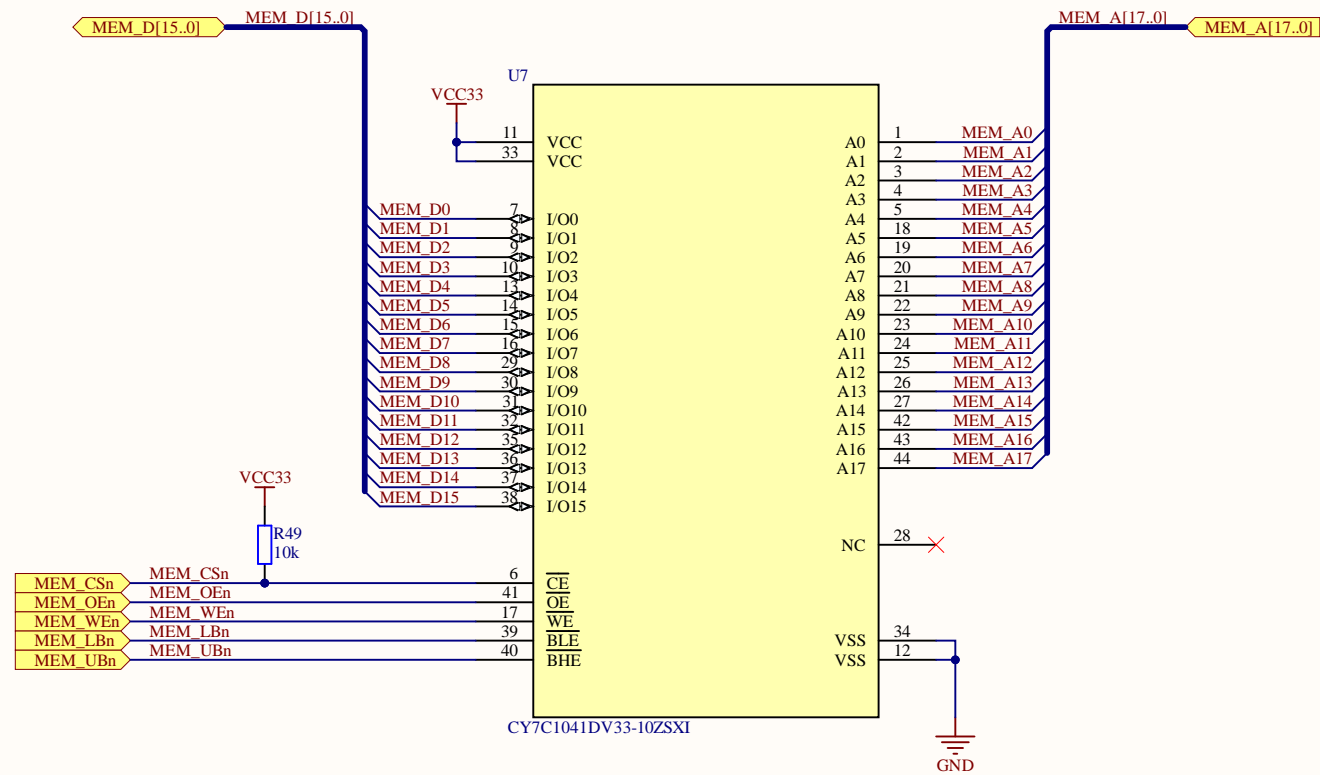


Δ DAC + JACK + Amplifier  
fázis fordító műveleti erősítő összegző erősítő

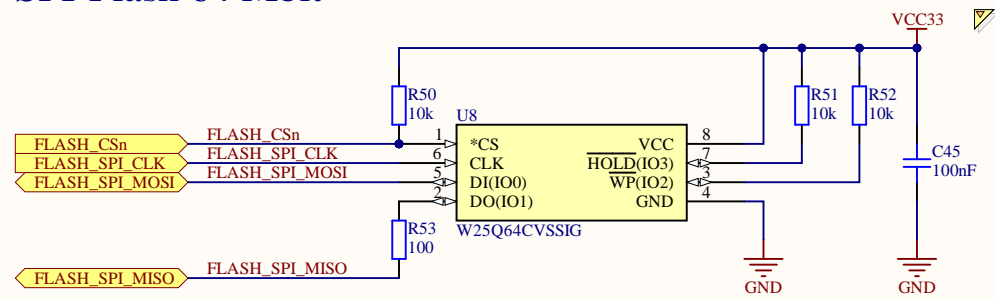
Gain<sub>LINEAR</sub> = -(R<sub>FEEED</sub>/R<sub>IN</sub>)  
Gain<sub>B</sub> = 20 Log(R<sub>FEEED</sub>/R<sub>IN</sub>)

Title		
Connectors: DAC and Controller ports		
Size	Number	Revision
A4		
Date: 5. 18. 2023		Sheet of
File: C:\Users\...\Connectors-DAC-Controllers		Drawn By: Dominik Varga

SRAM 512 kbyte

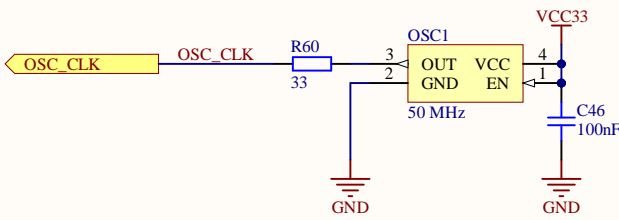
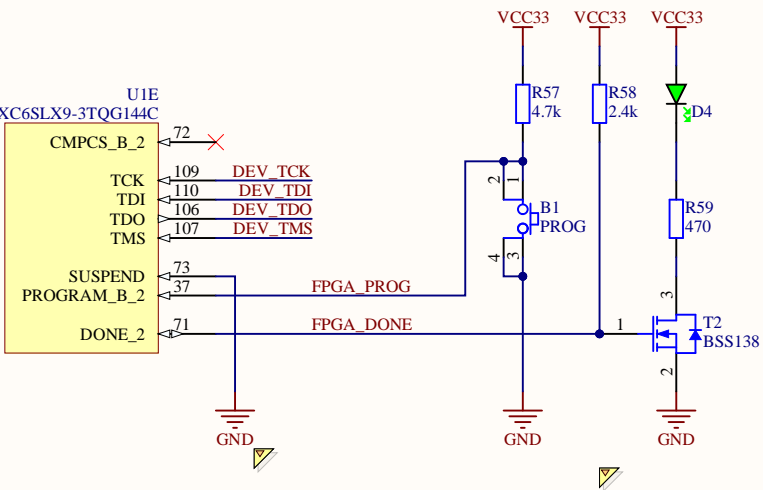


SPI-Flash 64 Mbit

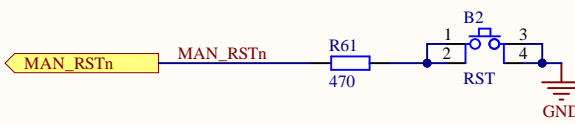
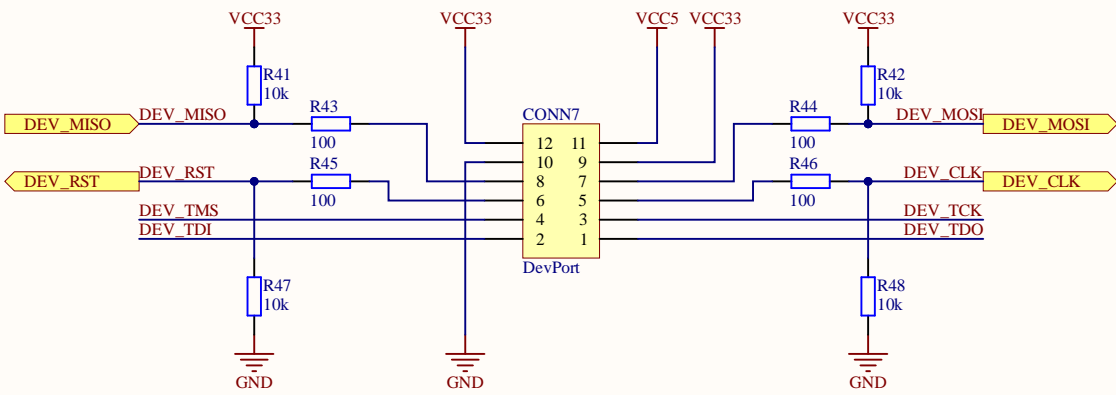


Title		
SRAM and SPI-Flash		
Size	Number	Revision
A4		
Date:	5. 18. 2023	Sheet of
File:	C:\Users\...SRAM-SPI-FLASH.SchDoc	Drawn By: Dominik Varga

FPGA-JTAG



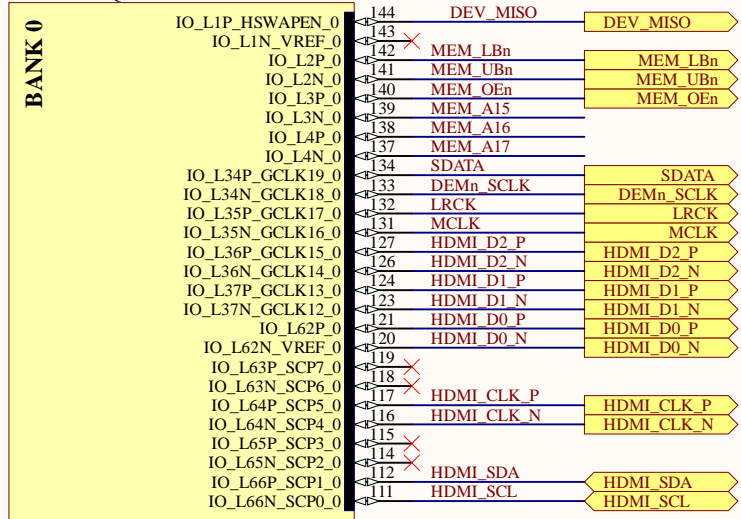
Development port



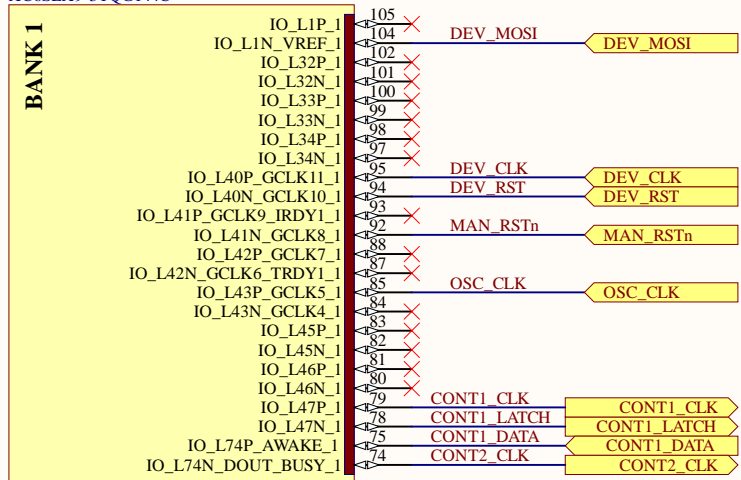
FPGA OSC, and JTAG

Title		
Size	Number	Revision
A4		
Date:	5. 18. 2023	Sheet of
File:	C:\Users\...\FPGA-OSC-JTAG.SchDoc	Drawn By: Dominik Varga

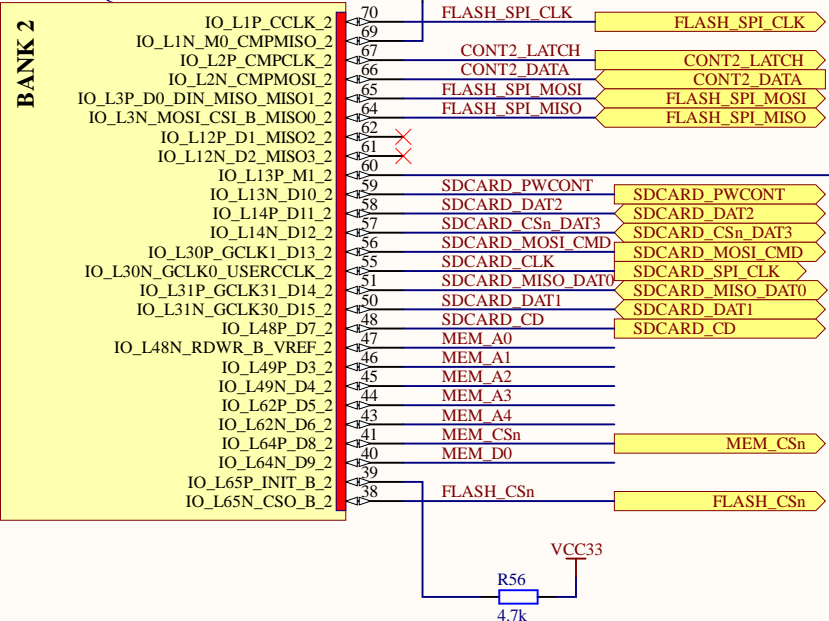
U1A  
XC6SLX9-3TQG144C



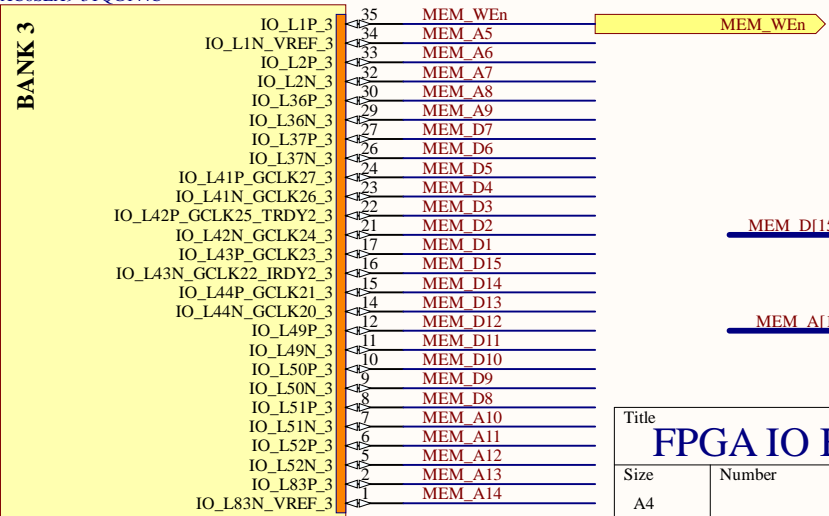
U1B  
XC6SLX9-3TQG144C



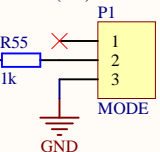
U1C  
XC6SLX9-3TQG144C



U1D  
XC6SLX9-3TQG144C



FPGA config mode:  
- JTAG (JP1: 1-2)  
- SPI (2-3)



Title		
FPGA IO Banks		
Size	Number	Revision
A4		
Date:	5. 18. 2023	Sheet of
File:	C:\Users\...\\FPGA-IO-BANKS.SchDoc	Drawn By: Dominik Varga