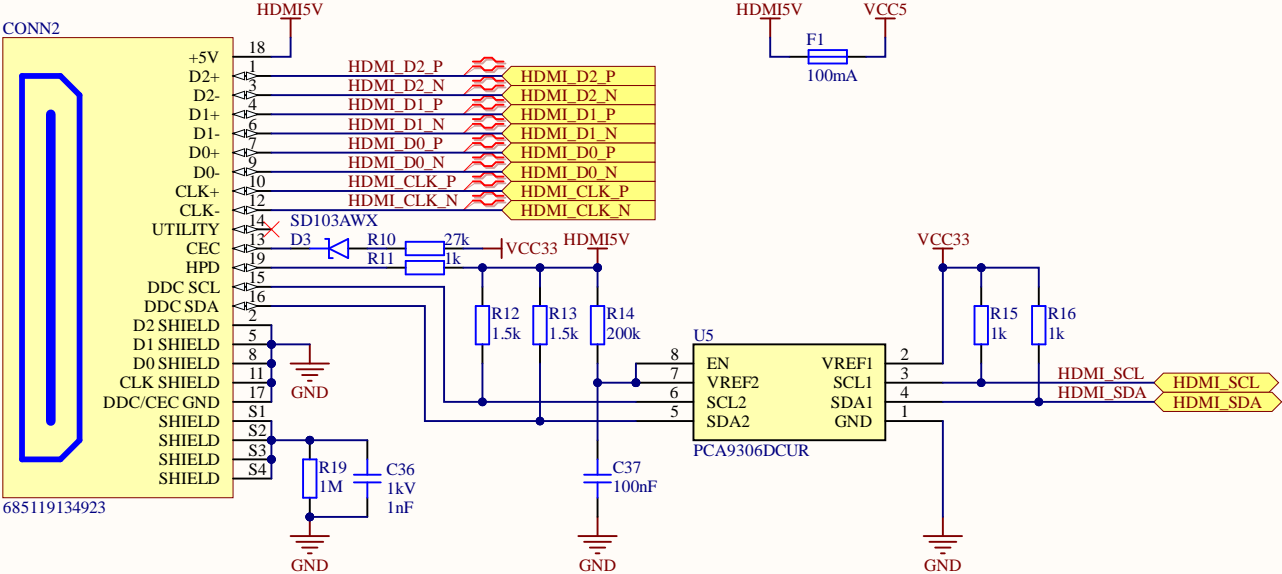
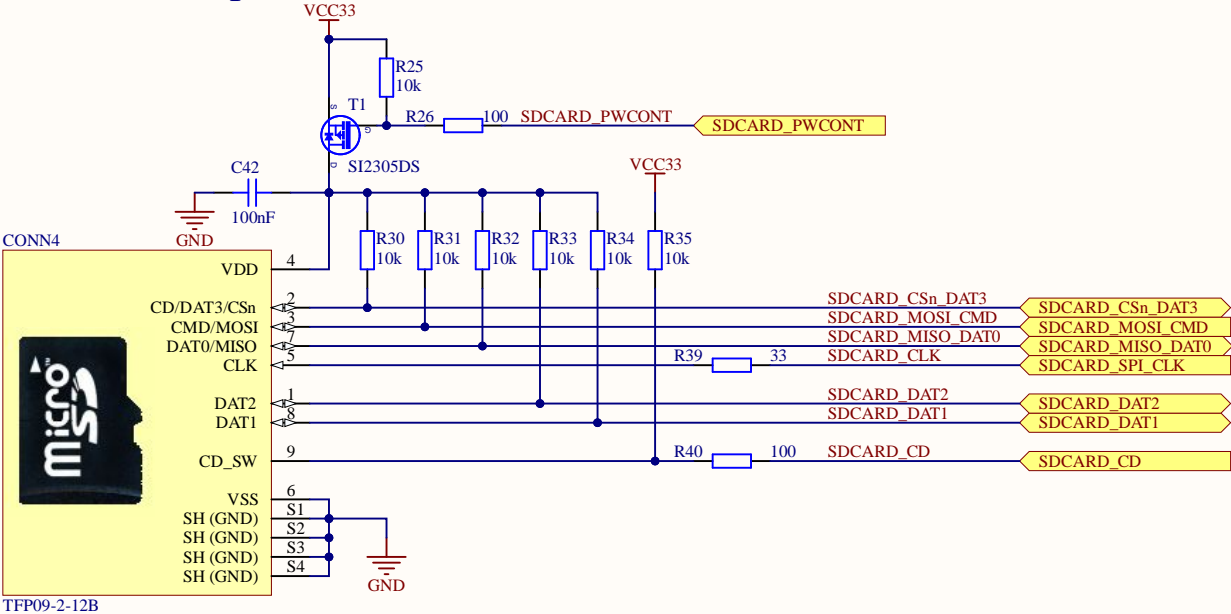


Title		
Power Supply		
Size	Number	Revision
A4		
Date:	5.25.2023	Sheet of
File:	C:\Users\...\Power-Supply.SchDoc	Drawn By: Dominik Varga

HDMI port

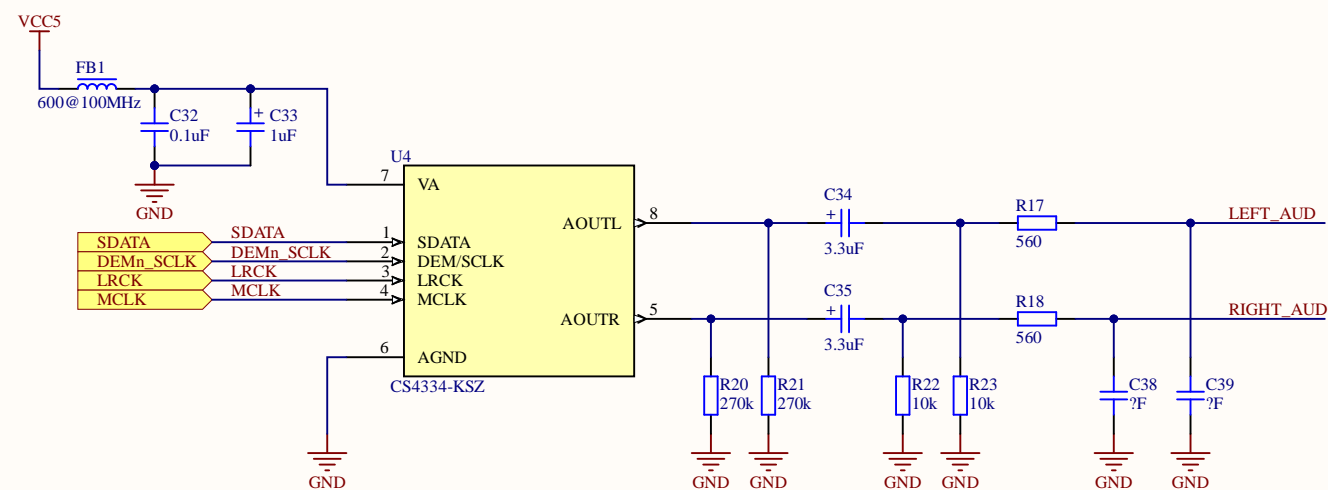


MicroSD card port

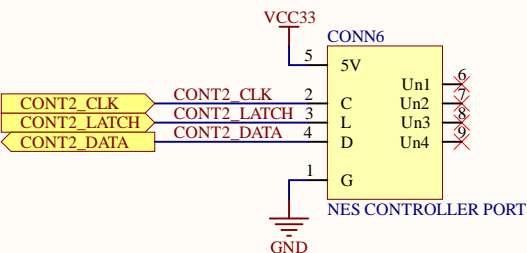
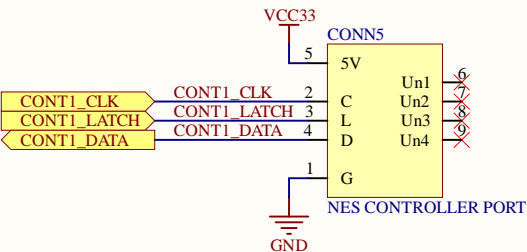


Title		
Connectors: HMI and MicroSD card		
Size	Number	Revision
A4		
Date:	5.25.2023	Sheet of
File:	C:\Users\...\Connectors-HDMI-MicroSD_SchDoc	Drawn By: Dominik Varga

DAC, Amplifier and 3.5mm Jack

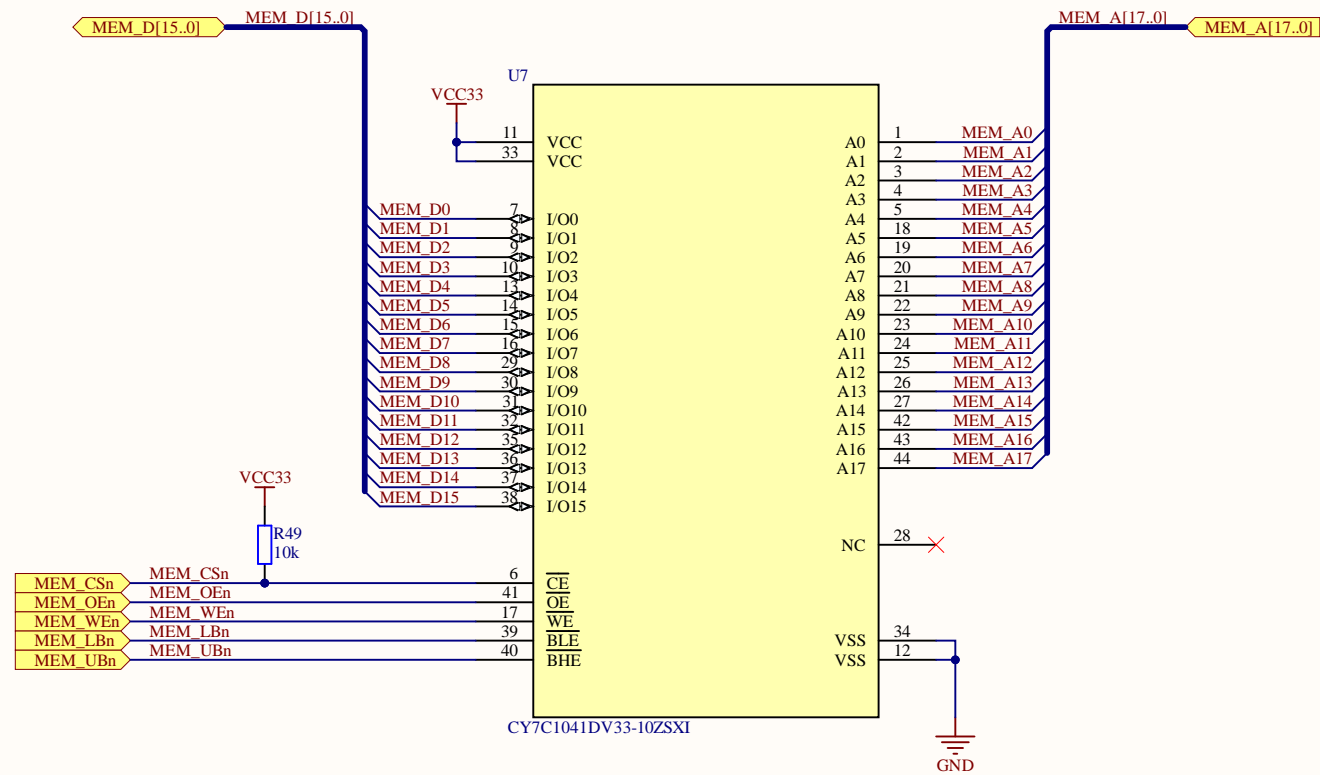


Game controller ports

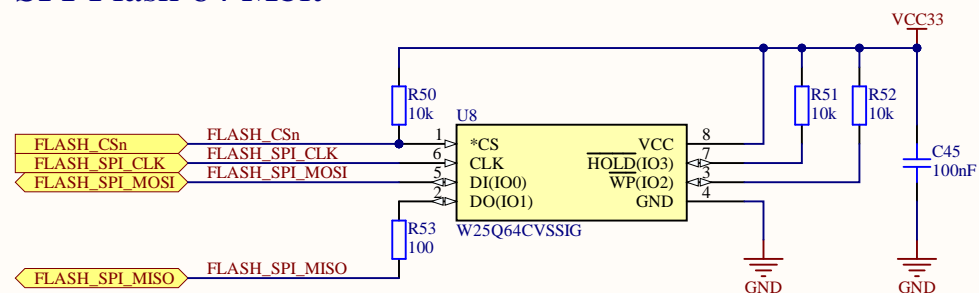


Title		
Connectors: DAC and Controller ports		
Size	Number	Revision
A4		
Date:	5.25.2023	Sheet of
File:	C:\Users\...\Connectors-DAC-Controllers	Drawn By: Dominik Varga

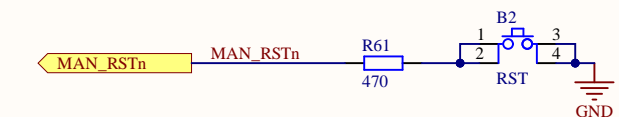
SRAM 512 kbyte



SPI-Flash 64 Mbit

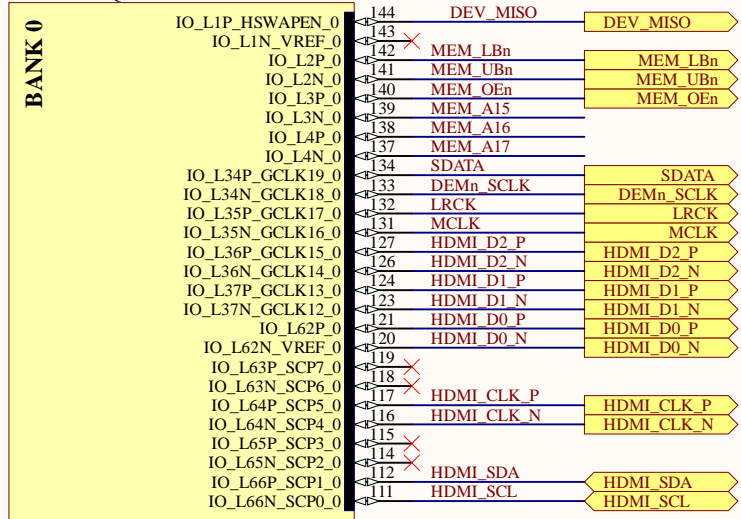


Title		
SRAM and SPI-Flash		
Size	Number	Revision
A4		
Date:	5.25.2023	Sheet of
File:	C:\Users\...\SRAM-SPI-FLASH.SchDoc	Drawn By: Dominik Varga

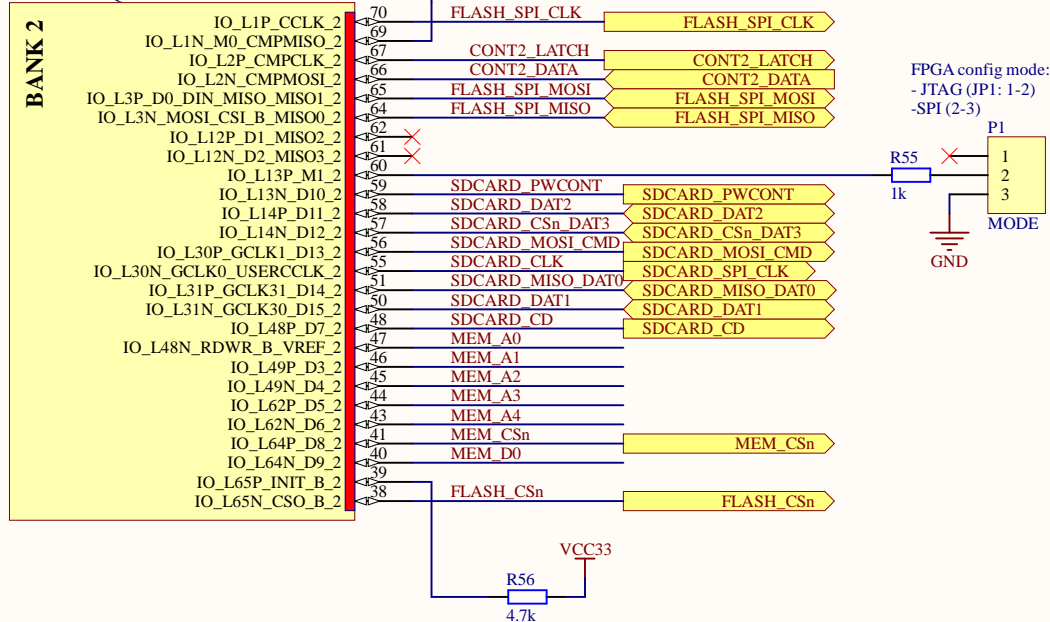


Title		
FPGA OSC, and JTAG		
Size	Number	Revision
A4		
Date:	5. 25. 2023	Sheet of
File:	C:\Users\...\FPGA-OSC-JTAG.SchDoc	Drawn By: Dominik Varga

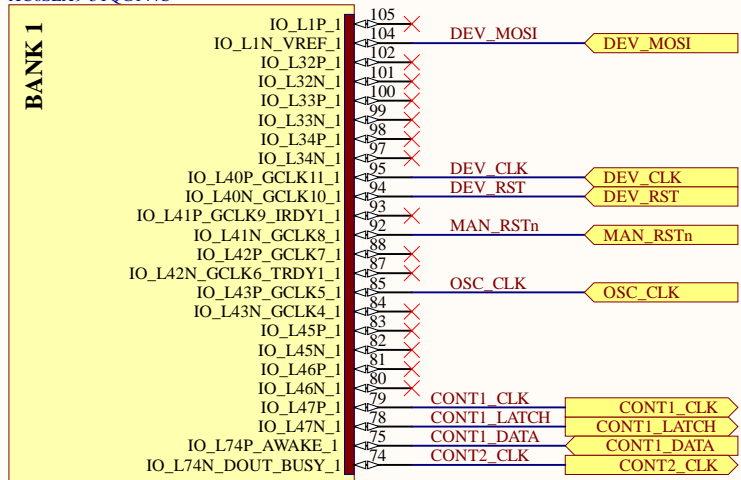
U1A
XC6SLX9-3TQG144C



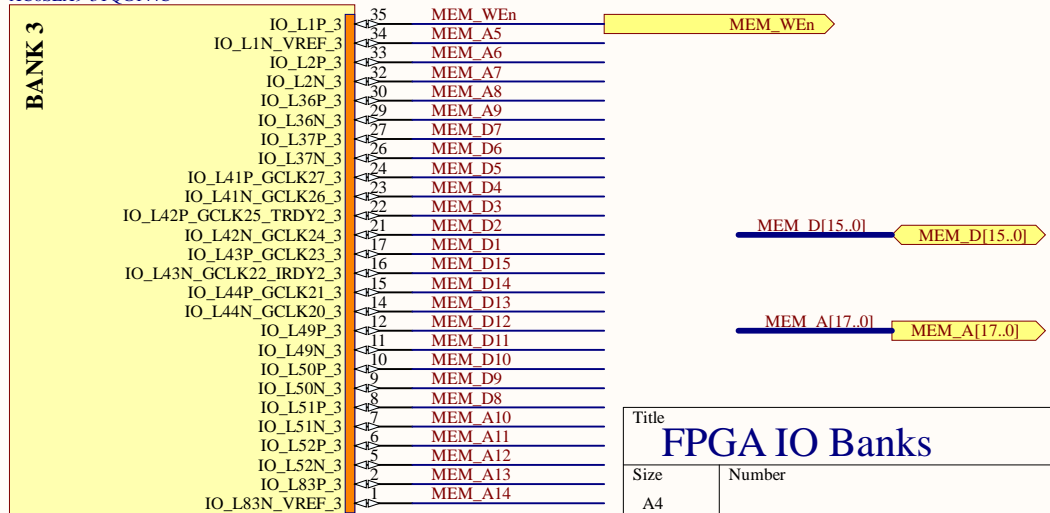
U1C
XC6SLX9-3TQG144C



U1B
XC6SLX9-3TQG144C



U1D
XC6SLX9-3TQG144C



Title		
FPGA IO Banks		
Size	Number	Revision
A4		
Date:	5.25.2023	Sheet of
File:	C:\Users\...\FPGA-IO-BANKS.SchDoc	Drawn By: Dominik Varga