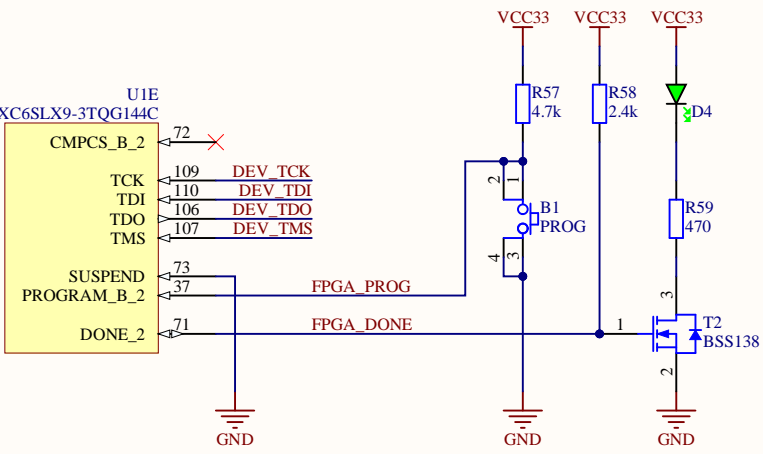
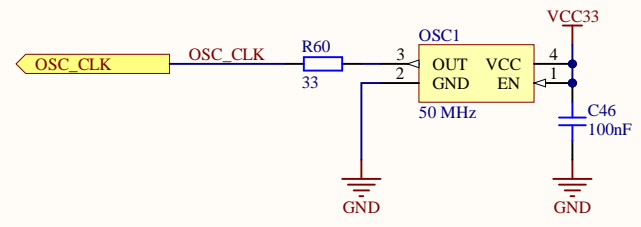


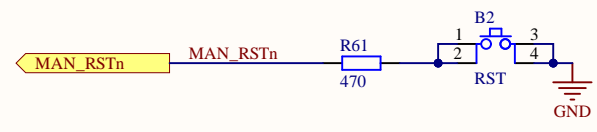
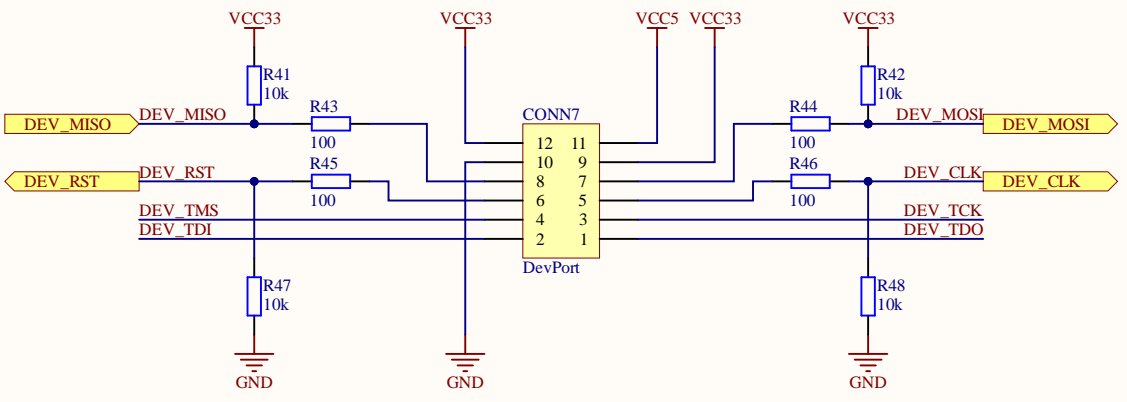
FPGA-JTAG



OSC, and JTAG



Development port



Title		
FPGA OSC, and JTAG		
Size	Number	Revision
A4		
Date:	5. 25. 2023	Sheet of
File:	C:\Users\...\FPGA-OSC-JTAG.SchDoc	Drawn By: Dominik Varga