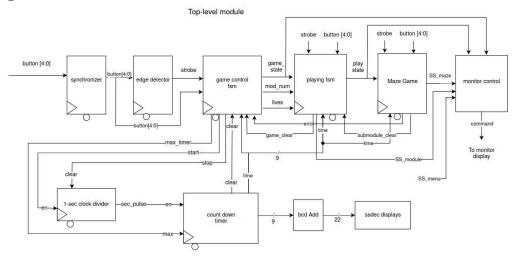
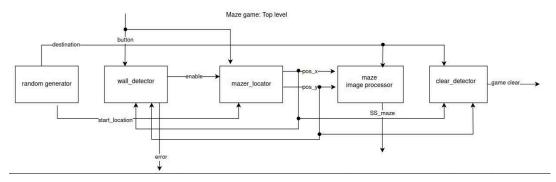
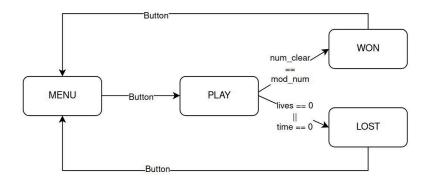
First Design Review



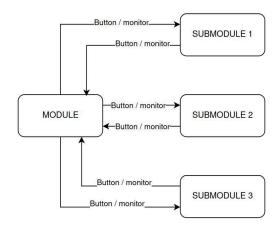
Display 1.1: Top Level Module (Only Maze Game and the menu states)



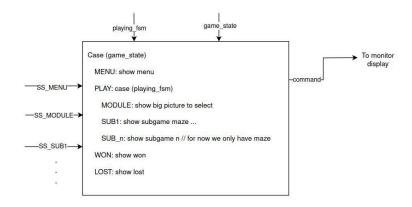
Display 1.2: Maze Game (Focussed specifically on Maze Game)



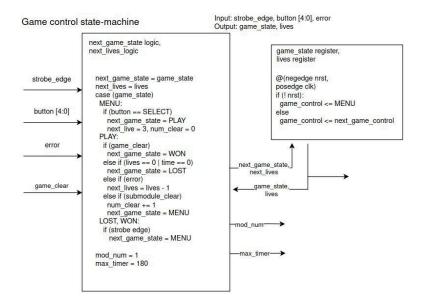
Display 1.3: Game Control FSM (The game states)



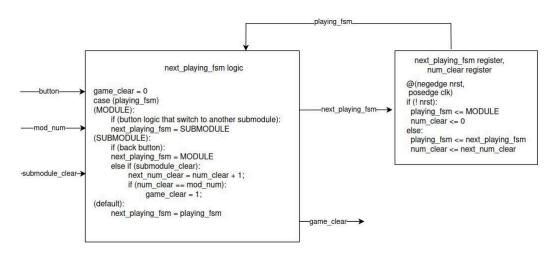
Display 1.4: Playing FSM (The Game Playing Menu)



Display 1.5: Monitor Control (SPI Display)

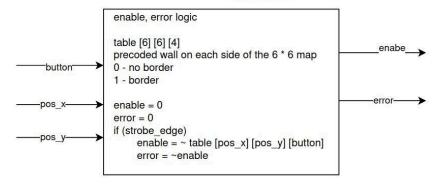


Display 1.6: Game Control FSM



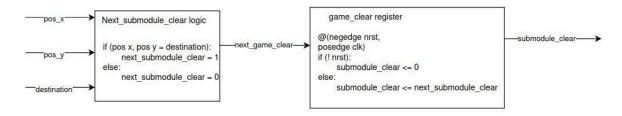
Display 1.7: Playing FSM

Wall detector

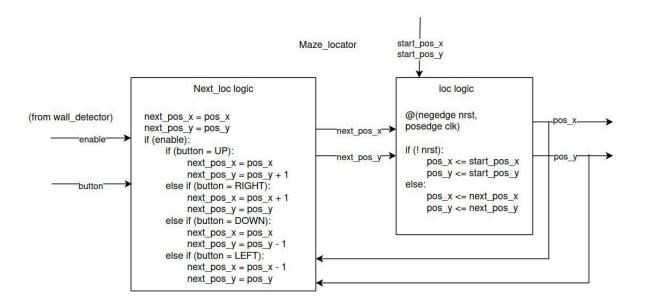


Display 1.8: Maze Game (Wall Detector)

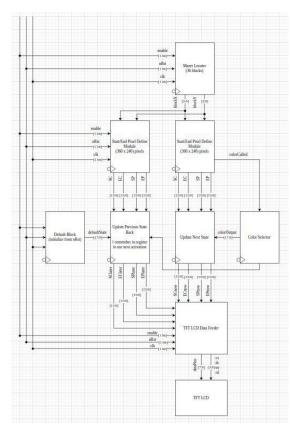
submodule_clear_detector



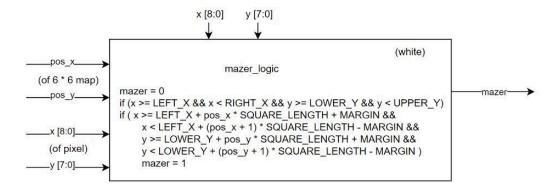
Display 1.9: Puzzle Clear Logic



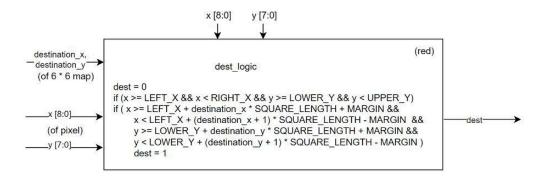
Display 1.10: Maze Locator



Display 1.11 : Maze Image Processor (Overall)

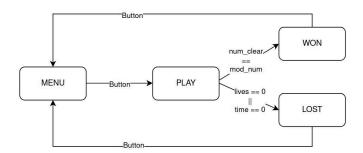


Display 1.12: Maze Image Processor (Mazer Logic Focused)

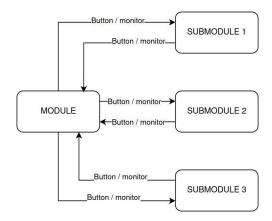


Display 1.13: Maze Image Processor Destination Logic

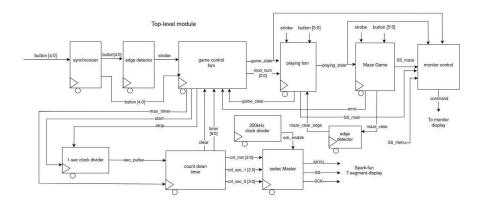
Intermediate Design Review



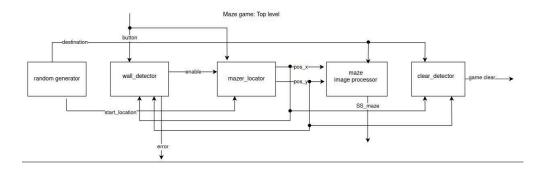
Display 2.1 Game Control FSM (Unchanged)



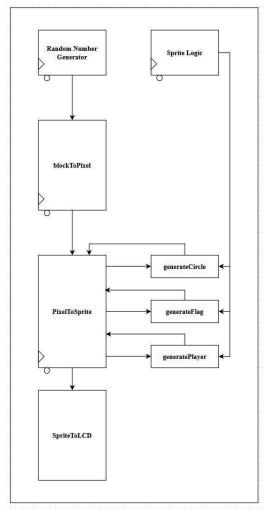
Display 2.2 Playing FSM (Unchanged)



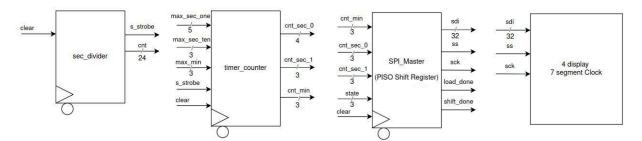
Display 2.3: Unfinished Top Level Diagram (Needed the Monitor Control connected)



Display 2.4: Rtl Diagram (Unchanged)



Display 2.5: Extended Top RTL (Attempt for Parallel Display)



Display 2.6: Timer RTL designs (Unfinished and not connected)

Final Product Rtls

