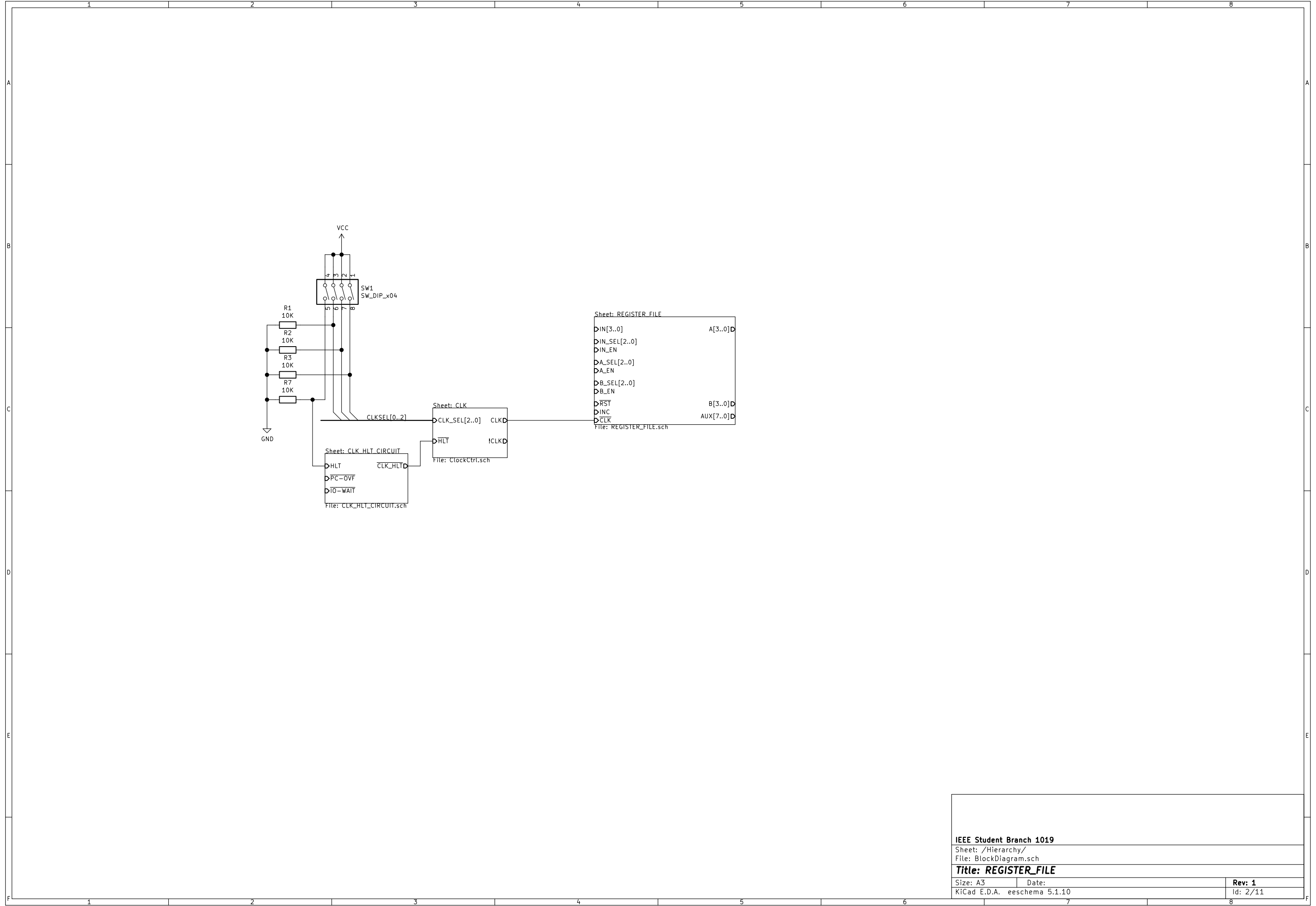
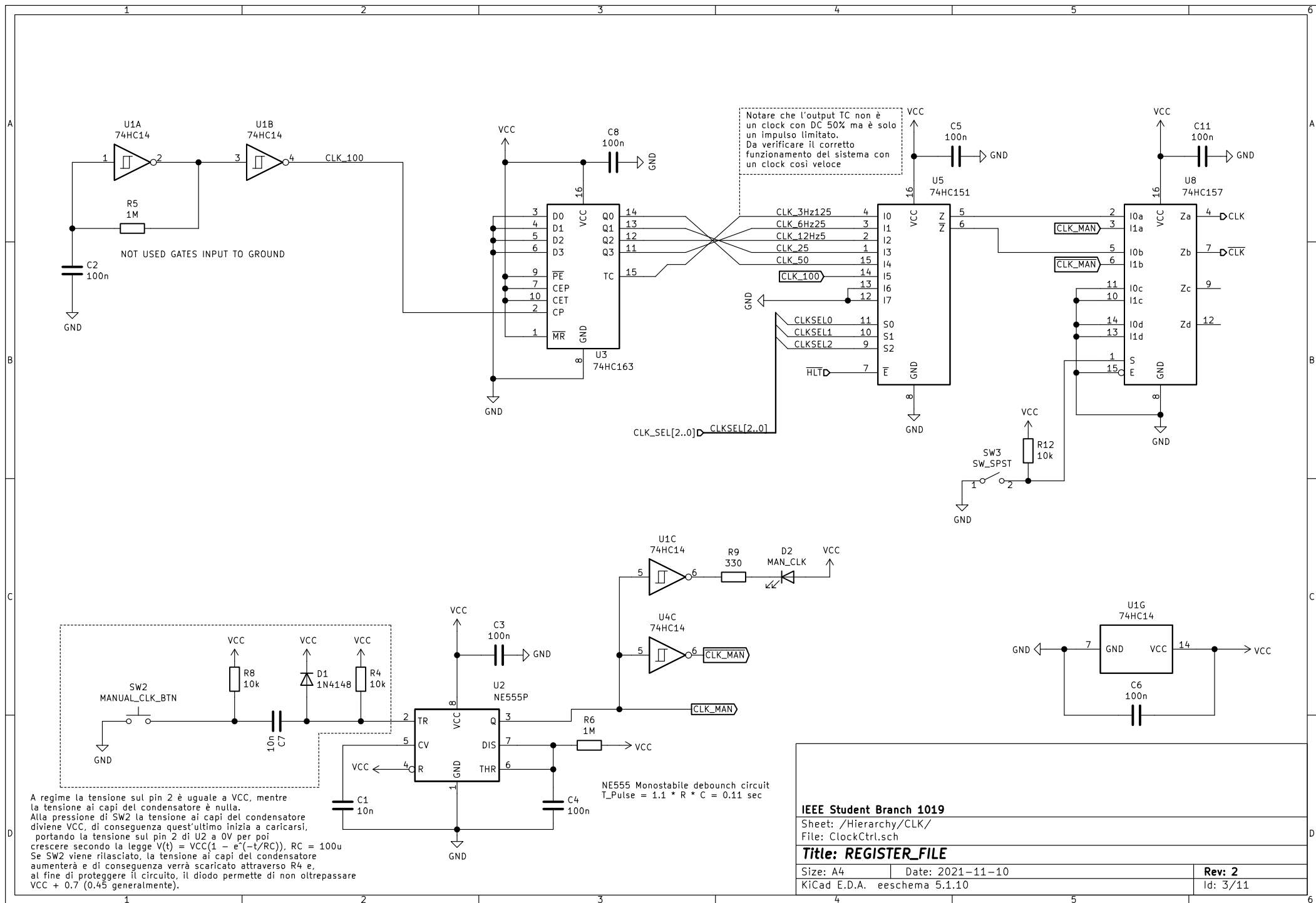


Progetto TTL

IEEE Student Branch 1019, University of Brescia

Rev.	Date	Revision Description
0	10/11	Creazione progetto, Prima progettazione del generatore di clock e clk multiplexer
1	24/11	Aggiunta contatore per divisore frequenza. modifica input pushbutton in CLK_GEN. Creazione circuito HLT, Aggiunta bit a CLK_SELECT. Aggiunta registri
2	29/11	Fix convenzione dei nomi, register selection and I/O logic





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Sheet: /Hierarchy/CLK/

File: ClockCtrl.sch

Title: REGISTER_FILE

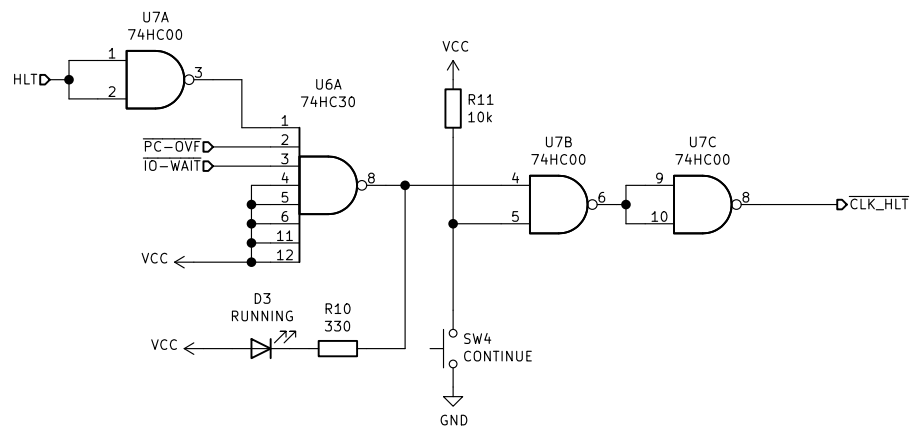
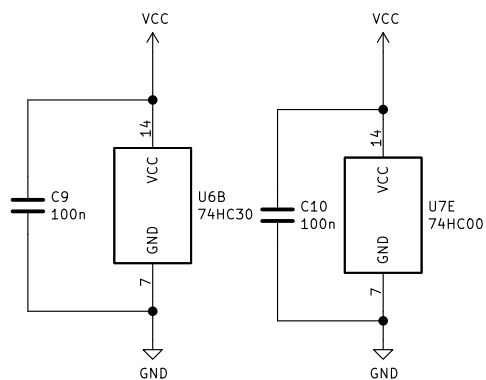
Size: A4

Date: 2021-11-10

KiCad E.D.A. eeschema 5.1.10

Rev: 2

Id: 3/11



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Sheet: /Hierarchy/CLK_HLT_CIRCUIT/
File: CLK_HLT_CIRCUIT.sch

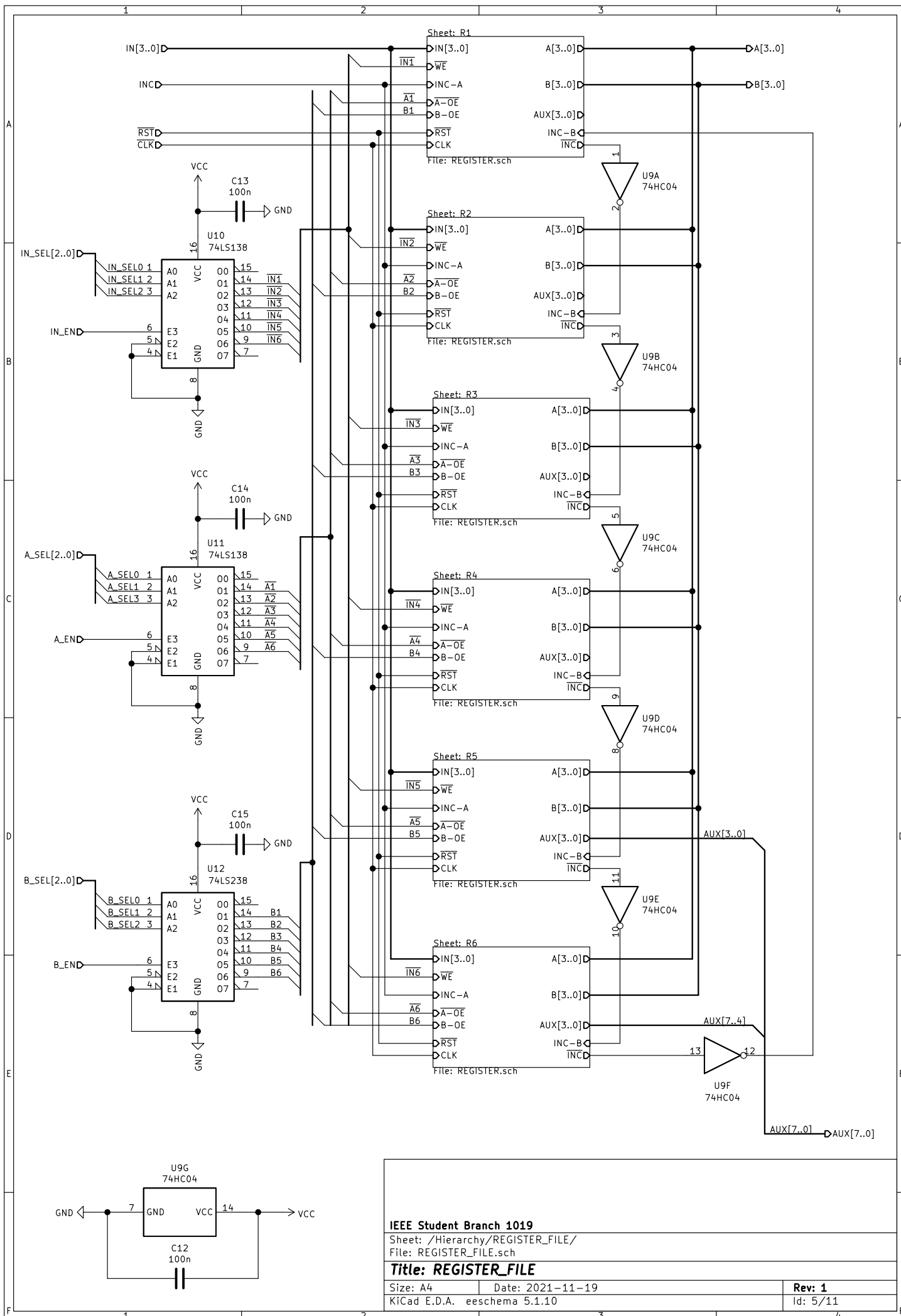
Title: REGISTER_FILE

Size: A4 Date: 2021-11-18

KiCad E.D.A. eeschema 5.1.10

Rev: 1

Id: 4/11



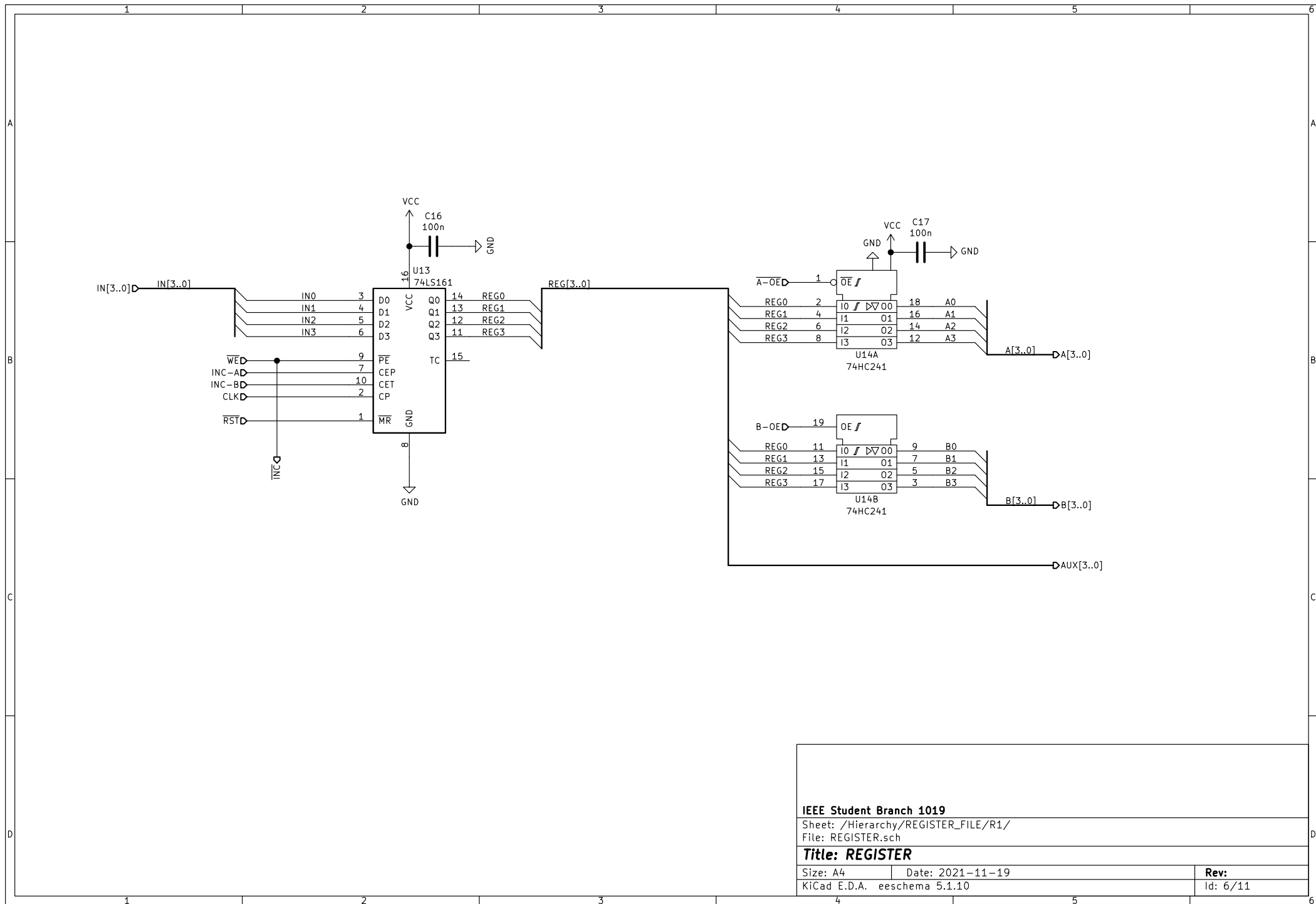
IEEE Student Branch 1019

Sheet: /Hierarchy/REGISTER_FILE/
File: REGISTER_FILE.sch

Title: REGISTER_FILE

Size: A4 Date: 2021-11-19
KiCad E.D.A. eschema 5.1.10

Rev: 1
Id: 5/11



IEEE Student Branch 1019

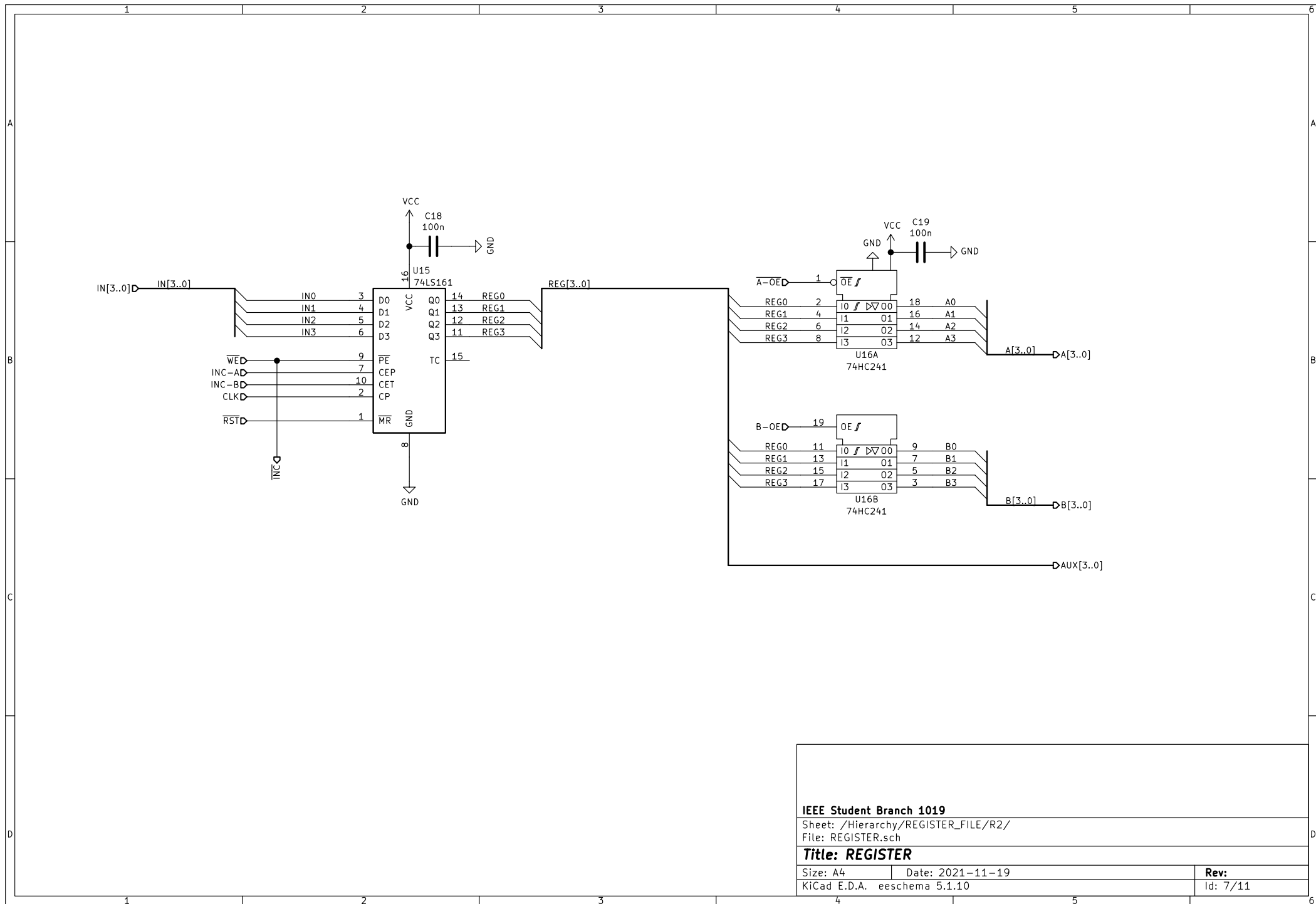
Sheet: /Hierarchy/REGISTER_FILE/R1/
File: REGISTER.sch

Title: REGISTER

Size: A4 Date: 2021-11-19

KiCad E.D.A. eeschema 5.1.10

Rev:
Id: 6/11



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Sheet: /Hierarchy/REGISTER_FILE/R2/
File: REGISTER.sch

Title: REGISTER

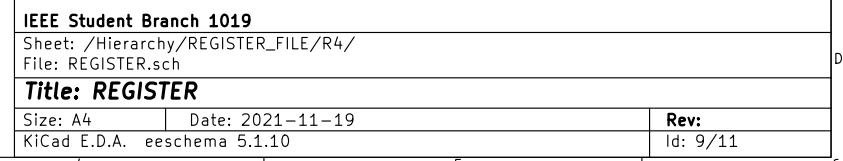
Size: A4 Date: 2021-11-19

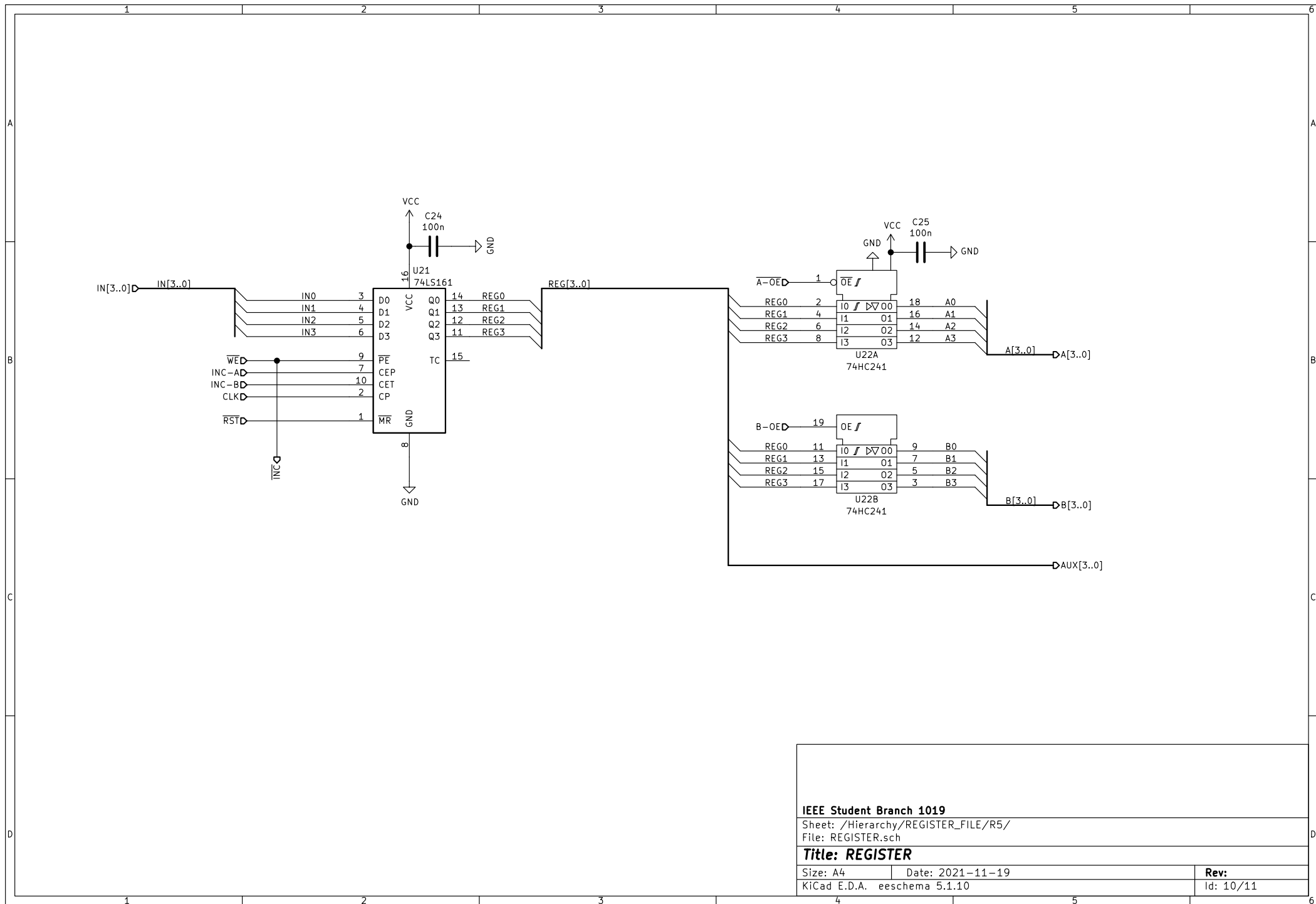
KiCad E.D.A. eeschema 5.1.10

Rev:

Id: 7/11







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Sheet: /Hierarchy/REGISTER_FILE/R5/
File: REGISTER.sch

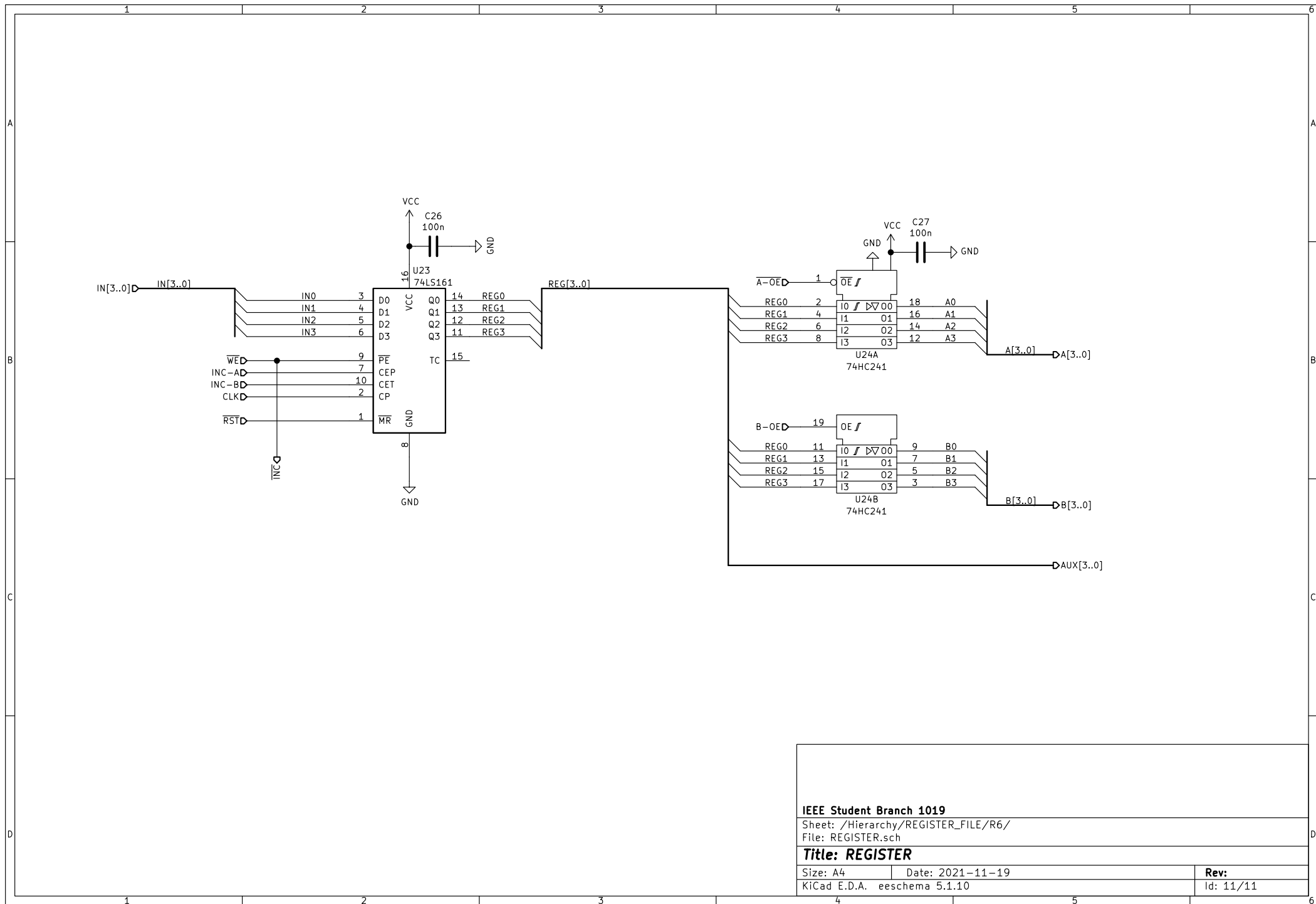
Title: REGISTER

Size: A4 Date: 2021-11-19

KiCad E.D.A. eeschema 5.1.10

Rev:

Id: 10/11



IEEE Student Branch 1019

Sheet: /Hierarchy/REGISTER_FILE/R6/
File: REGISTER.sch

Title: REGISTER

Size: A4 Date: 2021-11-19

KiCad E.D.A. eeschema 5.1.10

Rev:
Id: 11/11