

[illegible]

The schematic diagram illustrates the USB interface circuit for the J7A and J7B connectors. The J7A connector is connected to a +5V supply and ground. The J7B connector is connected to a +5V supply and ground. The circuit includes a USB D+ and D- signal path, a USB D+ and D- signal path, and a USB D+ and D- signal path. The circuit includes a USB D+ and D- signal path, a USB D+ and D- signal path, and a USB D+ and D- signal path.

SW debug interface

Three circuit diagrams illustrating different states of a 3V3 regulator:

- Top Diagram:** A 3V3 input is connected to a red diode labeled $D?$. The output is connected to a load resistor $R?$ and a 1k resistor to ground.
- Middle Diagram:** A 3V3 input is connected to a red diode labeled $D?$. The output is connected to a load resistor $R?$ and a 1k resistor to ground. The output is labeled **FAULT**.
- Bottom Diagram:** A 3V3 input is connected to a green diode labeled $D?$. The output is connected to a load resistor $R?$ and a 1k resistor to ground. The output is labeled **OK**.

Title			IMU Sensor Module Board V1.0		
Size		Number		Revision	
A3					
Date:	1/24/2026		Sheet of		
File:	D:\Mechatronics\1\S4V IMU Sensor Board V1.0		D:\Mechatronics\1\S4V IMU Sensor Board V1.0\SchematicSchDoc		

Board Stack Report