

DATA SHEET

SURFACE MOUNT MULTILAYER CERAMIC CAPACITORS

General purpose & High capacitance Class 2, X5R 4 V TO 50 V 100 pF to 100 μF

RoHS compliant & Halogen free



YAGEO Phi(comp



SCOPE

This specification describes X5R series chip capacitors with leadfree terminations.

YAGEO Phicomp

<u>APPLICATIONS</u>

PCs, Hard disk, Game PCs Power supplies **DVD** players Mobile phones Data processing

FEATURES

Supplied in tape on reel Nickel-barrier end termination RoHS compliant Halogen free compliant

ORDERING INFORMATION-GLOBAL PART NUMBER, PHYCOMP

CTC & 12NC

All part numbers are identified by the series, size, tolerance, TC material, packing style, voltage, process code, termination and capacitance value.

YAGEO BRAND ordering code

GLOBAL PART NUMBER (PREFERRED)

CC	<u>XXXX</u>	<u>X</u>	<u>X</u>	X5R	<u>X</u>	BB	XXX
	(1)	(2)	(3)		(4)		(5)

(I) SIZE - INCH BASED (METRIC) 0201 (0603) 0402 (1005) 0603 (1608) 0805 (2012) 1206 (3216) 1210 (3225)

(2) TOLERANCE

 $K = \pm 10\%$ $M = \pm 20\%$

(3) PACKING STYLE

R = Paper/PE taping reel; Reel 7 inch K = Blister taping reel; Reel 7 inch P = Paper/PE taping reel; Reel 13 inch F = Blister taping reel; Reel 13 inch

(4) RATED VOLTAGE

C = Bulk case

4 = 4 V 5 = 6.3 V6 = 10 V7 = 16 V8 = 25 V9 = 50 V

(5) CAPACITANCE VALUE

2 significant digits+number of zeros

The 3rd digit signifies the multiplying factor, and letter R is decimal point

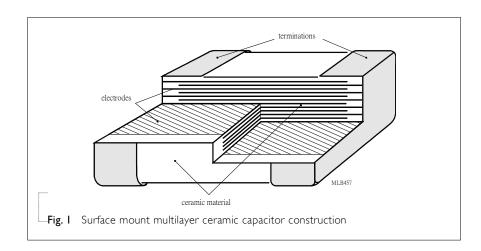
Example: $103 = 10 \times 10^3 = 10,000 \text{ pF} = 10 \text{ nF}$



CONSTRUCTION

The capacitor consists of a rectangular block of ceramic dielectric in which a number of interleaved metal electrodes are contained. This structure gives rise to a high capacitance per unit volume.

The inner electrodes are connected to the two end terminations and finally covered with a layer of plated tin (NiSn). The terminations are lead-free. A cross section of the structure is shown in Fig.I.

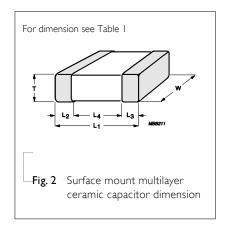


DIMENSION

Table I For outlines see fig. 2

TYPE	L _I (mm)	W (mm)	T (MM)	L_2 / L_3 min.	(mm) max.	L ₄ (mm) min.
	0.6 ±0.03 ^(I)	0.3 ±0.03 ⁽¹⁾				
0201	0.6 ±0.05 ⁽²⁾	0.3 ±0.05 ⁽²⁾		0.10	0.20	0.20
	0.6 ±0.09 ⁽³⁾	0.3 ±0.09 ⁽³⁾	_			
	1.0 ±0.05 ⁽¹⁾	0.5 ±0.05 ^(I)				
0402	1.0 ±0.10 ⁽²⁾	0.5 ±0.10 ⁽²⁾		0.15	U 3E	0.40
0 102	1.0 ±0.15 ⁽³⁾	0.5 ±0.15 ⁽³⁾		0.15	0.35	0.40
	1.0 ±0.20 ⁽⁴⁾	0.5 ±0.20 ⁽⁴⁾				
	1.6 ±0.10 ⁽¹⁾	0.8 ±0.10 ⁽¹⁾				
0603	1.6 ±0.15 ⁽²⁾	0.8 ±0.15 ⁽²⁾	Refer to table 2 to 4	0.20	0.60	0.40
	1.6 ±0.20 ⁽³⁾	0.8 ±0.20 ⁽³⁾	- Lable 2 to 4			
0805	2.0 ±0.10 ⁽¹⁾	1.25 ±0.10 ⁽¹⁾		0.25	0.75	0.55
	2.0 ±0.20 ⁽²⁾	1.25 ±0.20 ⁽²⁾	_	0.23	0.75	0.55
	3.2 ±0.15 ⁽¹⁾	1.6 ±0.15 ⁽¹⁾				
1206	3.2 ±0.30 ⁽²⁾	1.6 ±0.20 ⁽²⁾		0.25	0.75	1.40
	3.2 ±0.30 ⁽³⁾	1.6 ±0.30 ⁽³⁾				
1210	3.2 ±0.20 ⁽¹⁾	2.5 ±0.20 ⁽¹⁾		0.25	0.75	1.40
1210	3.2 ±0.40 ⁽²⁾	2.5 ±0.30 ⁽²⁾		0.25	0.75	1.40

OUTLINES



NOTE

- 1. Dimensions for size 0201, C < 1 μ F; 0402, C < 4.7 μ F; 0603, C < 4.7 μ F; $0805 \text{ to } 1210, C \le 100 \text{nF}$
- 2. Dimensions for size 0201, C \geq 100 nF (25V), C \geq 1 μ F; 0402, C =470nF(16V to 50V); 0603, $10\mu F$ (6.3V) ≥C ≥ 4.7 μF , C ≥2.2 μF (25V); 0805 to 1210, C > 100 nF
- 3. Dimensions for size 0201, C=2.2 μ F; 0402, C \geq 4.7 μ F, C=2.2 μ F(16V); 0603, C \geq $10\mu F$ (≥10V), C=22 μF (6.3V); 1206, C ≥ 22 μF (25V), C ≥ 100 μF
- 4. Dimensions for size 0402, $C \ge 10\mu F$



CAPACITANCE RANGE & THICKNESS FOR X5R

Table 2	Sizes from 0201 to 0402
·	0201

CAP.	0201	0111 0201	10 0 102				0402					
	4 V	6.3 V	10 V	16 V	25 V	50 V	4 V	6.3 V	10 V	16 V	25 V	50 V
100 pF		0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03						
150 pF		0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03						
220 pF		0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03						
330 pF		0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03						
470 pF		0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03						
680 pF		0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03						
1.0 nF		0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03						
1.5 nF		0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03							
2.2 nF		0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03							
3.3 nF		0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03							
4.7 nF		0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03							
6.8 nF		0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03							
10 nF		0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.03							
15 nF		0.3±0.03	0.3±0.03	0.3±0.03								
22 nF		0.3±0.03	0.3±0.03	0.3±0.03				0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05
33 nF		0.3±0.03	0.3±0.03	0.3±0.03				0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05
47 nF		0.3±0.03	0.3±0.03	0.3±0.03				0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05
68 nF		0.3±0.03	0.3±0.03	0.3±0.03				0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05
100 nF		0.3±0.03	0.3±0.03	0.3±0.03	0.3±0.05			0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05
150 nF								0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05
220 nF	0.3±0.03	0.3±0.03	0.3±0.03					0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05
330 nF								0.5±0.05	0.5±0.05			
470 nF	0.3±0.03	0.3±0.03						0.5±0.05	0.5±0.05	0.5±0.10	0.5±0.10	0.5±0.10
680 nF								0.5±0.05	0.5±0.05			
Ι.0 μF	0.3±0.05	0.3±0.05						0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05	
2.2 µF	0.3±0.09	0.3±0.09						0.5±0.05	0.5±0.05	0.5±0.15	0.5±0.20	
4.7 µF							0.5±0.15	0.5±0.15	0.5±0.15	0.5±0.15		
ΙΟ μF							0.5±0.20	0.5±0.20				

NOTE

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-6 series is available on request



CAPACITANCE RANGE & THICKNESS FOR XSR

YAGEO Phicomp

I		om 0603 to	0805									
CAP.	0603 4V	6.3 V	10.\/	16 V	2E V	E0\/	0805 4V	(2 \/	10.1/	16.17	25 V	50V
	47	0.3 V	10 V	10 4	25 V	50V	41	6.3 V	10 V	16 V	23 V	301
10 nF												
15 nF												
22 nF												
33 nF												
47nF												
68 nF												
100 nF												
150 nF												
220 nF		0.8±0.1	0.8±0.1	0.8±0.1	0.8±0.1	0.8±0.1						
330 nF		0.8±0.1	0.8±0.1	0.8±0.1	0.8±0.1	0.8±0.1						
470 nF		0.8±0.1	0.8±0.1	0.8±0.1	0.8±0.1	0.8±0.1		0.85±0.1 1.25±0.2	0.85±0.1 1.25±0.2	0.85±0.1 1.25±0.2	1.25±0.2	1.25±0.2
680 nF		0.8±0.1	0.8±0.1	0.8±0.1	0.8±0.1	0.8±0.1		0.85±0.1 1.25±0.2	0.85±0.1 1.25±0.2	0.85±0.1 1.25±0.2	1.25±0.2	1.25±0.2
1.0 μF		0.8±0.1	0.8±0.1	0.8±0.1	0.8±0.1	0.8±0.1		0.85±0.1 1.25±0.2	0.85±0.1 1.25±0.2	0.85±0.1 1.25±0.2	1.25±0.2	1.25±0.2
2.2 µF		0.8±0.1	0.8±0.1	0.8±0.1	0.8±0.15			0.85±0.1 1.25±0.2	0.85±0.1 1.25±0.2	0.85±0.1 1.25±0.2	0.85±0.1 1.25±0.2	1.25±0.2
4.7 µF		0.8±0.1	0.8±0.1	0.8±0.15	0.8±0.15			0.85±0.1 1.25±0.2	0.85±0.1 1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2
10 μF		0.8±0.15	0.8±0.2	0.8±0.2	0.8±0.20			0.85±0.1 1.25±0.2	0.85±0.1 1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2
22 µF		0.8±0.2	0.8±0.2					1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	
47 μF	0.8±0.2							1.25±0.2	1.25±0.2			
100 µF							1.25±0.2					

NOTE

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-6 series is available on request



6 18

Surface Mount Multilayer Ceramic Capacitors | General Purpose & High Cap. | X5R | 4 V to 50 V

Table 4 Sizes from 1206 to 1210 1206 1210 CAP. 6.3 V 10 V 16 V 25 V 50V 10 V 16 V 25 V 50V 6.3 V 10 nF 15 nF 22 nF 33 nF 47nF 68 nF 100 nF 150 nF 220 nF 330 nF 470 nF 680 nF 1.0 µF 1.15±0.1 1.15±0.1 1.15±0.1 1.15±0.1 1.6±0.2 1.25±0.2 1.25±0.2 1.25±0.2 1.25±0.2 1.25±0.2 $2.2~\mu F$ 1.15±0.1 1.15±0.1 1.15±0.1 1.15±0.1 1.6±0.2 1.9±0.2 1.9±0.2 1.9±0.2 1.9±0.2 1.9±0.2 4.7 µF 1.6±0.2 1.6±0.2 1.6±0.2 1.6±0.2 1.6±0.2 1.9±0.2 1.9±0.2 1.9±0.2 1.9 ± 0.2 2.5 ± 0.2 2.5 ± 0.2 10 μF 1.6±0.2 1.6±0.2 1.6±0.2 1.6±0.2 1.6±0.3 1.9±0.2 1.9±0.2 1.9±0.2 1.9±0.2 22 µF 1.6±0.2 1.6±0.2 1.6±0.2 1.6±0.3 2.5 ± 0.2 2.5 ± 0.2 2.5±0.2 2.5 ± 0.3 47 µF 1.6±0.2 1.6±0.2 1.6±0.3 2.5 ± 0.2 2.5 ± 0.2 2.5 ± 0.2 Ι00 μΕ 1.6±0.3 2.5±0.3 2.5±0.3 2.5±0.3 220 μF 2.5 ± 0.3

NOTE

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-6 series is available on request



Surface Mount Multilayer Ceramic Capacitors | General Purpose & High Cap. | X5R | 4 V to 50 V

THICKNESS CLASSES AND PACKING QUANTITY

 Tal	ble	- 5	
ıaı	יוט		

Table 3							
SIZE	THICKNESS	TAPE WIDTH -		I / 7 INCH		1 / 13 INCH	QUANTITY
CODE	CLASSIFICATION	QUANTITY PER REEL	Paper	Blister	Paper	Blister	PER BULK CASE
0201	0.3 ±0.03 mm	8 mm	15,000		50,000		
0402	0.5 ±0.05 mm	8 mm	10,000		50,000		50,000
0603	0.8 ±0.1 mm	8 mm	4,000		15,000		15,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		10,000
0805	0.85 ±0.1 mm	8 mm	4,000		15,000		8,000
	1.25 ±0.2 mm	8 mm		3,000		10,000	5,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		
	0.85 ±0.1 mm	8 mm	4,000		15,000		
1206	1.00 / 1.15 ±0.1 mm	8 mm		3,000		10,000	
1200	1.25 ±0.2 mm	8 mm		3,000		10,000	
	1.6 ±0.15 mm	8 mm		2,500		10,000	
	1.6 ±0.2 mm	8 mm		2,000		8,000	
	0.6 / 0.7 ±0.1 mm	8 mm		4,000		15,000	
	0.85 ±0.1 mm	8 mm		4,000		10,000	
	1.15 ±0.1 mm	8 mm		3,000		10,000	
	1.15 ±0.15 mm	8 mm		3,000		10,000	
	1.25 ±0.2 mm	8 mm		3,000			
1210	1.5 ±0.1 mm	8 mm		2,000			
	1.6 / 1.9 ±0.2 mm	8 mm		2,000			
	2.0 ±0.2 mm	8 mm		2,000 1,000			
	2.5 ±0.2 mm	8 mm		1,000 500			



ELECTRICAL CHARACTERISTICS

YAGEO Phicomp

X5R DIELECTRIC CAPACITORS; NISN TERMINATIONS

Unless otherwise specified, all tests and measurements shall be made under standard atmospheric conditions for testing as given in 5.3 of IEC 60068-1:

- Temperature: 15 $^{\circ}\text{C}$ to 35 $^{\circ}\text{C}$ - Relative humidity: 25% to 75% - Air pressure: 86 kPa to 106 kPa

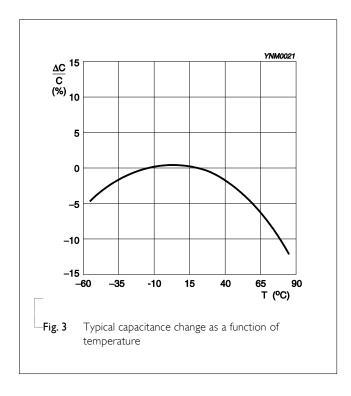
Before the measurements are made, the capacitor shall be stored at the measuring temperature for a time sufficient to allow the entire capacitor to reach this temperature.

The period as prescribed for recovery at the end of a test is normally sufficient for this purpose.

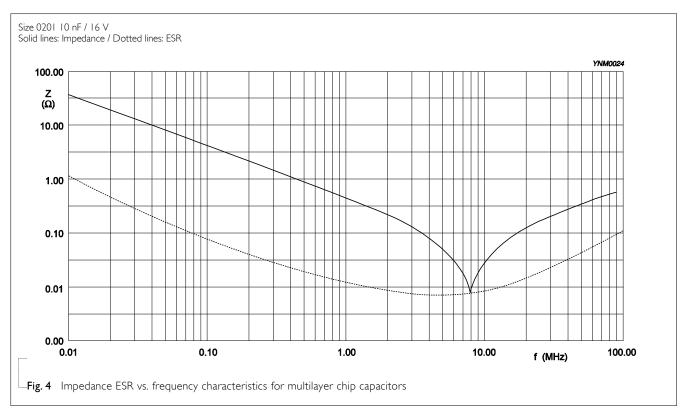
Table 6	6						
DESCRIPT	ION						VALUE
Capacitano	ce range					I0 nF	to 100 μF
	ce tolerance					±10%	and ±20%
Dissipation	n factor (D.F.)						
X5R	0201	0402	0603	0805	1206	1210	D.F.
6.3V	100pF to 10nF	22nF to 100nF	220nF to TuF	470nF to 680nF	luFto 10uF	luF to 10uF	≤ 5%
		120nF to 220nF				22uF	≤ 7%
	12nF to 1uF	330nF to 10uF	2.2uF to 47uF	luF to 100uF	22uF to 47uF	47uF to 220uF	≤ 10%
	2.2uF				100uF		≤ 15%
IOV	100pF to 10nF	22nF to 100nF	220nF to 470nF	470nF to 680nF	I uF to 4.7uF	IuF to 4.7uF	≤ 5%
		120nF to 220nF	680nF	luF			≤ 7%
	I 2nF to 220nF	330nF to 4.7uF	I uF to 22uF	2.2uF to 47uF	10uF to 47uF	10uF to 100uF	≤ 10%
16V	100pF to 10nF	22nF to 100nF	220nF to 470nF	470nF to 680nF	luF to 4.7uF	IuF to 4.7uF	≤ 5%
		120nF to 220nF	680nF to TuF	I uF to 2.2 µ F			≤ 7%
	12nF to 100nF	470nF to 4.7uF	2.2uF to 10uF	4.7uF to 22uF	10uF to 47uF	10uF to 100uF	≤ 10%
25V	100pF to 10nF	22nF		470nF to TuF	luF to 2.2uF	IuF to 4.7uF	≤ 3.5%
		27nF to 100nF	220nF to 470nF	2.2uF	4.7uF	I OuF	≤ 5%
		120nF to 220nF	680nF to TuF				≤ 7%
	I 00nF	470nF to 2.2uF	2.2uF to 10uF	4.7uF to 22uF	10uF to 22uF	22uF	≤ 10%
50V	100pF to 1nF	22nF					≤ 3.5%
		27nF to 120nF					≤ 5%
		150nF to 220nF					≤ 7%
		470nF	220nF to TuF	470nF to 10uF	luF to 10uF	luF to 10uF	≤ 10%
Insulation i	resistance after 1 min	ute at U _r (DC)		$R_{ins} \ge 10 \text{ G}\Omega \text{ or } R_i$	$_{ns} \times C_r \ge 500(100$	/50) seconds which	ever is less
	capacitance change as		perature				±15%
<u> </u>	ure characteristic/coef	fficient):					
Operating	temperature range:					-55 °C t	to +85 °C

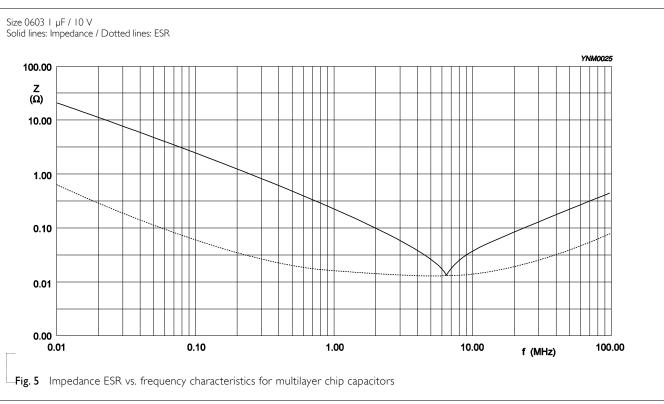




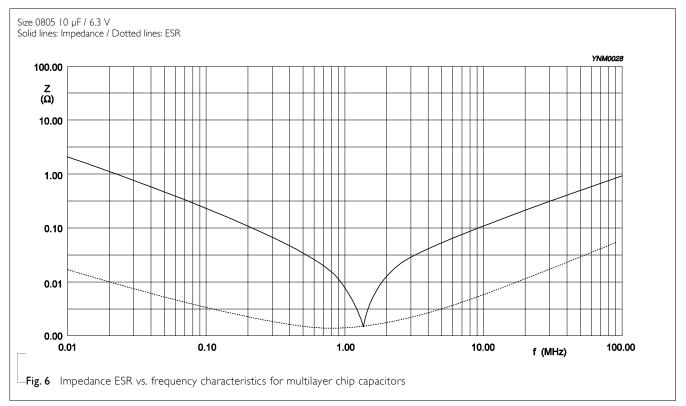


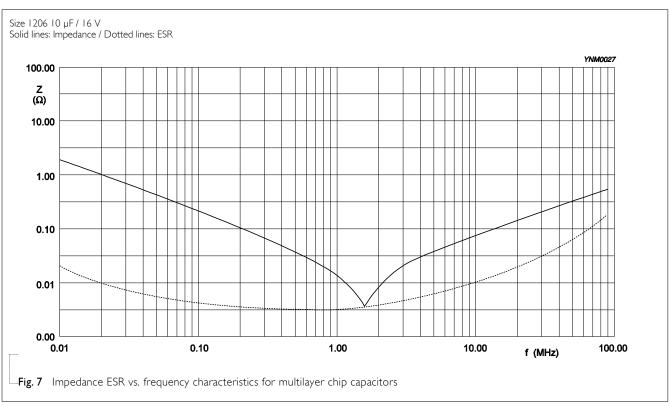








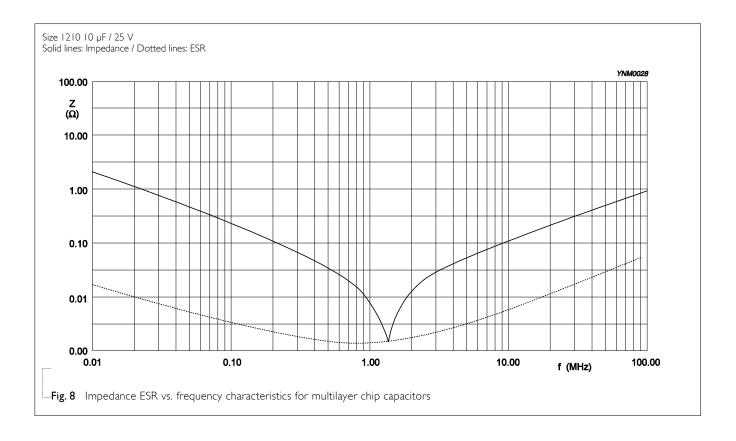






YAGEO Phicomp

Surface Mount Multilayer Ceramic Capacitors | General Purpose & High Cap. | X5R | 4 V to 50 V



SOLDERING RECOMMENDATION

	I	a	Ы	le	7
--	---	---	---	----	---

SOLDERING METHOD	SI∠E 0402	0603	0805	1206	≥ 1210
Reflow	≥ 0.1 µF	≥ 1.0 µF	≥ 2.2 µF	≥ 4.7 µF	Reflow only
Reflow/Wave	< 0.1 µF	< 1.0 µF	< 2.2 µF	< 4.7 µF	



Surface Mount Multilayer Ceramic Capacitors | General Purpose & High Cap. | X5R | 4 V to 50 V

TESTS AND REQUIREMENTS

Table 8 Test procedures and requirements

TEST	TEST MET	HOD	PROCEDURE	REQUIREMENTS		
Mounting	IEC 60384- 21/22	4.3	The capacitors may be mounted on printed-circuit boards or ceramic substrates	No visible damage		
Visual Inspection and Dimension Check		4.4	Any applicable method using × 10 magnification	In accordance with specification		
Capacitance (I)		4.5.1	Class 2: At 20 °C, 24 hrs after annealing	Within specified tolerance		
			f = 1 KHz for C \leq 10 μ F, rated voltage $>$ 6.3 V, measuring at voltage 1 V _{rms} at 20 °C			
			$f=1$ KHz, for C \leq 10 μF , rated voltage \leq 6.3 V, measuring at voltage 0.5 V_{rms} at 20 $^{\circ}C$			
			$f=$ 120 Hz for C $>$ 10 μF , measuring at voltage 0.5 V_{rms} at 20 $^{\circ} C$			
Dissipation Factor (D.F.) (1)		4.5.2	Class 2: At 20 °C, 24 hrs after annealing $f = 1$ KHz for $C \le 10$ μ F, rated voltage > 6.3 V, measuring at voltage 1 V _{rms} at 20 °C	In accordance with specification		
			$f=$ 1 KHz, for C \leq 10 μF , rated voltage \leq 6.3 V, measuring at voltage 0.5 V_{rms} at 20 $^{\circ} C$			
			f = 120 Hz for C > 10 μ F, measuring at voltage 0.5 V_{rms} at 20 $^{\circ}$ C			
Insulation Resistance		4.5.3	At U_r (DC) for I minute	In accordance with specification		

NOTE

 $I.\ The\ figure\ indicates\ typical\ inspection.\ Please\ refer\ to\ individual\ specifications.$

REQUIREMENTS

 Δ C/C: ± 30 ppm

X7R: Δ C/C: ±15% Y5V: Δ C/C: 22~-82%

Class I:

Class2:

Class2:

<General purpose series>

<High Capacitance series>

 \times 7R/ \times 5R: Δ C/C: \pm 15% Y5V: Δ C/C: 22~-82%

TEST **TEST METHOD PROCEDURE**

Temperature Characteristic

YAGEO Phicomp

Capacitance shall be measured by the steps shown in the following table.

> The capacitance change should be measured after 5 min at each specified temperature stage.

Step	Temperature(°C)
a	25±2
Ь	Lower temperature±3℃
С	25±2
d	Upper Temperature±2℃
е	25±2

(I) Class I

Temperature Coefficient shall be calculated from the formula as below

Temp, Coefficient =
$$\frac{C2 - C1}{C1 \times \Delta T} \times 10^6$$
 [ppm/°C]

C1: Capacitance at step c

C2: Capacitance at 125℃

 ΔT : 100°C(=125°C-25°C)

(2) Class II

Capacitance Change shall be calculated from the formula as below

$$\Delta C = \frac{C2 - C1}{C1} \times 100\%$$

C1: Capacitance at step c

C2: Capacitance at step b or d

Adhesion

4.7 A force applied for 10 seconds to the line joining the terminations and in a plane parallel to the substrate

Force

size ≥ 0603: 5N size = 0402: 2.5N

Bending Strength

IEC 60384-21/22

4.8

Mounting in accordance with IEC 60384-22 paragraph 4.3

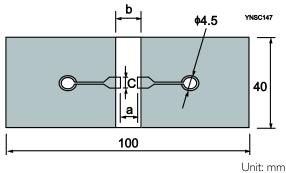
size = 0201: 1NNo visible damage

Conditions: bending I mm at a rate of I mm/s, radius jig 5 mm

 Δ C/C Class2:

X5R: ±12.5%

Test Substrate:



Dimension(mm) Type a b c 0201 0.3 0.9 0.3 0402 0.4 1.5 0.5 0603 1.0 3.0 1.2				
0201 0.3 0.9 0.3 0402 0.4 1.5 0.5		Dimension(mm)		
0402 0.4 1.5 0.5	Туре	a	Ь	С
	0201	0.3	0.9	0.3
0603 1.0 3.0 1.2	0402	0.4	1.5	0.5
	0603	1.0	3.0	1.2
0805 1.2 4.0 1.65	0805	1.2	4.0	1.65
1206 2.2 5.0 1.65	1206	2.2	5.0	1.65
1210 2.2 5.0 2.0	1210	2.2	5.0	2.0

YAGEO Phicomp

Surface Mount Multilayer Ceramic Capacitors | General Purpose & High Cap. | X5R | 4 V to 50 V

TEST	TEST METHO	DD	PROCEDURE	REQUIREMENTS
Resistance to Soldering Heat		4.9	Precondition: $150 + 0/-10$ °C for I hour, then keep for 24 ± I hours at room temperature Preheating: for size ≤ 1206 : 120 °C to 150 °C for I	Dissolution of the end face plating shall not exceed 25% of the length of the edge concerned
			minute	<general purpose="" series=""></general>
			Preheating: for size >1206: 100 °C to 120 °C for I	∆C/C
			minute and 170 °C to 200 °C for I minute	Class2:
			Solder bath temperature: 260 ±5 °C	X5R: ±10%
			Dipping time: 10 ±0.5 seconds	<high capacitance="" series=""></high>
			Recovery time: 24 ±2 hours	ΔC/C
				Class2:
				X5R: ±10%
			-	D.F. within initial specified value
				R _{ins} within initial specified value
Solderability		4.10	Preheated the temperature of 80 °C to 140 °C and maintained for 30 seconds to 60 seconds.	The solder should cover over 95% of the critical area of each termination
			I. Temperature: 235±5°C / Dipping time: 2 ±0.5 s	
			2. Temperature: 245±5°C / Dipping time: 3 ±0.5 s (lead free)	
			Depth of immersion: 10mm	
Rapid Change of	IEC 60384-	4.11	Preconditioning; 150 +0/-10 °C for I hour, then keep for 24 ±1 hours at _	No visual damage
Temperature			room temperature	<general purpose="" series=""></general>
			'	ΔC/C
			5 cycles with following detail:	Class2:
			30 minutes at lower category temperature 30 minutes at upper category temperature	X5R: ±15%
				<high capacitance="" series=""></high>
			Recovery time 24 ±2 hours	ΔC/C
				Class2:
				X5R: ±15%
			-	D.F. meet initial specified value
				R _{ins} meet initial specified value



TEST TEST METHOD PROCEDURE REQUIREMENTS Damp Heat 1. Preconditioning, class 2 only: No visual damage after recovery with U_r Load 150 + 0/-10 °C /I hour, then keep for 24 \pm I hour <General purpose series> at room temp Δ C/C 2. Initial measure: Class2: Spec: refer to initial spec C, D, IR X5R: ±15% 3. Damp heat test: D.F. 500 \pm 12 hours at 40 \pm 2 °C; Class2: 90 to 95% R.H. I.O U_r applied X5R: ≤ 16V: ≤ 7% 4. Recovery: ≥ 25V: ≤ 5% Class 2: 24 ±2 hours R_{ins} 5. Final measure: C, D, IR Class2: $X5R: \ge 500 \ M\Omega \ \text{or} \ R_{\text{ins}} \times C_r \ge 25s$ P.S. If the capacitance value is less than the minimum whichever is less value permitted, then after the other measurements have been made the capacitor shall be preconditioned <High Capacitance series> according to "IEC 60384 4.1" and then the Δ C/C requirements shall be met. Class2: X5R: ±20% D.F. Class2: X5R: 2 x initial value max R_{ins} Class2: Rins x Cr ≥ 5s whichever is less



TEST	TEST METHO	D	PROCEDURE	REQUIREMENTS
TEST Endurance		9D 4.14	PROCEDURE 1. Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for 24 ±1 hour at room temp 2. Initial measure: Spec: refer to initial spec C, D, IR 3. Endurance test: Temperature: X5R: 85 °C	REQUIREMENTS No visual damage <general purpose="" series=""> ΔC/C Class2: X5R: ±15% D.F.</general>
			Specified stress voltage applied for 1,000 hours: Applied 2.0 × Ur for general product. Applied 1.5 × Ur for high cap. product. Applied 1.0 × Ur for high cap. product. Applied 1.0 × Ur for high cap. product. 0201: 100nF/25V, 220nF/10V,/ 1uF, 2.2uF/ 4V, 6.3V; 0402: 4.7uF/16V, 10V, 6.3V; 10uF/4V, 6.3V; 0603: 10uF/ 10V; 22uF/6.3V, 10V; 47uF/4V; 0805: 10uF/ 25V, 50V; 22uF/ 6.3V, 10V, 16V; 47uF/ 6.3V, 10V; 100uF/ 4V;1206: 10uF/ 50V; 4. Recovery time: 24 ±2 hours 5. Final measure: C, D, IR	Class2:
			P.S. If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the capacitor shall be preconditioned according to "IEC 60384 4.1" and then the requirements shall be met.	Class 2: X5R: ±20% D.F. Class 2: X5R: 2 × initial value max R _{ins} Class 2: Rins × Cr ≥ 10s whichever is less
Voltage Proof		4.6	Specified stress voltage applied for I~5 seconds Ur ≤ 100 V: series applied 2.5 Ur 100 V < Ur ≤ 200 V series applied (1.5 Ur + 100) 200 V < Ur ≤ 500 V series applied (1.3 Ur + 100) Ur > 500 V: I.3 Ur Ur ≧ 1000 V: I.2 Ur Charge/Discharge current is less than 50 mA	No breakdown or flashover





Surface Mount Multilayer Ceramic Capacitors | General Purpose & High Cap. | X5R | 4 V to 50 V

REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 21	Jan. 28, 2016	-	- Tests and requirements updated
Version 20	Dec. 04, 2015	-	- Size updated
Version 19	Apr. 09, 2015	-	- Voltage updated
Version 18	Jul. 07, 2014	-	- Voltage updated
Version 17	Mar. 31, 2014	-	- Test condition updated
Version 16	Nov. 29, 2012	-	- Test condition updated
Version 15	Sep. 03, 2012	-	- Test condition updated
Version 14	May 16, 2012	-	- Product range updated
Version 13	May 02, 2012	-	- Product range updated
Version 12	Feb 10, 2012	-	- Product range updated
Version 11	Oct 21, 2011	-	- Product range updated
Version 10	Jun 21, 2011	-	- Product range updated
Version 9	Mar 23, 2011	-	- Product range updated
Version 8	Jan 25, 2011	-	- Rated voltage of 0201 extend to 50V
Version 7	Jan 05, 2011	-	- Product range updated
Version 6	Jul 27, 2010	-	- Dimension on 0603 and 1206 case size updated
Version 5	Apr 21, 2010	-	- The statement of "Halogen free" on the cover added
			- Dimension updated
Version 4	Jan 13, 2010	-	- Thickness updated
Version 3	Aug 17, 2009	-	- Dimension updated
Version 2	Jun 09, 2009	-	- Ordering code updated
Version I	May 15, 2009	-	- Product range updated
Version 0	Apr 15, 2009	-	- New datasheet for general purpose and high capacitance X5R series with RoHS compliant
			- Replace the "6.3V to 50V" part of pdf files: UP-X5R_X7R_HighCaps_6.3-to-25V_II, UY-X5R_X7R_HighCaps_6.3-to-25V_II
			- Combine 0201 from pdf files: UP-NP0X5RX7RY5V_0201_6.3-to-50V_2 and UY-NP0X5RX7RY5V_0201_6.3-to-50V_2
			- Define global part number
			- Description of "Halogen free compliant" added
			- Test method and procedure updated