

The diagram illustrates the SPI (Serial Peripheral Interface) connection between a microcontroller and a memory chip. The microcontroller's pins are connected to the memory chip's pins as follows:

- MISO** (Master In Slave Out) is connected to the memory chip's MISO pin.
- GND** (Ground) is connected to the memory chip's GND pin.
- SCK** (Serial Clock) is connected to the memory chip's SCK pin.
- 3V3** (3.3V) is connected to the memory chip's 3V3 pin.
- MOSI** (Master Out Slave In) is connected to the memory chip's MOSI pin.
- CS SD** (Chip Select) is connected to the memory chip's CS pin.

