Shane T. Fleming

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Education

Imperial College London
Ph.D., Reconfigurable Computing [2018].
MEng, Electronic and Information Engineering 1st [2012].

Thesis

"Program Slicing for Reliability and Runahead in High-Level Synthesis"
Developed fully automated HLS compiler optimisations that produce circuits
that are: more reliable, with guaranteed protected control-flow; and higher
performing, via application-specific prefetchers.

Research

Microsoft Research Cambridge [2019 - now]
Researcher
Project: Honeycomb - Hardware offloads for distributed data-stores

Cambridge University Computer Laboratory [2018 - 2019]
Visiting Researcher

Imperial College London [2017 - 2019]
Postdoctoral Research Associate
Project: Partially Ordered Event-Triggered Systems

Imperial College London [2015 - 2016]
Research Assistant
Project: Codesign: A higher-order approach

Teaching

Electrical and Electronic Engineering, Imperial College London
Lecturer, User Centered Information Systems, [2018]

Electrical and Electronic Engineering, Imperial College London
Lead GTA, Language Processors, [2015]
Lead GTA, Electronics Laboratory, [2013, 2014]

Awards	**2018 Faculty of Engineering Julia Higgins Fund** **2014 GTA of the year (shortlisted)** Student Academic Choice Awards **2013 GTA of the Year Award (shortlisted)** Faculty of Engineering **2013 GTA of the year award** Electrical & Electronic Engineering Dept. **2013 Doctoral Training Funding** EPSRC
Supervision	**Qiang Li [2015 - 2019]** PhD Co-Supervisor **Jianyi Cheng [2018 - 2019]**, MSc Co-Supervisor
External Responsibilities	**Field Programmable Logic 2020 (FPL)** Program committee member **Workshop on Open Source Design Automation 2019 (OSDA)** Program committee member. **IEEE Trans on Architecture and Code Optimization (TACO)** Journal reviewer