



POLITÉCNICA

Sistemas Empotrados y Ubicuos

Máster Universitario en Ingeniería Infórmatica

Cross-Development Environment Work

Version 1.2 — September 17, 2024

CROSS-DEVELOPMENT ASSIGNMENT

UNIVERSIDAD POLITÉCNICA DE MADRID
DATSI

DEPARTAMENTO DE ARQUITECTURA Y TECNOLOGÍA DE SISTEMAS

INFORMÁTICOS

<https://www.datsi.fi.upm.es>

© 2024 DATSI/UPM.



This work is licensed under a Creative Commons Attribution-NonCommercial-ShareAlike 4.0 International License. <https://creativecommons.org/licenses/by-nc-sa/4.0/>

Contents

Introduction	1
References	1
Acronyms	2
Overview	3
Laboratory kit components	3
Architecture of the laboratory system	3
Computer board and connections	4
1 Set up	7
Set up	7
1.1 Install a native programming environment	7
Install a native programming environment	7
1.2 Install the cross-compilation tools	9
Install the cross-compilation tools	9
1.3 Install MATLAB™and Simulink™	11
Install MATLAB™and Simulink™	11
2 OBDH	13
2.1 Software architecture	13
2.2 Serial line connections	14
2.3 Host terminal application	14
2.4 Download the code and study the implementation	16
2.5 Compile and run	17
2.6 Make changes to the program	18
2.7 Perform a temporal analysis of the system	18
3 OBDH with ACS	21
3.1 Model In the Loop (MIL) validation	21
3.2 Code generation	23
3.3 Software architecture	24
3.4 Compile and run with the debugger.	27
3.5 Processor In the Loop (PIL) validation	27
3.6 Make changes to the simulation	27
4 Documented report	29
A Temperature sensor	31

Introduction

This document provides instructions for laboratory work in the field of embedded systems. The laboratory is part of the Embedded and Ubiquitous Systems course of the UPM's Máster Universitario en Ingeniería Informática (MUII) program. The laboratory is based on a computer kit that is used to build a simplified version of a satellite on-board software system (OBSW). An instance of the laboratory kit will be made available to every student registered in the course during the laboratory session.

Students are required to use their own personal computer, running Windows, MacOS, or GNU/Linux, to carry out the laboratory assignments. The outline of the laboratory assignments is as follows:

1. Installation of a native programming environment.
2. Installation of the cross-platform programming tools.
3. On-board data handling (OBDH) system.
4. OBDH system with Attitude Control System (ACS).

References

The following documents contain additional information about the software and hardware tools used to develop the work:

Hardware

1. STMicroelectronics. DB1421 Data Brief. STM32F4DISCOVERY - Discovery kit with STM32F407VG MCU.
2. STMicroelectronics. UM1472 User manual - Discovery kit with STM32F407VG MCU.
3. STMicroelectronics. DS 8626. Data sheet - STM32F405xx, STM32F407xx. ARM Cortex-M4 32b MCU+FPU, 210DMIPS, up to 1MB Flash/192+4KB RAM, USB OTG HS/FS, Ethernet, 17 TIMs, 3 ADCs, 15 comm. interfaces & camera.
4. STMicroelectronics. RM0090 Reference manual - STM32F405/415, STM32F407/417, STM32F427/437 and STM32F429/439 advanced Arm[®]-based 32-bit MCUs.

Software

1. Access and installation procedure of MATLAB for UPM students.
2. Ada Reference Manual.
3. GNAT User's Guide for Native Platforms.
4. GNAT User's Guide Supplement for Cross Platforms
5. GNAT Reference Manual.

Acronyms

ADC	Analog to Digital Converter.
ACS	Attitude Control System.
DAC	Digital to Analog Converter.
FPU	Floating Point Unit.
GCC	GNU compilation system..
GDB	GNU Debugger.
GNAT	GNU Ada Translator.
GNU	GNU is not Unix.
GPL	GNU Public License.
GPS	GNAT Programming Studio.
LGPL	Lesser GNU Public License (formerly Library GPL).
MCU	Microcontroller Unit.
OBC	On-Board Computer.
OBDH	On-Board Data Handling.
OBSW	On-Board Software.
OS	Operating System.
PC	IBM Personal Computer architecture.
TC	Telecommand.
TM	Telemetry.
USART	Universal Synchronous Asynchronous Receiver Transmitter.
USB	Universal Serial Bus.

Overview

Laboratory kit components

The laboratory kit includes:

- An STM32F407 computer board that emulates an on-board computer (OBC) system.
- A USB A / mini USB cable that is used to connect the OBC to the development station hosted on the student PC.
- A USB / UART interface cable that is used to provide a serial line link between the OBC board and the ground station software running on the student PC.

The figure 1 shows the components of the laboratory kit and the connections to a PC.

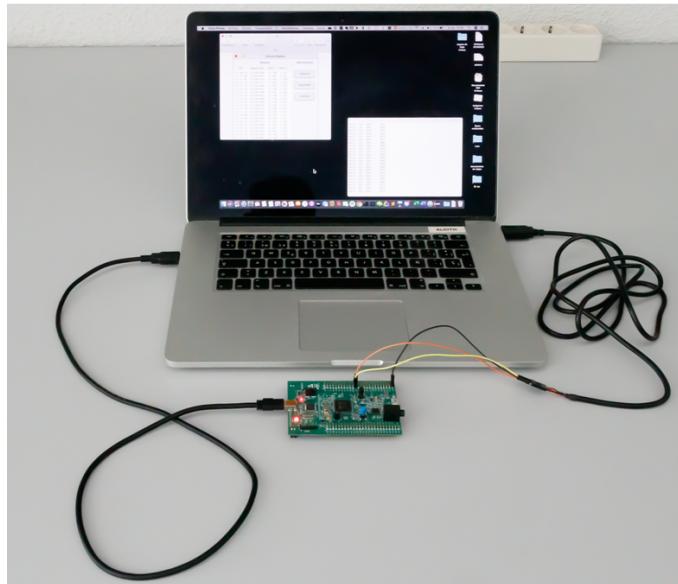


Figure 1: Laboratory kit.

Architecture of the laboratory system

The figure 2 shows the architecture of the laboratory system which consists of two segments:

- The *flight segment* is deployed on the laboratory computer board.
- The *ground segment* is deployed on the student's PC.

The communication between both segments is carried out by means of a serial line, simulating the radio link of a real satellite mission. The student's work is centered on programming the computer board. The ground station software will be provided by the professors.

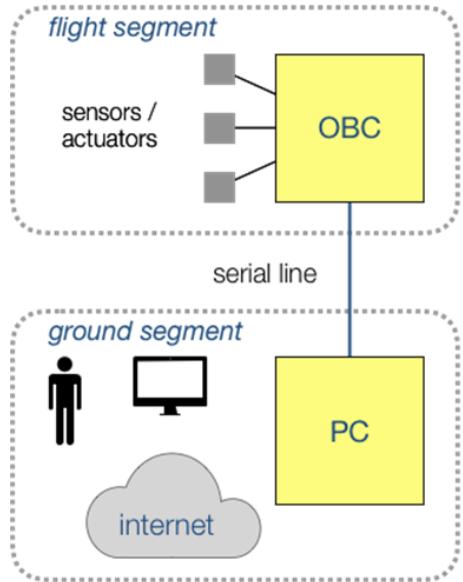


Figure 2: Architecture of the laboratory system.

Computer board and connections

The STM32F407 board is used as a low-cost replacement for a satellite OBC. The board features a 32-bit ARM Cortex-M4 microcomputer, 192 KB of RAM, 1 MB of Flash memory and a number of embedded devices. The figure 3 shows an overall view of the computer board.

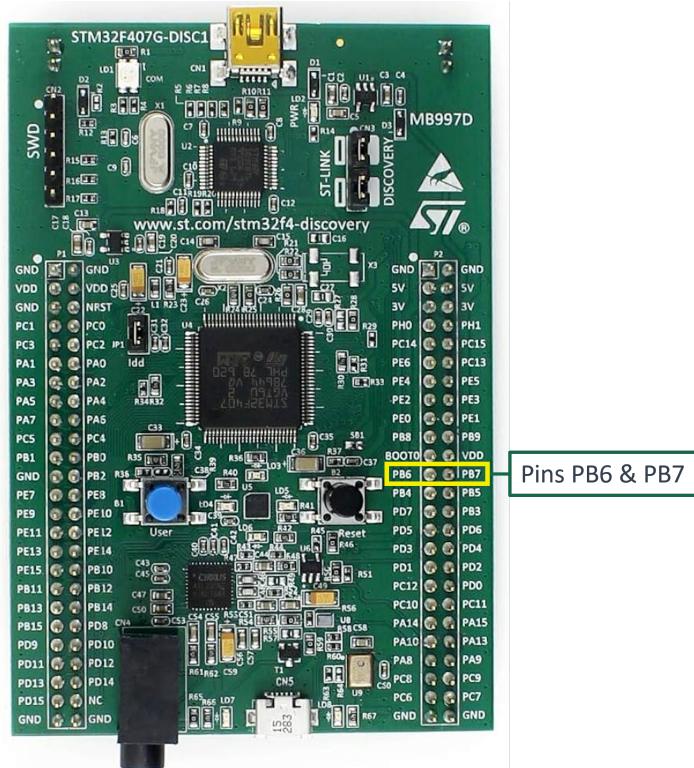


Figure 3: Computer board.

The following are the main components used in this laboratory:

- USB ST-LINK connector, which is linked to a PC with a mini-USB to USB-A cable. This connection is used to supply power to the board (5 V) and load and debug the software from the host PC.

- General Purpose Input-Output (GPIO) pins PB6, PB7 and GND. GPIO is a standard interface for connecting external devices. These GPIO pins are used in the laboratory to connect a serial line to a USB port on a PC, emulating the connection to the on-board radio equipment in a satellite. Specifically the pin:
 - **PB6** is used as the transmitter pin (TX).
 - **PB7** is used as the receiver pin (RX).
 - **GND** is used for grounding.
- Temperature and voltage sensors. These sensors are part of the STM32 microcomputer chip, and can be read using internal registers in the MCU. They are used in the laboratory to emulate the housekeeping devices onboard the satellite.

Assignment 1

Set up

1.1 Install a native programming environment

The aim of this assignment is to install a native programming environment for the Ada language on the student's PC. This environment will later be extended with cross-compilation tools for the STM32 board to be used in the laboratory.

The programming environment is the GNAT Community version, an open-source software development environment freely available from AdaCore, a company specialized in providing tools and solutions for developing high-integrity software.

1.1.1 Download and install GNAT

The GNAT Community compilation system can be downloaded from <https://www.adacore.com/download/more>. Installation packages for Windows, MacOS-Intel and GNU Linux are available at the download page. The file `README.txt` provides installation instructions, which are summarized in the following subsections:

Windows

1. Download the file `gnat-2021-20210519-x86_64-windows64-bin.exe`
2. Run the file and follow the instructions.

Important: GNATStudio installs additional configuration files in the `.gnatstudio` folder, which is located under your Home directory (e.g.: `C:\\\\user_name\\\\.gnatstudio`). Notice that the application will not start if your user account contains special character such as spaces or accent marks. Then, to solve this you must remove all special characters from your user account. This link provides detailed instructions to solve this issue.

MacOS-Intel

1. Download the file `gnat-2020-20200818-x86_64-darwin-bin.dmg`
2. Open the dmg disk and execute the application inside it. In order to circumvent the system protection, control-click on the file and then click on "opens" in the emergent window.

Notice that you need to have installed the Xcode application to install GNAT. If you still see the following error:

```
ld: library not found for -lSystem
```

then you might have to execute the following:

```
xcode-select -s /Applications/Xcode.app/Contents/Developer
```

GNU Linux

1. Download the file `gnat-2021-20210519-x86_64-linux-bin`
2. You will need provide execution permissions to the binary in order to run it. Run the following command in your terminal:

```
chmod +x path_to_the_package.bin
```

and execute the package. The `README.txt` file contains additional installation and execution instructions.

1.1.2 Test the installation with a simple program

The GNAT compilation system includes the GPS (GNAT Programming Studio) integrated development environment, which allows users to edit, compile, and run Ada and C programs. Figure 1.1 shows the main GPS window, which is composed of the following areas:

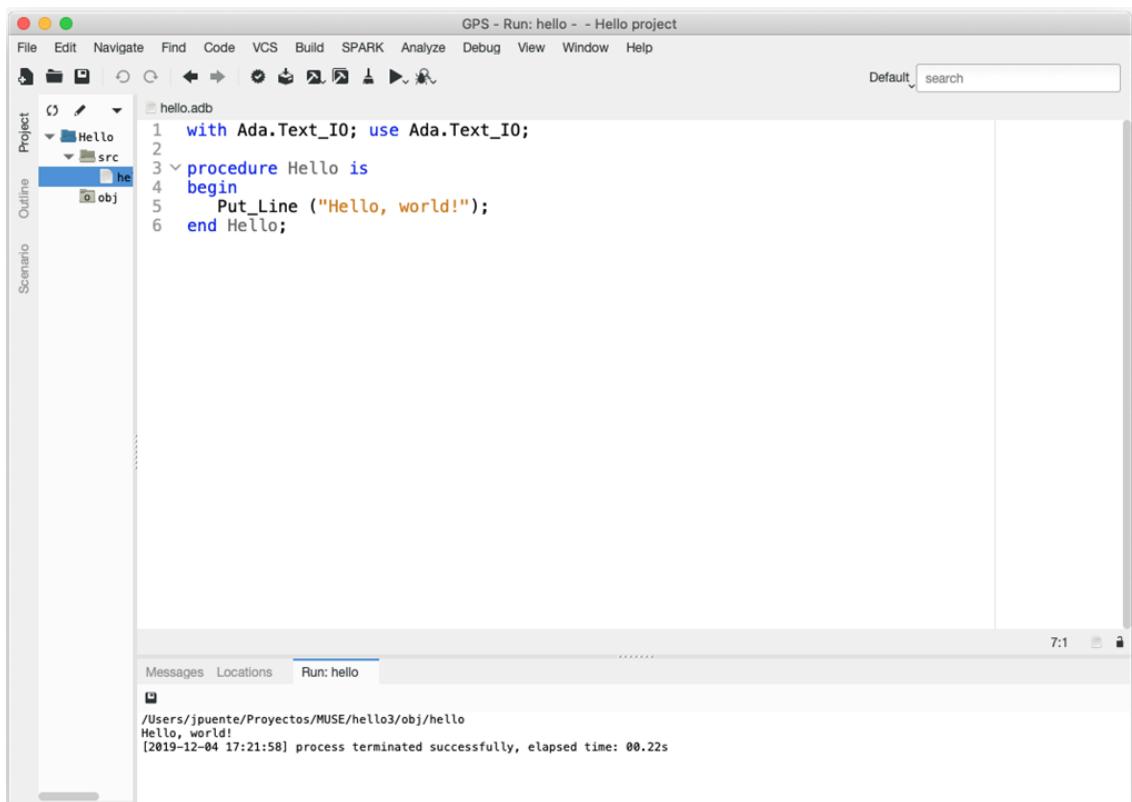


Figure 1.1: GNAT Programming Studio (GPS).

- a menu bar at the top
- a tool bar under the menu bar
- on the left, a notebook allowing you to switch between Project, Outline and Scenario views
- the working area in the center
- the messages window at the bottom

GPS organizes source code in projects. A project is a set of source files which are compiled together in order to produce a single binary executable. Before starting you will need to create a folder to store your software projects. The recommendation is to create a folder named **SEU-OBDH-Lab** in the location of your choice.

The next activity consists on writing and running a simple Ada program with GPS:

1. Create a new project by clicking on File → New Project ... in the top menu. Choose the Simple Ada Project template.
2. Choose a folder to deploy the project, e.g. **SEU-OBDH-Lab/lab-1**. Set the project's name to **Hello** and the main name also to **Hello**.
3. Double click on the **hello.adb** file in the project view to open the file in the working area.
4. Edit the file in the working area so that it has the same content as in figure 1.1.
5. Build and run the executable by clicking on the **>** symbol in the tool bar. You should see a number of compilation-related messages and, if everything is right, you will see the text **Hello, world!** in the Run tab of the bottom window.

1.2 Install the cross-compilation tools

The aim is to get acquainted with the embedded computer board and to install and test the cross-compilation tools for GNAT that will be used to develop executable code for it.

1.2.1 Cross-compilation tools

The computer board will programmed in Ada, a systems programming language suitable for high-integrity applications. The GNAT cross-platform development system will be used to compile the software in the student's PC (aka. the host platform) so that it can be uploaded in the SMT32 board (the target platform).

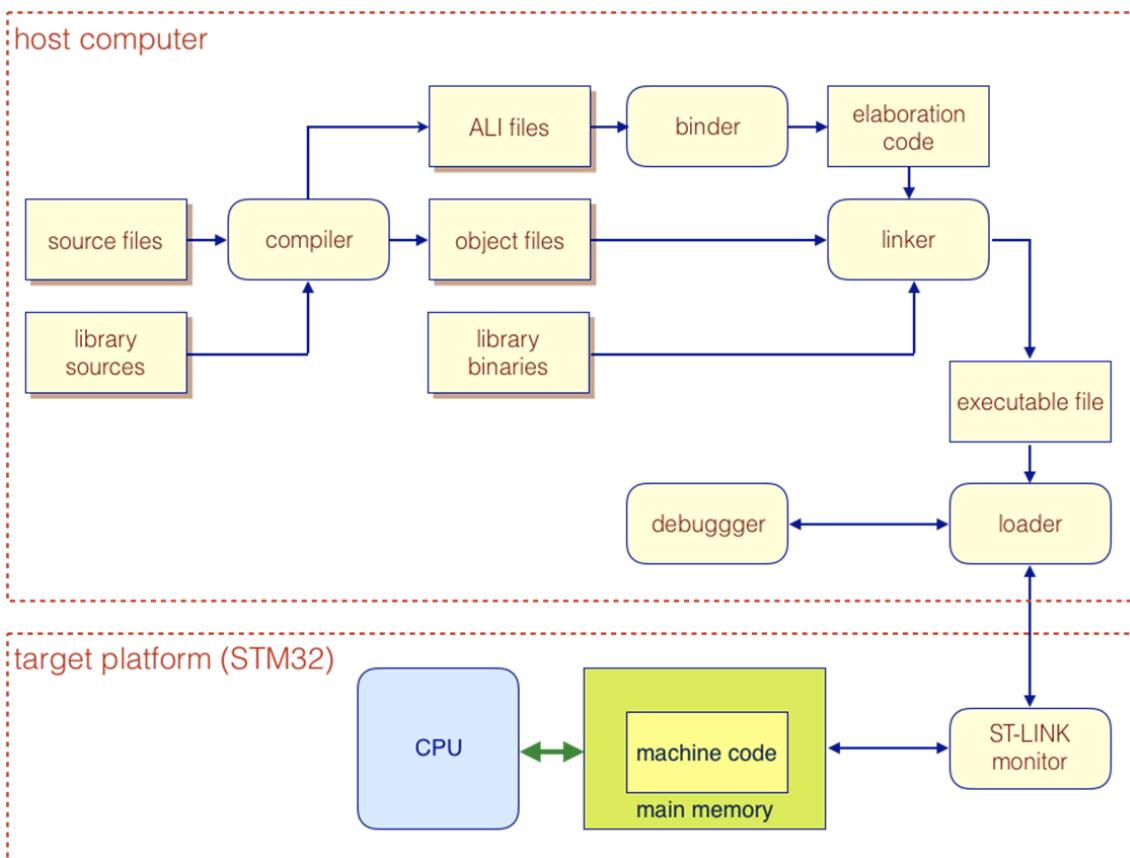


Figure 1.2: Cross-compilation and debugging system

In order to compile a program, the compilation chain is run on the host computer to produce an executable file suitable for the target computer. The executable is then loaded into the target memory, from where it can be executed. A monitor program is preinstalled on the target board that supports loading and debugging from the host platform.

1.2.2 Download and install GNAT ARM ELF

GNAT ARM ELF is the cross-compilation chain to be used with the STM32F4 board. It can be downloaded from the same page as the native GNAT system, and there are installation packages for Windows, MacOS-Intel and GNU Linux available. The file `README.txt` provides installation instructions, which are summarized as follows.

Windows

1. Select the platform ARM ELF (hosted on windows64) 2021, and download the file `gnat-2021-20210519-arm-elf-windows64-bin.exe`
2. Run the file and follow the instructions.
3. You will also need to install the USB driver for the ST-LINK probe. To do so, go to http://www.st.com/content/st_com/en/products/embedded-software/development-tool-software/stsw-link009.html, and click on Get Software. Click on Get Software under the Download column of the table that shows up to obtain the driver. You will need to accept ST Micro's license agreement and enter your contact details. Once downloaded unzip the USB device driver and run the installer, accepting all the defaults.

MacOS-Intel

1. Select the platform ARM ELF (hosted on darwin) 2021, and download the file `gnat-community-2019-20190517-arm-elf-darwin-bin.dmg`
2. Open the `dmg` disk and execute the application inside it. In order to circumvent the system protection, control-click on the file and then click on “open” in the emergent window.
3. You will also need the `st-util`, `st-flash`, and `st-info` tools. You can download the binaries from <https://github.com/texane/stlink/releases/download/1.3.0/stlink-1.3.0-macosx-amd64.zip>. Unzip and copy the files in the `bin` directory to a directory in your `PATH`. You may need to circumvent MacOS-Intel protection by executing the command:
`$ xattr -d com.apple.quarantine path-to-executable-file`

GNU Linux

1. Select the platform ARM ELF (hosted on linux) 2021, and download the file `gnat-2021-20210519-arm-elf-linux64-bin`
2. You will need to make the package executable before running it. In a command prompt, execute the following command:
`chmod +x path_to_the_package.bin`
and then execute the package.
3. You will also need to install the `stlink` tools. In Ubuntu and Debian `stlink` must be installed from the latest built packages available in the following GitHub repository (release v1.8.0).
`git clone https://github.com/texane/stlink.git`
`cd stlink`
`sudo ./script/install_stlink.sh`

The `README.txt` file contains additional installation and execution instructions.

1.2.3 Test your installation with an embedded program

The next activity is to compile and run a simple embedded program. This program is only intended to test that the compilation chain and the ST-LINK tools have been properly installed.

Open GPS and do the following:

1. Create a new project by clicking on `File → New Project ...` in the top menu. Choose the `STM32F compatible → LED demo` project template.
2. Choose a folder to deploy the project, e.g. `SEU-OBDH-Lab/lab-2`. Set the project name to `led_demo` and the main name to `main`. Then, a window with a project including a source files will be opened.

3. Right-click on the project icon on the left side area, and choose Project → Properties. On the emerging window, select Embedded and change the Connection tool selector to st-util. Save the settings.
4. Connect the STM32F4 board to the computer by means of a USB-A to mini-USB cable.
5. Build the executable and load it into the board by clicking on the  symbol in the tool bar (or select Build → Bareboard → Flash to board on the top menu). You should see a number of compilation-related messages ending with "Flashing complete. You may need to reset or cycle power".
6. If everything is all right, you will see the LEDs on the board blinking in a circular pattern.
7. Download and install the Ada Drivers Library The Ada Drivers Library is a set of Ada packages that make it easier to write software for embedded devices, including the STM32F4 microcontroller family and some demonstration boards. The source code can be found at https://github.com/AdaCore/Ada_Drivers_Library. To install the library, click on the green Clone or download button on the upper right side and then on Download Zip in the emerging window. You will get a zip archive in your downloads folder. Unzip the archive and move the resulting folder to your **SEU-OBHD-Lab** folder. Rename the folder to **Ada_Drivers_Library**, removing any trailing text.
8. Compile and run a test program with the Ada Drivers Library

Open GPS and do the following:

1. Select Open project on the welcome window. Navigate to
`.../SEU-OBHD-Lab/-Ada_Drivers_Library/examples/STM32F4_DISCO`
and open the project file named `blinky_f4disco.gpr`
2. **Important:** Update the project's runtime to full Ravenscar, as follows: Open "Edit" → "Project properties"; then, inside the "Build" → "Toolchain" → "Ada Runtime" option choose the `ravenscar-full-stm32f4 profile`.
3. Build the executable and load it into the board by clicking on the  symbol in the tool bar (or select Build → Bareboard → Flash to board on the top menu). When the loading is complete, you will see the board LEDS blinking all at the same time.

1.3 Install MATLAB™ and Simulink™

MATLAB and Simulink will be used to generate C code from a Simulink model and to validate the system by the Processor In the Loop (PIL) technique. The UPM has a campus license available for students. Please read this document to access and install MATLAB with the UPM's license.

Important: Please, consider the following notes:

- The complete installation of MATLAB, including add-ons, requires approximately 10 GB.
- Choose the Individual License, not the Concurrent.
- During the installation procedure, on the 3rd tab of **products**, install the following **add-ons**:
 - MATLAB
 - Simulink
 - MATLAB Coder
 - Simulink Coder
 - Aerospace Toolbox
- After the full installation, the **Embedded Coder** add-on must be installed.

Assignment 2

OBDH

The aim of this assignment is to experiment with a reduced version of an On-Board Data Handling (OBDH) system implemented on the UPMSat-2 microsatellite. The OBDH is typically implemented by software, which is also known as the On-Board Software (OBSW) of the satellite. In this assignment, the OBSW reads the MCU temperature using a temperature sensor that is embedded in the MCU and connected to one of its ADCs.

2.1 Software architecture

The software architecture of the OBSW is depicted in 2.1. The software components are:

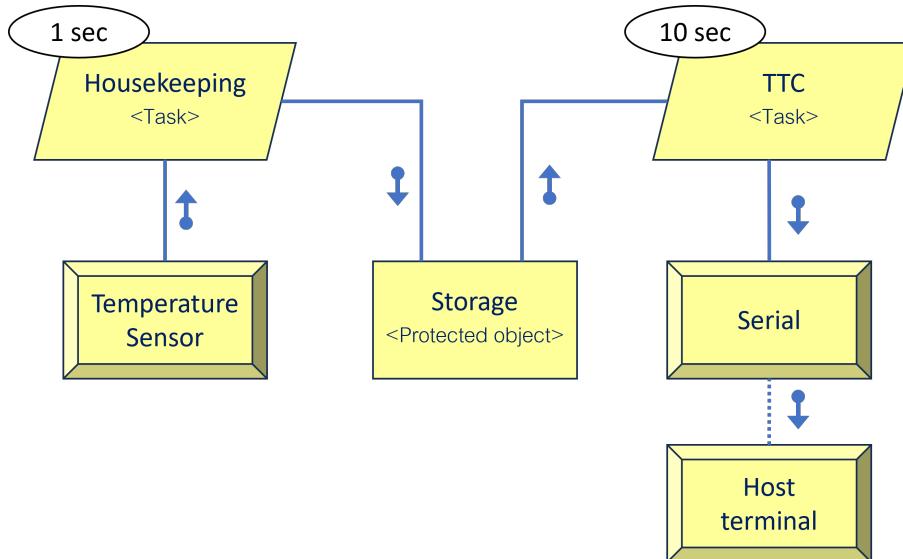


Figure 2.1: Software architecture of the OGSW.

Telemetry and Telecommand (TTC). This component is in charge of the communications with the ground station. The ground station is simulated by the Host terminal. the TTC must be activated cyclically with a 10 second period.

Serial. This component provides a high-level interface to a text console where the measured temperature values can be transmitted.

Housekeeping. Main component, which performs the basic functionality of the system. It reads a temperature value and stores it in the **Storage** component. It must be activated cyclically with a 1 second period.

Sensor. This component provides a high-level interface to the temperature sensor and deals with all the details of reading the ADC to which the sensor is connected.

Storage. This component is a data object storing one temperature value, which is written by Housekeeping and read by TTC. As it is accessed concurrently by two tasks, it must be a protected object guarantying the mutual exclusion.

Since the OBC board does not have a text output device, temperature values are sent by a serial line to the ground station. In this way, the radio link between the satellite and the ground station is simulated by the **Serial** and **Host Terminal** connection (dashed line).

2.2 Serial line connections

This scheme makes use of the USB/UART interface cable provided to the students. The USB/UART cable has a TTL connector that must be connected to the STM32f4 board pins that convey the serial line (UART) signals (figure 2.2).



Figure 2.2: UART cable connector.

The connections to be made are summarized in the following table (see figure 3 for the location of the pins on the board):

Connector pin	Board pin
1 (black)	GND
4 (orange)	PB7
5 (yellow)	PB6

Table 2.1: Serial line connections on board.

The other end of the interface cable has a USB-A connector that must be plugged to a USB port on the host computer. The values sent to the host computer are displayed using a terminal application that can handle a USB serial port. The host terminal application should be set to taking the USB serial port as input with a transmission rate of 115200 bps and a configuration of 8N1 (8 data bits, no bit parity, 1 bit for stop).

2.3 Host terminal application

2.3.1 Windows

The recommended application to display messages received on the USB serial port is PuTTY. You can download an installation package from <https://www.putty.org>.

In order to configure the application, you need first to identify the COM port corresponding to the USB serial line. Open the Device Manager and look at the USB Serial Port entry. The COM port is displayed next to it (e.g. COM4 in figure 2.3).

Now, to set up PuTTY, open the application and set the configuration parameters as shown in figure 2.2.

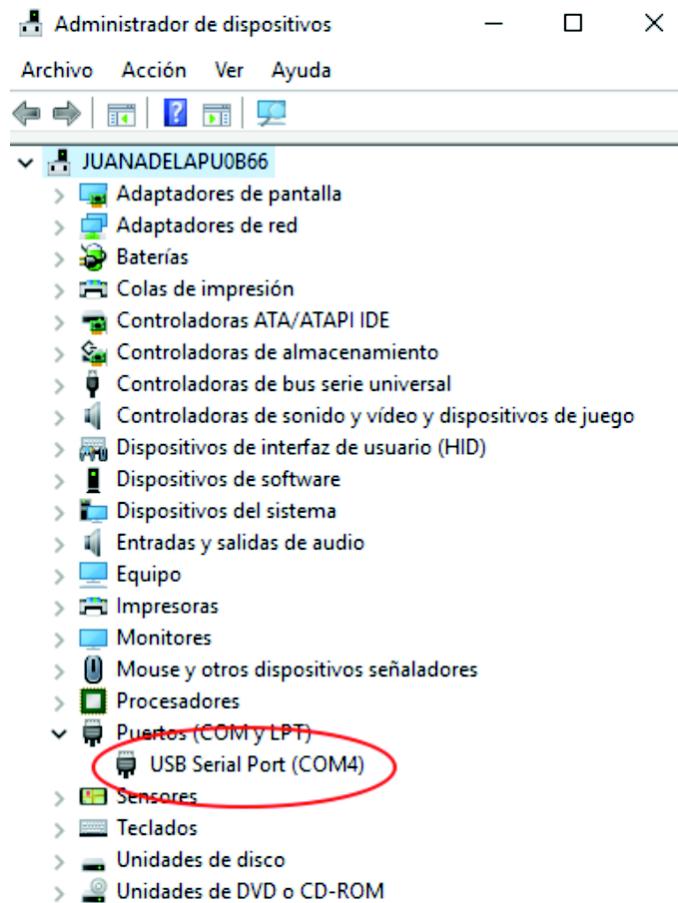


Figure 2.3: Identification of usb serial port.

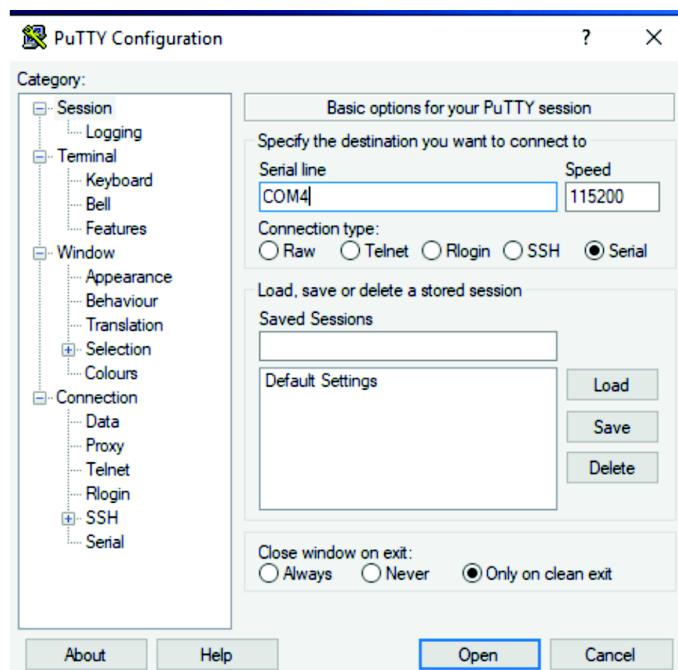


Figure 2.4: PuTTY configuration.

2.3.2 MacOS

The recommended application is screen, which is already installed in MacOS. First you have to identify the USB serial port. Open a terminal window and type

```
$ ls /dev | grep -i usb
```

You will get a list of devices like the following:

```
cu.usbserial-FTA5I24G
tty.usbserial-FTA5I24G
```

As you can see, there are two devices for each serial line. You can use any of them, but for reasons not to be discussed here it is better, in general, to use the one starting with cu.

To use the screen application enter the following command:

```
$ screen /dev/cu.usbserial-XXXX 115200
```

where /dev/cu.usbserial-XXXX is the name of your device.

To exit the application, type CTRL-A and then CTRL-K.

2.3.3 GNU Linux

The recommended application is screen,⁴ which can be installed in Ubuntu Linux with:

```
$ sudo apt install screen
```

In order to identify the USB serial port, type the following command on a terminal:

```
$ ls /dev | grep -i usb
```

You will get a result like the following:

```
ttyUSB0
```

To use the screen application enter the following command:

```
$ screen /dev/ttyUSB0 115200
```

To exit the application, type CTRL-A and then SHIFT-K.

2.4 Download the code and study the implementation

The implementation code, as initially provided to the students, can be downloaded from

<https://github.com/STR-UPM/SEU-OBDH-Lab>

You can clone the repository or download it as a zip archive. The code for this assignment is located in the **lab-3** folder.

The software components presented in the software architecture section (figure 2.1) are implemented in Ada as packages. Specifically, the **Housekeeping** package is the root element of the housekeeping component. Its specification consists of one procedure called **Initialize** that starts the operation of the component. The **Housekeeping** has four subpackages:

Housekeeping is the root package of the subsystem and contains a concurrent task, **Housekeeping_Task** that stores Data in Storage and toggles the blue LED every second.

Housekeeping.Data contains the definitions of the data types used in the subsystem. The data type **Analog_Data** is used to read the ADC, which are integers in the range 0 to 4095 ($2^{12}-1$) as directly provided by the 12-bit ADC. The data type **State** is a record that contains the ADC reading and the corresponding timestamp.

These ADC values have to be converted to engineering units. i.e. degrees Celsius, by following the specification of the temperature sensor (see appendix A). Unless the sensors readings must be used to control the satellite, raw readings are usually sent to ground station that is in charge of converting them to engineering units. This way, the OBSW is kept as simple as possible.

Housekeeping.Sensor is a low level component that contains the implementation details of the temperature sensor. Its specification includes the `Initialize` and `Get` procedures. This package uses the Ada Drivers Library to interact with the OBC board hardware.

Housekeeping.Images includes functions which are used to convert the temperature and timestamp values to Strings. These functions are called `Image` per the Ada language conventions, similar to `printf` in *nix environments.

The **TTC** package is the root of the telecommunications system, which in this version is greatly simplified with respect to a real application. It contains a concurrent task, `HK_Task`, which periodically obtains the sensor measurements located in the **Storage** and sends them to the ground station by using **Serial**. This activity is performed with a period of ten seconds. The task also toggles the orange LED for a visual inspection.

The **Storage** package implements the communication between the **Housekeeping** and **TTC** subsystems. Since this object is shared by two concurrent tasks, it is implemented as a *protected object*, so that its operations are executed in mutual exclusion. There is also *conditional synchronization*: the **TTC** task must wait until there is a fresh value in the store. However, **Housekeeping** should not wait if the previous values put into **Storage** have not been consumed, in order not to delay the housekeeping function. In this case, the stored value is overwritten. Notice that this differs from the classical specification of a bounded buffer.

The **Serial** component is implemented by the **Serial.IO** package and other packages in the **serial_ports** folder. These packages have been adapted from the examples in the Ada Drivers Library. The blocking kind of serial port was chosen for this project. This means that the task calling the `Put` operation (`TM_Task`) waits on a busy loop (aka. active wait) until the operation is complete.

The main procedure is **OBSW**. It calls **Housekeeping.Initialize**, which initializes the sensor so that **Housekeeping_Task** can proceed. Additionally, the green LED is toggled on and off to provide a visual check that the program is running.

Notice that `Run`, and hence `Initialize` and `OBSW`, never return. Therefore the program executes indefinitely, as is common in embedded systems. Also note that the Main task runs at the lowest priority level (`Main.ads:23: with Priority => System.Priority'First`) to let other components run when they are active.

2.5 Compile and run

Open GPS and do the following:

1. Select Open project on the welcome window. Navigate to the **SEU-OBDH-Lab/lab-3** directory and open the **realtime_housekeeping.gpr** project file.
 2. Build the executable and load it into the board by clicking on the  symbol in the tool bar (or select Build → Bareboard → Flash to board on the top menu).
- The program will be compiled, and the executable will be loaded into the board flash memory. After that, the program starts to run on the board (check the blinking LEDs).
3. Connect the serial cable to a USB port on the host computer, if not already done.
 4. Identify the serial port name on the host computer and launch the remote terminal application as explained in section 2.3. The sensor measured values together with their respective timestamps will start being displayed on the host application (figure 2.5).

```
0000000026:1060
0000000036:1060
0000000046:1061
0000000056:1060
0000000066:1061
0000000076:1060
0000000086:1062
0000000096:1061
0000000106:1060
0000000116:1062
```

Figure 2.5: Sample output on host terminal.

2.6 Make changes to the program

It is advised that you may make changes to the provided program in order to make sure that you understand the project implementation, and the mapping from the architecture to source code.

The proposed change is to: Include the conversion to Celsius in the `TTC.Send` procedure. The temperature transfer function is implemented by `HK_data-converter` which can be found in utilities.

2.7 Perform a temporal analysis of the system

Real-time systems need to fulfill temporal requirements, like activation patterns (cyclic/periodic or sporadic), the activation periods of the tasks, or guaranteeing the *schedulability* of the system. The latter ensures the *temporal behaviour* of the system, which means that *all tasks execute within their deadlines*. This is assured analytically by conducting a **Response-Time Analysis (RTA)**. To perform the RTA, you will need to measure the execution time of the task bodies and the protected procedure bodies. A simple loop technique using the standard real-time clock will be enough for this assignment.

An execution time measurement tool is available. To use it, follow these steps:

1. Open GPS and select Open project on the welcome window. Navigate to the `lab-3` directory and open the `wcet_meter.gpr` project file.
2. Build the executable and load into the board in the same way as for the `realtime_housekeeping.gpr` project, see section 2.5.
3. Make sure that the serial cable is still connected to the board and the USB port in the host computer. If the remote terminal application is not open, open it.

```
Start test no 1
HK ( 1000000 times) : 13.027373679 s
TC ( 1000000 times) : 26.069529321 s
ST
Put ( 1000000 times) : 2.561030637 s
Get ( 1000000 times) : 3.448276304 s
```

Figure 2.6: Output of wcet measurement tool.

A measurement test is executed on the board, and repeated every 60 s. The output of the test is shown on the host terminal application (figure 2.6). The output shows the execution times for the bodies of the Housekeeping (HK) and TTC (TC) tasks, as well as the bodies of the protected operations of the Storage object (ST). Notice that a new entry, `Get_Immediate`, has been added for the latter in order *to avoid the measuring task to get blocked*. The new entry is exactly the same as `Get` but has a True barrier so that it is always open.

In the example shown on figure 2.6, the HK execution time has been measured 10^6 times, with a total measurement time of 13.02 s. Therefore, the value to be taken for the response time analysis is $13.02 \cdot 10^{-6} \text{ s} = 13.02 \mu\text{s}$, and the same for the other tasks. Take into account that the values measured on your board will probably be slightly different from the above shown.

Once you have an estimate of worst case execution times, apply the RTA equations for computing the worst-case response time and check if all the deadlines are met. The setup for the calculations is shown on table 2.2 containing the period (T), execution time (C), blocking time (B), deadline (D), response time (R), and priority (P) of all tasks. The last two columns (Storage and Operation) present the protected object called `Storage` that is accessed with a mean execution time of $3 \cdot 10^{-6}$ seconds by the `Housekeeping` task through its `CPut` operation, and $4 \cdot 10^{-6}$ seconds by the `TTC` task through its `CGet` operation. Finally, the last row contains the ceiling priority of the `Storage` protected object.

Important: The temporal analysis (RTA) will be explained later in the course. Therefore, execution times should be stored for later.

Task	T	C	B	D	R	P	Storage	Operation
Housekeeping	1.0	$13 \cdot 10^{-6}$?	1.0	?	20	$3 \cdot 10^{-6}$	CPut
TTC	10.0	$26 \cdot 10^{-6}$?	2.0	?	10	$4 \cdot 10^{-6}$	CGet
Ceiling Priority							?	

Table 2.2: Data arrangement for RTA of the housekeeping system. Time units in seconds.

Assignment 3

OBDH with ACS

The aim of this assignment is to validate the Attitude Control System (ACS) with the Processor In the Loop (PIL) technique. PIL consists on simulation from the external environment through mathematical models (in Simulink) that are directly connected to the onboard computer. This way, we can analyze the system's behaviour based on simulated readings and actuations performed by the OBSW.

The reduced version of an OBSW is used with the ACS of the UPMSat-2 satellite. The ACS uses magnetic sensors (magnetometers) and actuators (magnetorquers). This system conforms to a magnetic attitude control (figure 3.1).

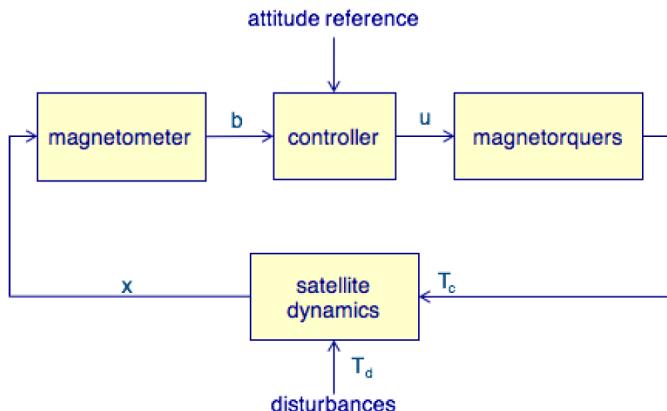


Figure 3.1: Magnetic attitude control system.

Magnetometers are magnetic sensors that provide a measurement of the strength and direction of the magnetic field, i.e. the magnetic field vector, at a given point. **Magnetorquers** are magnetic coil which produce a magnetic moment that interacts with the Earth's field, thus enabling the attitude of the satellite to be changed.

3.1 Model In the Loop (MIL) validation

Software validation usually includes testing the system under real operating conditions. However, for obvious reasons, on-board space software as well as many other embedded systems cannot be tested in this way. Simulation models are commonly used in these cases.

The first validation phase uses a model of the ACS, together with models of the space environment and the spacecraft dynamics, to assess the validity of the control law and the design parameters (figure 3.2). This is usually carried out by a control engineer using a simulation tool. Simulink is commonly used for ACS development.

The ACS Simulink model can be simulated by running MATLAB from the `lab-4/acs` directory and opening `ACS.slx`. Three new windows will pop-up: the Simulink window with the ACS model

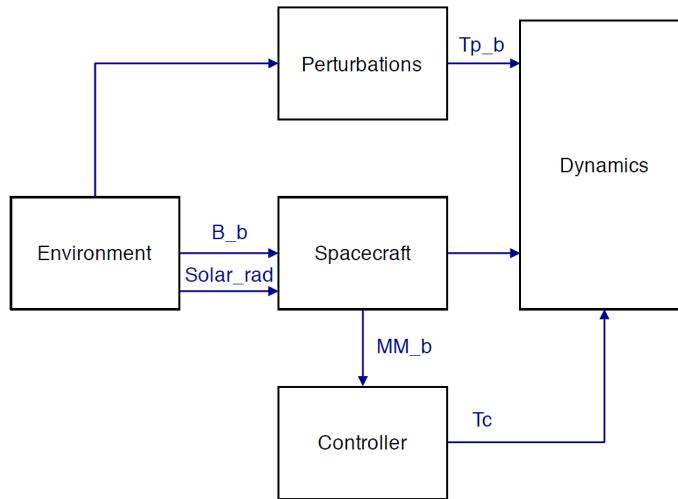


Figure 3.2: UPMSat-2 ACS high level model view.

and two scope windows that show the angular velocity of the satellite in body reference and the actuation over its three magnetorquers.

The Simulink window (figure 3.3) shows the high level blocks: the satellite's model is located in the middle (turquoise block), its dynamics and the models of the Earth's field and Sun with the perturbations.

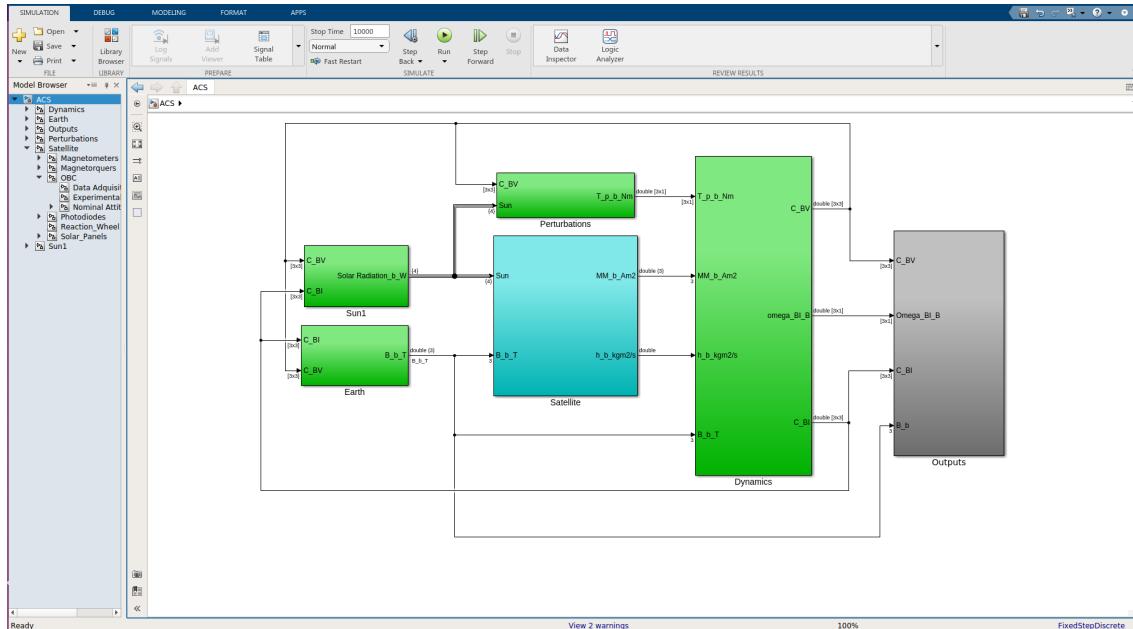


Figure 3.3: UPMSat-2 Simulink model.

The nominal attitude control can be visualized by selecting Nominal Attitude Control in the Model Browser menu (left part of simulink window) or by clicking on Satellite → OBC → Nominal Attitude Control blocks. The Nominal attitude control has three blocks (figure 3.4):

Sensor samples the analog inputs of the magnetometers. The inputs are converted to engineering units using calibration data.

Control implements the attitude control law that computes the control action to be output to the magnetorquers.

Actuator activates the magnetorquers according to the computed control action.

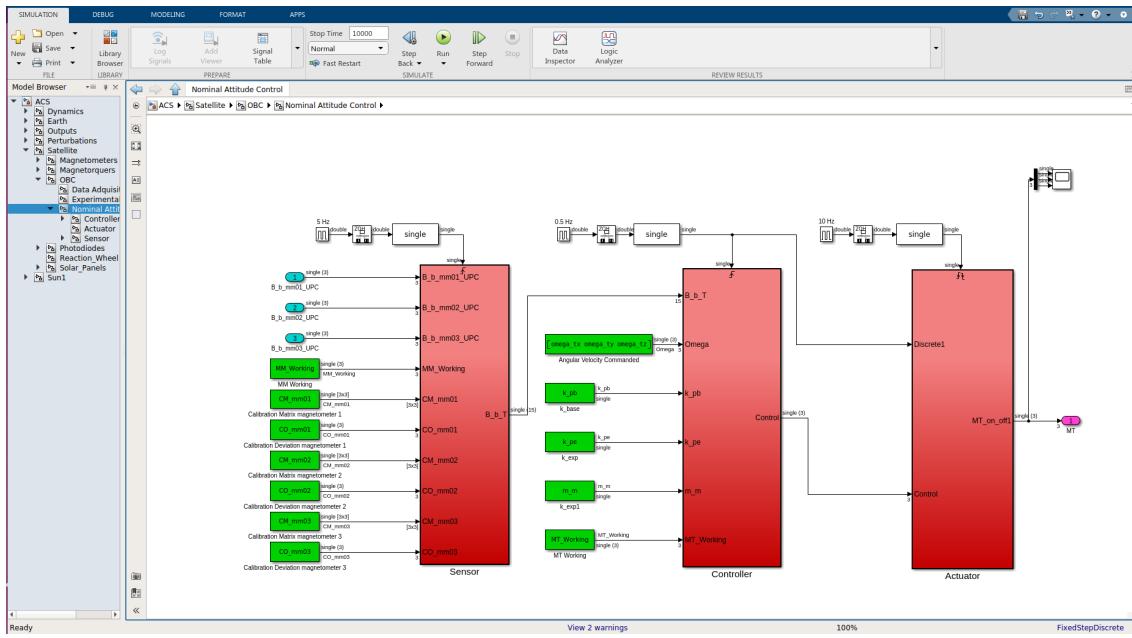


Figure 3.4: Nominal attitude control.

To simulate the model and verify its behaviour, click on Run bottom. The evolution of the angular velocity of the satellite and the actuation over the three magnetorquers will be shown in the corresponding scope windows. The commanded angular velocity set-point is $[0, 0, 0.1]$ rad/s and the result of the simulation (figure 3.5) shows the evolution from the initial angular velocity ($[0.1, -0.1, -0.1]$ rad/s).

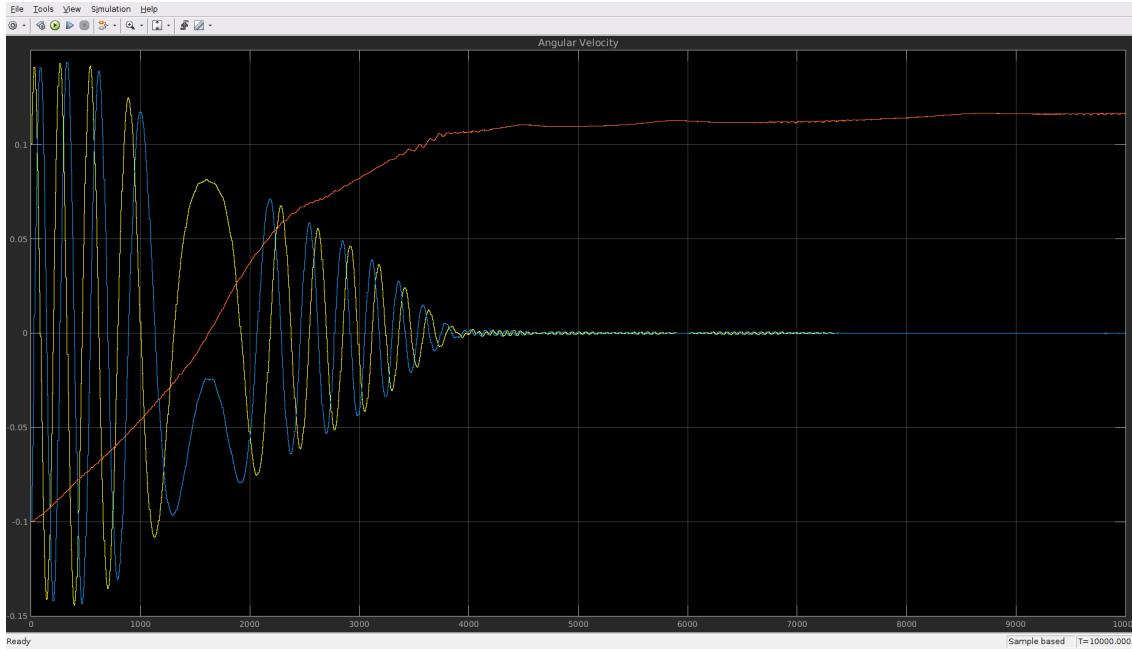


Figure 3.5: Angular velocity evolution.

3.2 Code generation

The next step is to execute the ADCS on the board. In this assignment, only the **Control** block will be executed on the target board. The corresponding code can be generated by using the

Embedded Coder toolbox but it is needed to isolate **Control** block from the ACS model. It can be done by clicking in the Control block, selecting all the block content (except the trigger block) and saving it in a new model.

This model named **control.slx** can be found in **lab-4/acs** directory. Open it with MATLAB and then select APPS in the top menu, Embedded Coder will appear. If not, it must be installed by clicking Get Add-Ons and searching it. The Embedded Coder window (figure 3.6) will appear after clicking on Embedded Coder icon.

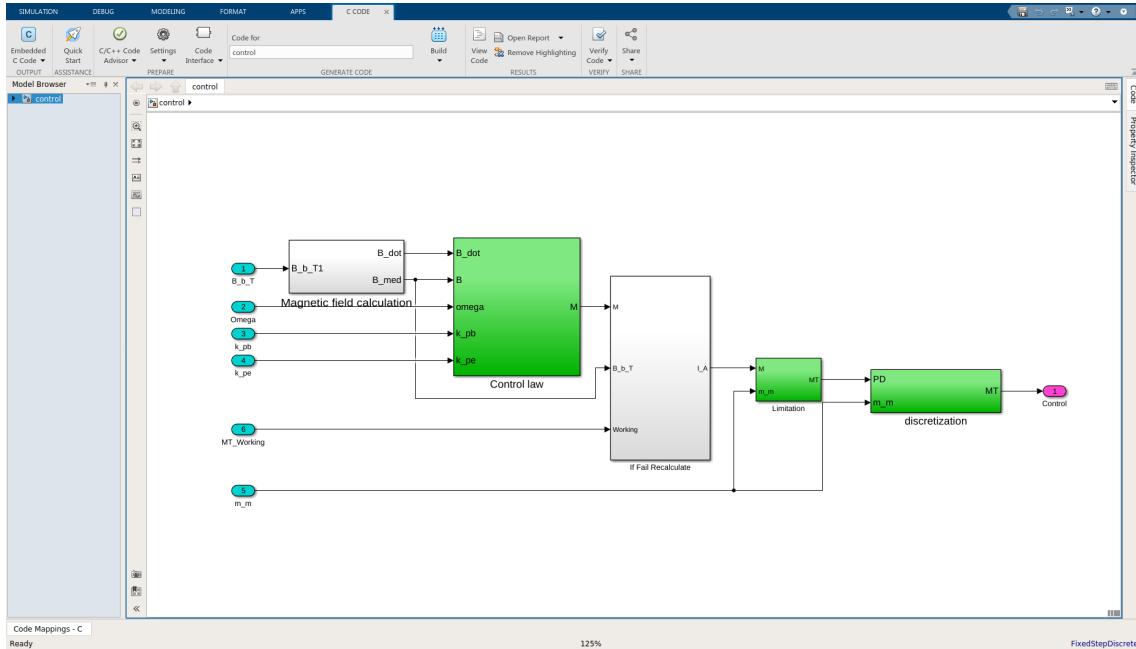


Figure 3.6: Embedded Coder toolbox.

The code generation option as well as characteristics of the target hardware can be set by clicking on the Settings menu. The **control.slx** model has already the proper options, therefore you can take a look but be carefully and do not modify them.

Now, the code can be generated by clicking on the Build menu. Once upon the code is generated, a code generation report window appears. It is possible to explore the generated code together with different code metrics. Click on **control.h** and look for lines 50-79 (figure 3.7) where the generated code interface is located.

There are two record type definitions (C structs) called **ExternalInputs** and **ExternalOutputs** that are used to interchange data with the blocks Sensor and Actuator (figure 3.4). Data are interchanged with two global variables: **rtU** and **rtY**. Moreover, function **control_initialize** initializes the control code and function **control_step** performs the control algorithm. The generated code will be embedded in the OBSW by taking into account this interface.

3.3 Software architecture

The implementation code, as initially provided to the students, can be downloaded from <https://github.com/STR-UPM/SEU-OBDH-Lab/ass-3>.

```

50
51 /* External inputs (root import signals with default storage) */
52 typedef struct {
53     real32_T B_b_T[15];           /* <Root>/B_b_T' */
54     real32_T Omega[3];            /* <Root>/Omega' */
55     real32_T k_pb;                /* <Root>/k_pb' */
56     real32_T k_pe;                /* <Root>/k_pe' */
57     real32_T m_m;                /* <Root>/m_m' */
58     real32_T MT_Working[3];      /* <Root>/MT_Working' */
59 } ExternalInputs;
60
61 /* External outputs (root outports fed by signals with default storage) */
62 typedef struct {
63     real32_T Control[3];          /* <Root>/Control' */
64 } ExternalOutputs;
65
66 /* Block signals and states (default storage) */
67 extern D_Work rtDWork;
68
69 /* External inputs (root import signals with default storage) */
70 extern ExternalInputs rtU;
71
72 /* External outputs (root outports fed by signals with default storage) */
73 extern ExternalOutputs rtY;
74
75 /* Model entry point functions */
76 extern void control_initialize(void);
77 extern void control_step(void);
78
79 /*
...

```

Figure 3.7: Code generation report.

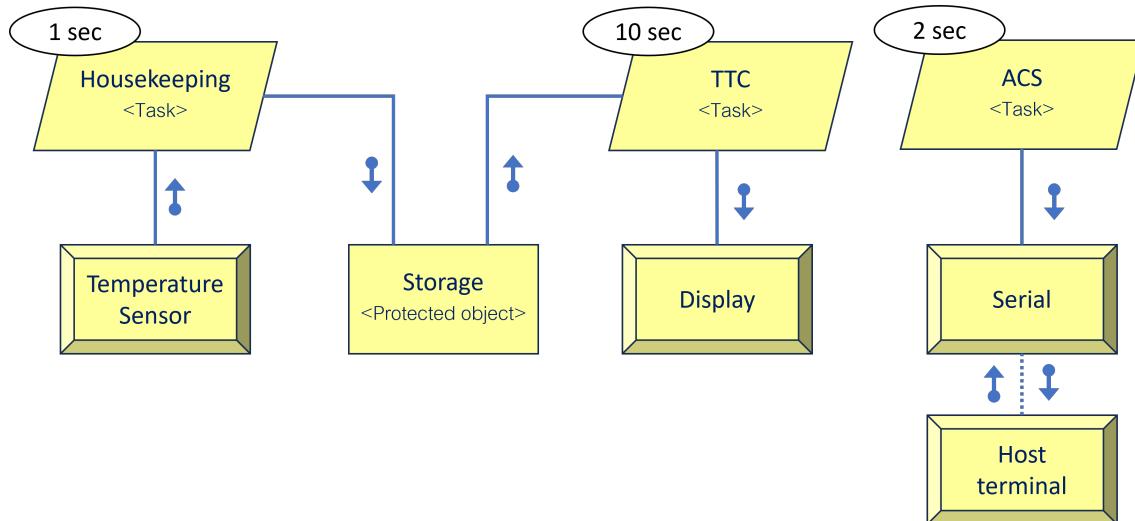


Figure 3.8: Software architecture of OBSW with ACS.

The software architecture of the OBSW with ACS is depicted in figure 3.8 and the differences with the previous architecture (figure 2.1) are:

The ADCS package is the root element of the ADCS. Its specification consists of one procedure, Initialize, that starts the operation of the component. It has three subpackages:

ADCS is the root package of the subsystem and contains a concurrent task, ADCS_Task that reads the magnetic field vector, calculates the torque vector and sends it to magnetorquers. It also toggles the red LED every two second. The body of this package uses the generated code by setting the inputs, calling the functions and retrieving the outputs following the interface of figure 3.4. ADCS_Task performs the control algorithm by calling `control_step`. This is shown in figure 3.9. It uses Export and Import pragmas to interface the generated C code.

```

23 package body ADCS is
24
25     -- controller block input and output objects must be global
26     Control_Input : Controller_Input;
27     pragma Export (C, Control_Input, "rtU");
28
29     Control_Output : Controller_Output;
30     pragma Export (C, Control_Output, "rtY");
31
32     -- Initialize the ADCS subsystem
33     procedure Initialize is
34         procedure Control_Initialize;
35         pragma Import (C, Control_Initialize, "control_initialize");
36     begin
37         Control_Initialize;
38     end Initialize;
39
40     -- ADCS task
41     task body ADCS_Task is
42         procedure Control;
43         pragma Import (C, Control, "control_step");
44         Next_Time   : Ada.Real_Time.Time := Ada.Real_Time.Clock + Initial_Delay;
45     begin
46         loop
47             delay until Next_Time;
48             Control_Input.B_b_T := ADCS.HW.Get;
49             Control;
50             ADCS.HW.Put (Control_Output.Control);
51             STM32.Board.Red_LED.Toggle;
52             Next_Time := Next_Time + Period;
53         end loop;
54     end ADCS_Task;
55 end ADCS;

```

Figure 3.9: Implementation of ACS package.

ADCS.Parameters contains the definitions of the data types used in the subsystem and parameters that are used to tune the control algorithm. These parameters can be changed by telecommand in the UPMSat-2 OBSW. The data type `Controller_Input` is used to read the magnetic field vector in teslas, which are IEEE single precision float numbers. The data type `Controller_Output` are used to send the actuation to magnetorquers in newton-meters, which are IEEE single precision float numbers. These data types correspond to the generated C structs `ExternalInputs` and `ExternalOutputs` (see figure 3.7), taking advantage of the C and Ada languages interoperability.

ADCS.HW is in charge of getting the magnetic field vector and putting the torques. It hides the details of the hardware. Its specification includes the `Put` and `Get` subprograms. This package uses the serial port to interchange magnetic field and torque values with the Software Validation Facility (SVF).

The SVF is an auxiliary computer (the student's PC), linked to the OBC by a serial line, to run a simulation model of the Earth's magnetic field and satellite dynamics. In this way, engineering values (Nm and T) can be interchanged.

Host computers are also used as SVF by executing a Simulink™ model of the Earth's magnetic field and satellite dynamics.

In this assignment, the serial line is used to interchange data between ADCS and SVF. Therefore, housekeeping telemetry are send to a text console that is simulated on the host computer, using a mechanism called semihosting. When the target board is connected to the host by means of the ST-LINK USB cable, and the embedded program is run using the debugger in the host, the standard output is re-directed to the debugger console. The GPS environment supports semihosting.

3.4 Compile and run with the debugger.

Open GPS and do the following:

1. Select Open project on the welcome window. Navigate to the OBSD directory and open the realtime_housekeeping.gpr project file.
2. Build the executable and load it into the board by clicking on the  symbol in the tool bar (or select Build → Bareboard → Debug on board on the top menu).

The program will be compiled, and the executable will be loaded into the board memory by the debugger. The debugger console (lowest window in GPS) shows the following lines:

```
...
(gdb) monitor reset halt
(gdb)
```

3. Type continue or just c on the debugger console (or select Debug → Continue on the top menu).

```
(gdb) c
Continuing.
[program running]
```

After that, the program starts to run on the board and temperature reads are displayed on Messages tab of the debugger window. However, the Red LED does not blink because ACS is waiting sensor inputs from SVF.

3.5 Processor In the Loop (PIL) validation

The SVF shall provide sensor inputs and retrieve magnetotorquer outputs. To do that, the remain part of the original simulink model will be used, i.e. all the blocks except the Sensor block.

This model named ACS_PIL.slx can be found in [lab-4/acs-pil](#) directory. Open it and again three new windows will be pop-up: the Simulink window with the ACS_PIL model and two scope windows that show the angular velocity of the satellite in body reference and the actuation over the three magnetotorquers.

The Control block of this model has been substituted by serial link connections as shown in figure 3.10. Identify the serial port name on the host computer and edit the serial configuration block by selecting the serial line of your PC.

Additional rate transition blocks has been added for a proper communication and a Real-Time Pacer block has been also added to set the simulation speed. In a real case, an speedup equal to 1 should be used but it is to slow.

Start the simulation and verify angular velocity stabilization. Now ADC runs and red LED is toggled.

3.6 Make changes to the simulation

- Default parameters for control blocks can be changed in Ada source file ACS-parameters.ads.
 - `Default_omega` is the consigned angular velocity.
 - `Default_MT_Working` contains the operational magneto-torques.
- In UPMSat-2 many parameters can be changed by TC.
- The initial angular velocity can be changed in Simulink source file initialization.m.
 - `omega_BLB0 = [0.1;-0.1;-0.1];`

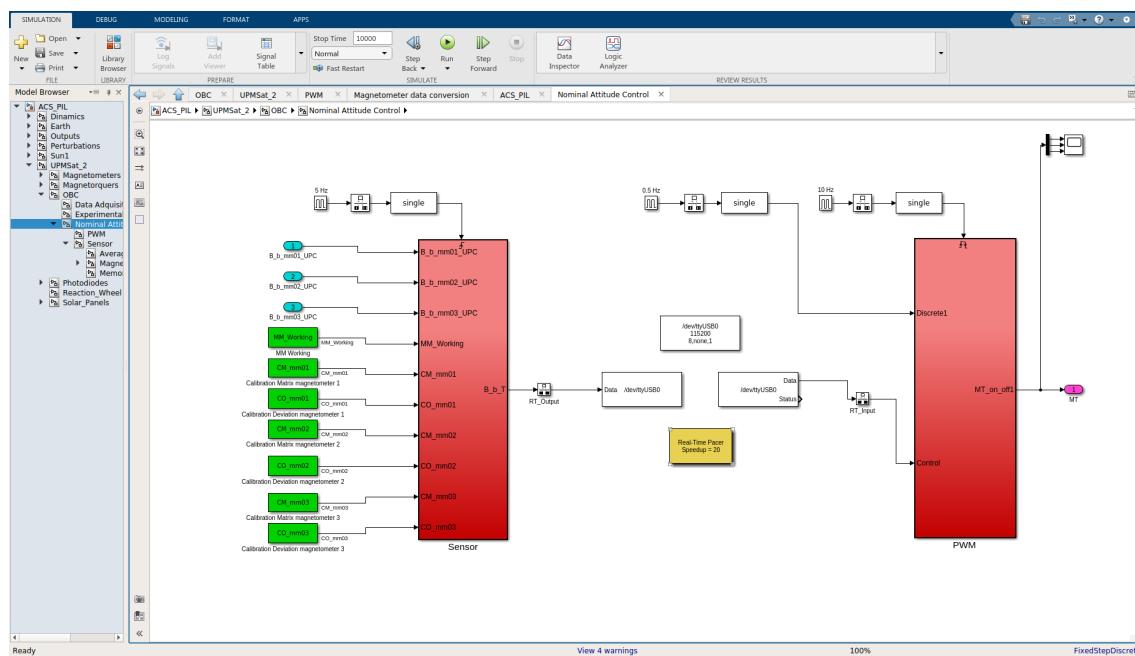


Figure 3.10: Nominal attitude control for PIL.

Assignment 4

Documented report

Students have to write a brief report of about 3-5 pages on the work carried out during the session. This report **must** include the following:

- An analysis about cross-development environment for embedded systems and the main differences with a native environment.
- The schedulability analysis proposed in section 2.7.
- The results from the ACS PIL simulation as shown in figure 3.5.
- The report can contain recommendations to improve the assignment and personal opinions.
- A description of the changes proposed in the assignments (or any other), including the source code with comments.

All sections are mandatory, and the report must be uploaded in the designated assignment on the Moodle platform.

Appendix A

Temperature sensor

The STM32F407 reference manual (section 13.10) states that the internal temperature sensor of the MCU is internally cabled to the ADC1_IN16 analog input channel. The steps required to read the sensor are:

1. Select ADC1_IN16 input channel in the ADC.
2. Select a sampling time greater than the minimum sampling time specified in the datasheet (see table A.1 below).
3. Set the TSVREFE bit in the ADC_CCR register to wake up the temperature sensor from power down mode.
4. Start the ADC conversion by setting the SWSTART bit (or by external trigger).
5. Read the resulting VSENSE data in the ADC data register.
6. Calculate the temperature using the following formula:

$$\text{Temperature (in } ^\circ\text{C)} = (\text{VSENSE} - \text{V25}) / \text{Avg_Slope} + 25$$

Where:

- V25 = VSENSE value for 25 °C (table A.1)
- Avg_Slope = average slope of the temperature vs. VSENSE curve (table A.1).

The sensor has a startup time after waking from power down mode before it can output VSENSE at the correct level. The ADC also has a startup time after power-on, so to minimize the delay, the ADON and TSVREFE bits should be set at the same time.

The sensor has a range of -40 to 125 °C, with a precision of ±1.5 °C. Its main characteristics are described in the STM32F407 datasheet (table A.1).

Symbol	Parameter	Min	Typ	Max	Unit
TL	VSENSE linearity with temperature	-	±1	±2	°C
Avg_Slope	Average slope	-	2.5		mV/°C
V25	Voltage at 25 °C	-	0.76		V
tSTART	Startup time	-	6	10	μs
TS_temp	ADC sampling time when reading the temperature (1 °C accuracy)	10	-	-	μs

Table A.1: STM32F407 temperature sensor characteristic.

The Ada Drivers Library includes the package STM32.ADC, which provides facilities for handling the analog to digital converter.