# **SUGASHINI T**

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#### **PROFILE**

Passionate about developing innovative solutions to enhance systems and processes. With a strong foundation in programming and design, I aim to contribute to your team and support the company's growth while continuously learning and exploring new technologies

EDUCATION	
ME – VLSI Design, K. Ramakrishnan College of Technology	2021 – 2023
Anna university , CGPA : 9.57	India
BE - ELECTRONICS & COMMUNICATION, K. Ramakrishnan College of Engineering Anna university , CGPA : 8.41	2016 – 2020 India
<b>HSC- State Board,</b> Brindavan Higher Secondary School	2015 – 2016
Percentage : 88%	India

# SKILLS

Languages	Databases	Frameworks	<b>Tools and Software</b>
• C	• SQL	<ul> <li>BOOTSTRAP</li> </ul>	<ul> <li>XILINX</li> </ul>
• CSS3	<ul> <li>MYSQL</li> </ul>		<ul> <li>TANNER</li> </ul>
• HTML5	<ul> <li>MONGODB</li> </ul>		<ul> <li>MODELSIM</li> </ul>
<ul> <li>VERILOG</li> </ul>			<ul> <li>MICROWIND</li> </ul>
<ul> <li>JAVASCRIPT</li> </ul>			<ul> <li>MS OFFICE</li> </ul>
<ul> <li>SYSTEM VERILOG</li> </ul>			

### **PROJECTS**

# IMPLEMENTATION OF AREA EFFICIENT APPROXIMATE KARATSUBA USING WALLACE TREE MULTIPLIER.

• Designed Wallace tree multiplier using XOR gate and carry-in prediction techniques to reduce area, power and delay

### IRIS RECOGNITION USING DCNN.

 Developed a DCNN algorithm for semantic segmentation to determine time interval and identify matching iris samples, useful in forensic analysis 2023

2020

#### **INTERNSHIP**

Embedded system Intern, Pantech Protech Ltd | 2020.

• Gained hands-on experience in developing embedded system and applications.

#### **ACHIVEMENTS**

- Secured third place as the class topper in the fifth semester | 2018-2019.
- Achieved 69%(ELITE) for the course "System design through Verilog" in NPTEL | 2022.

#### **AREA OF INTEREST**

- ASIC Design
- Digital IC Design
- Digital Electronics
- Front End Developer

#### **CERTIFICATES**

- Workshop on Intel FPGA for Embedded, AI/ML and heterogeneous application | 2023.
- Workshop on FPGA Design Using Xilinx, Vivado and Mentor Graphics | 2023.
- Diploma in PHP Full Stack Developer | 2023.
- Course completion in C, C++, PYTHON programming | 2021.
- Mini project contest "EINSTACTOMIA" | 2019
- Three day Workshop on Robotics using Arduino | 2018.

#### **CAPABILITIES**

- Skilled in managing demanding workloads and deadlines.
- Collaborative team player.
- Eagerness to learn new things.

#### **LANGUAGES**

- English
- Tamil

#### **DECLARATION**

I hereby declare that the above mention details about me are true to the best of my knowledge and belief.

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