

B.Tech II Year II Semester (R09) Supplementary Examinations May/June 2016

**COMPUTER ORGANIZATION**

(Common to CSE & ECC)

Time: 3 hours

Max. Marks: 70

Answer any FIVE questions

All questions carry equal marks

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- 1 Explain about following floating point representations:
  - (a) Single-precision format.
  - (b) Double-precision format.
- 2
  - (a) Explain the setup involved in instruction execution using state diagram.
  - (b) Explain the various addressing modes of an instruction.
- 3
  - (a) With the help of a diagram, explain the computer hardware configuration.
  - (b) Describe briefly about fetch routine.
- 4 Explain how multiplication can be implemented using Booth's algorithm.
- 5
  - (a) List the specifications of floppy disks and magnetic disks.
  - (b) Explain disc access time and SCSI bus.
- 6
  - (a) Explain in detail the data communication over telephone lines.
  - (b) Discuss about error detection.
- 7 With the help of flowchart, explain the processor with multiple functional units.
- 8
  - (a) Discuss on system bus. What are the different ways through which data transfer can be carried out?
  - (b) Explain the timing diagram for synchronous input and output transfer.

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