Chapter 1

Computer Abstractions and Technology

Classes of Computers

Personal computers

- General purpose, variety of software
- Subject to cost/performance tradeoff
- Server computers (=> cloud computing)
 - Network based
 - High capacity, performance, reliability
 - Range from small servers to building sized

Supercomputers

- High-end scientific and engineering calculations
- Highest capability but represent a small fraction of the overall computer market
- Embedded computers
 - Hidden as components of systems
 - Stringent power/performance/cost constraints





Notations Used in Textbook

• Figure 1.1

Decimal term	Abbreviation	Value	Binary term	Abbreviation	Value	% Larger
kilobyte	KB	10 ³	kibibyte	KiB	210	2%
megabyte	MB	10 ⁶	mebibyte	MiB	220	5%
gigabyte	GB	10 ⁹	gibibyte	GiB	230	7%
terabyte	TB	10 ¹²	tebibyte	TiB	240	10%
petabyte	PB	1015	pebibyte	PiB	250	13%
exabyte	EB	1018	exbibyte	EiB	260	15%
zettabyte	ZB	1021	zebibyte	ZiB	270	18%
yottabyte	YB	1024	yobibyte	YiB	280	21%

Processor Cores

What You Will Learn

- How programs are translated into the machine language
 - And how the hardware executes them.
- The hardware/software interface
 - How software can instruct hardware
- What determines program performance
 - And how it can be improved
- How hardware designers improve performance
- Also, multicore, GPU, and parallel processing

Understanding Program Performance

- Algorithm
 - Determines number of operations executed
- Programming language, compiler, architecture
 - Determine number of machine instructions executed per operation
- Processor and memory system
 - Determine how fast instructions are executed
- I/O system (including OS)
 - Determines how fast I/O operations are executed
- Check Yourself!

Great Ideas in Computer Architecture

- Design for Moore's Law
- Use abstraction to simplify design
- Make the common case fast
- Performance via parallelism,
 pipelining,

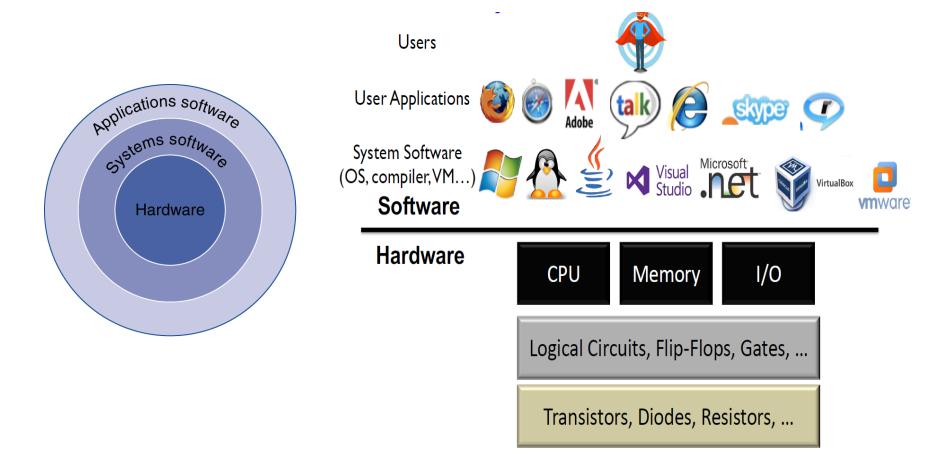
Hierarchy of memories

prediction

Dependability via redundancy



Below Your Program



Levels of Program Code

- High-level language
 - Level of abstraction closer to problem domain
 - Provides for <u>productivity</u> and <u>portability</u>

sort(), printf()

- Assembly language
 - Textual and symbolic representation of <u>instructions</u> add, sub
- Machine code (object code or binary)
 - Binary digits of instructions and data

```
High-level
                       swap(int v[], int k)
language
                       {int temp:
program
                          temp = v[k];
(in C)
                          v[k] = v[k+1]:
                          v[k+1] = temp;
                         Compiler
Assembly
                      swap:
language
                             muli $2, $5,4
                                  $2, $4,$2
program
(for MIPS)
                                   $15.0($2)
                                   $16, 4($2)
                                   $16, 0($2)
                                   $15, 4($2)
                             SW
                             .i r
                                   $31
                         Assembler
```

Binary machine language program (for MIPS)

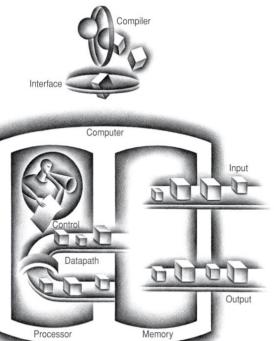
What kinds of data do we need to manipulate?

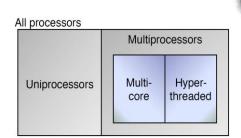
- Instructions
 - operations performed by computer
- Numbers
 - integers, floating point, complex, rational, irrational, ...
- Logical
 - true, false
- Text
 - characters, strings, …
- Images
 - pixels, colors, shapes, ...
- Sound

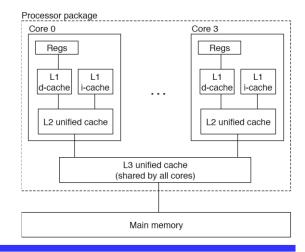
Components of a Computer

- Same components for all kinds of computer
 - Desktop, server, embedded

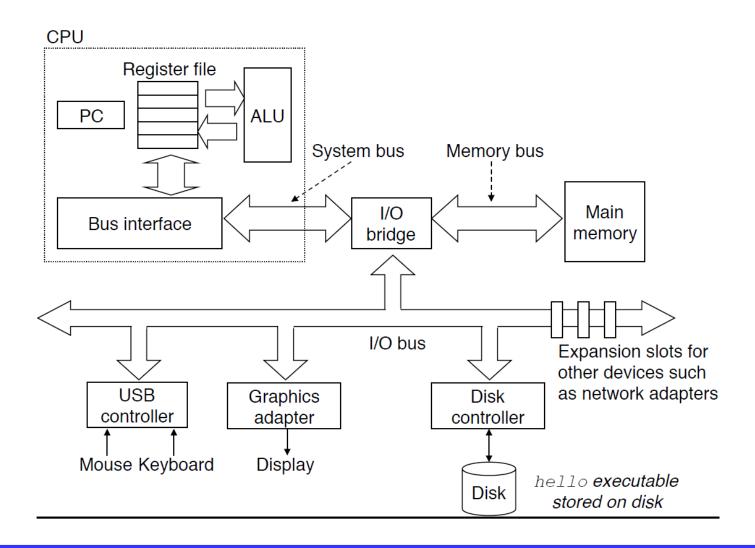
performance





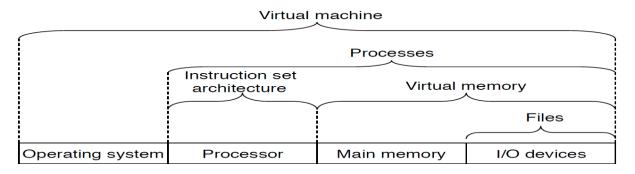


Hardware Organization of a Typical System



Abstractions

- Abstraction helps us deal with complexity
 - Hide lower-level detail
- Instruction set architecture (ISA)
 - The hardware/software interface
- Application binary interface
 - The ISA plus system software interface
- Implementation
 - The details underlying and interface



Performance: Response Time and Throughput

- Response time
 - How long it takes to do a task
- Throughput
 - Total work done per unit time
 - e.g., tasks/transactions/... per hour
- How are response time and throughput affected by
 - Replacing the processor with a faster version?
 - Adding more processors?
- We'll focus on response time for now...

Relative Performance

- Define Performance = 1/Execution Time
- "X is n time faster than Y"

Performance_X/Performance_Y

- = Execution time $_{Y}$ /Execution time $_{X} = n$
- Example: time taken to run a program
 - 10s on A, 15s on B
 - Execution Time_B / Execution Time_A
 = 15s / 10s = 1.5
 - So A is 1.5 times faster than B

Measuring Execution Time

Elapsed time

- Total response time, including all aspects
 - Processing, I/O, OS overhead, idle time
- Determines system performance

CPU time

- Time spent processing a given job
 - Discounts I/O time, other jobs' shares
- Comprises user CPU time and system CPU time
- Different programs are affected differently by CPU and system performance
- e.g. We are executing 'prog.sh' along with 'time' command.

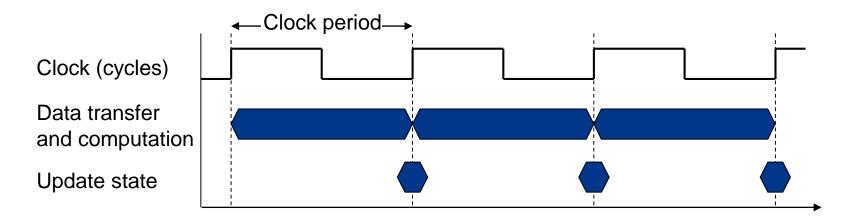
\$ time ./prog.sh Real 0m8.803s

Initiating merge User 0m0.010s

Merge completed Sys 0m0.000s Check Yourself! (pp. 33)

CPU Clocking and CPU Time

Operation of digital hardware governed by a constant-rate clock



CPU Time = clock cycle x clock cycle time
 IC x CPI x clock cycle time

Instruction Count for a program (IC)

Determined by program, ISA and compiler

Average cycles per instruction (CPI)

Determined by CPU hardware

If different instructions have different CPI

Average CPI affected by instruction mix

Comparing Code Segments (pp. 37)

 Alternative compiled code sequences using instructions in classes A, B, C

Class	А	В	С
CPI for class	1	2	3
IC in sequence 1	2	1	2
IC in sequence 2	4	1	1

- Sequence 1: IC = 5
 - Clock Cycles= 2×1 + 1×2 + 2×3= 10
 - Avg. CPI = 10/5 = 2.0

- Sequence 2: IC = 6
 - Clock Cycles= 4×1 + 1×2 + 1×3= 9
 - Avg. CPI = 9/6 = 1.5

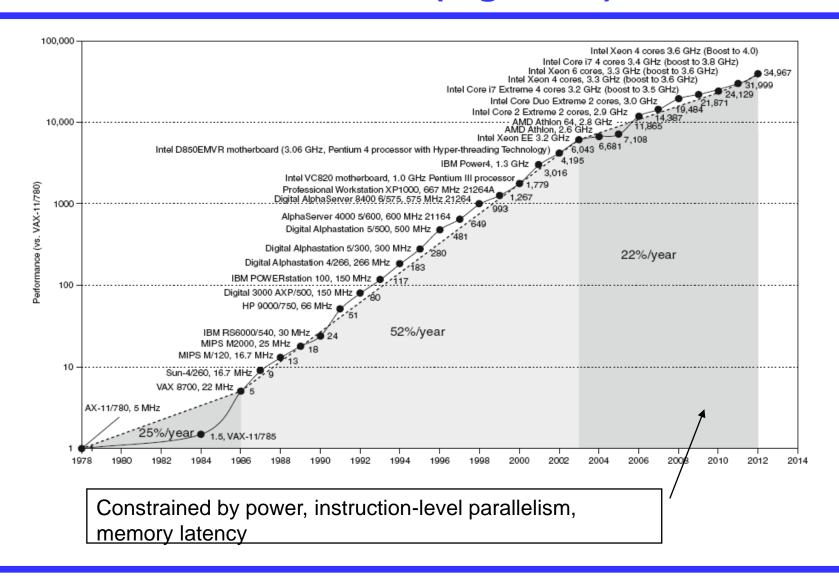
Performance Summary

The BIG Picture: Performance Equation(pp. 38)

$$CPU Time = \frac{Instructions}{Program} \times \frac{Clock \ cycles}{Instruction} \times \frac{Seconds}{Clock \ cycle}$$

- Performance depends on (pp. 39)
 - Algorithm: affects IC, possibly CPI
 - Programming language: affects IC, CPI
 - Compiler: affects IC, CPI
 - Instruction set architecture: affects IC, CPI, T_c
- Check Yourself! (pp. 40)

Processor Performance (Fig. 1. 17)



Multiprocessors

- Multicore microprocessors
 - More than one processor per chip

- Requires explicitly parallel programming
 - Compare with instruction level parallelism
 - Hardware executes multiple instructions at once
 - Hidden from the programmer
 - Hard to do
 - Programming for performance
 - Load balancing
 - Optimizing communication and synchronization

SPEC and **TPC**

- Programs used to measure performance
 - Supposedly typical of actual workload
- Standard Performance Evaluation Corp(SPEC)
 - https://www.spec.org/Figure 1. 18
 - Embedded System : https://www.eembc.org

EEMBC develops performance benchmarks for the hardware and software used in autonomous driving, mobile imaging, the Internet of Things, mobile devices, and many other applications. The EEMBC community includes member companies, commercial licensees, and academic licensees at institutions of higher learning around the world.

- Transaction Processing Performance Council (TPC)
 - Define <u>transaction processing</u> and <u>database</u> benchmarks
 - http://www.tpc.org/



Standard Performance Evaluation Corporation

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Benchmarks

- Cloud
- **CPU**
- Graphics/Workstations
- ACCEL/MPI/OMP
- Java Client/Server
- Mail Servers
- Storage
- Power
- Virtualization
- Web Servers
- Results Search
- Submitting Results Cloud/CPU/Java/Power SFS/Virtualization ACCEL/MPI/OMP SPECapc/SPECviewperf/SPECwpc

Tools

The Standard Performance Evaluation Corporation (SPEC) is a non-profit corporation formed to establish, maintain and endorse standardized benchmarks and tools to evaluate performance and energy efficiency for the newest generation of computing systems. SPEC develops benchmark suites and also reviews and publishes submitted results from our member organizations and other benchmark licensees.

What's New:

08/03/2020: The 12th ACM/SPEC International Conference on Performance Engineering, ICPE 2021, is set to be held April 19-23, 2021, in Rennes, France.

07/03/2020: SPEC is calling for nominations for the Kaivalya Dixit Distinguished Dissertation Award 2020. The program is open to dissertations that have been defended between October 2019 and September 2020. Deadline for submissions is September 30, 2020.

06/03/2020: The serverless computing paradigm delivers major benefits for cloud applications — low-cost, fine-grained deployment, and management-free operation. Read more in this SPEC Research Group report.

Summary

Amdahl's Law (pp. 49)

Performance Equation

MIPS (Million Instructions Per Second)

SPEC and TPC