

**UM1934** 

**CAENComm User & Reference Manual** 

Rev. 9 – October 1st, 2021

## **Purpose of this User Manual**

This User's Manual contains the full description of the CAENComm library (Windows®/Linux®).

## **Change Document Record**

Date	Revision	Changes			
Previous releases of the document are not available					
Jul 13 <sup>th</sup> , 2012	05	New graphical layout and added Chap. 4			
Jan 15 <sup>th</sup> , 2014	06	Removed "Preliminary".			
		Modified CAEN_Comm_ConnectionType removing PCI/PCIE options replaced with OpticalLink.			
		Added CAENComm_VMELIB_handle in CAENCOMM_INFO			
		Added message for developers in Sec. System requirements &			
		installation setup			
Apr 6 <sup>th</sup> , 2021	07	Added support to V3718 Bridge and A4818 Adapter. Added Chap. 5.			
		Updated Sec. CAENComm_ConnectionType, Sec.			
		CAENComm_ErrorCode, Sec. CAENComm_OpenDevice			
Sep 1 <sup>st</sup> , 2021	08	Added support to V4718 Bridge. Inserted Sec.			
		CAENComm_OpenDevice2.			
Oct 1st , 2021	09	Added LabVIEW VI in Sec. CAENComm_OpenDevice2. Updated			
		Chap. <b>4</b>			

## Symbols, Abbreviated Terms, and Notation

ADC	Analog to Digital Converter
DPP	Digital Pulse Processing
OS	Operating System
SBC	Single Board Computer
TDC	Time to Digital Converter

### **Reference Document**

[RD1] AN2472 - CONET1 to CONET2 migration
[RD2] GD2512 - CAENUpgrader QuickStart Guide

[RD3] UM1935 - CAENDigitizer User and Reference Manual

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## 1 CAENComm Overview

CAEN has developed families of acquisition modules (Digitizer, ADC, TDC, etc.) with different standards and formats (VME, NIM and Desktop). They all provide the possibility to be handled and read out by a host PC via several communication channels. The purpose of the CAENComm library is to implement a common interface to the higher software layers, masking the details of the physical channel and its protocol, thus making the libraries and applications that rely on the CAENComm independent from the physical layer.

Moreover, the CAENComm is based on CAENVMElib, a library specifically developed for CAEN Bridges like USB-VME (Mod V1718), Optical Link-VME (Mod V2718), Optical Link/USB-VME (Mod. V3718), Optical Link/USB/Ethernet-VME (Mod. V4718) which implements the basic functions for accessing the VME bus (besides other specific functions for these bridges). The CAENComm library also manages the USB3-Optical Link adapter (Mod. A4818) and the PCI/PCIe Controllers (Mod. A2818/A3818).

It is then necessary that the CAENVMELib is already installed on your PC before installing the CAENComm; however, the CAENVMELib is completely transparent to the user.

CAENComm (and so the CAENDigitizer) supports the following communication channels:

- PC → USB → Digitizer series 1.0 (either Desktop or NIM models)
- PC → USB → V1718/V3718/V4718 → VME → CAEN VME Slaves (Digitizers, ADC, TDC, etc.)
- PC → USB → A4818 → CONET → Digitizer series 1.0 (all models)
- PC → PCI (A2818) → CONET → Digitizer series 1.0 (all models)
- PC → PCI (A2818) → CONET → V2718/V3718/V4718 → VME → CAEN VME Slaves (Digitizers 1.0, ADC, TDC, etc.)
- PC → PCIe (A3818) → CONET → Digitizer series 1.0 (all models)
- PC → PCIe (A3818) → CONET → V2718/V3718/V4718 → VME → CAEN VME Slaves (Digitizers 1.0, ADC, TDC, etc.)
- PC → USB → A4818 → CONET → V2718/V3718/V4718 → VME → CAEN VME Slaves (Digitizers 1.0, ADC, TDC, etc.)
- PC  $\rightarrow$  ETH  $\rightarrow$  V4718  $\rightarrow$  VME  $\rightarrow$  CAEN VME Slaves (Digitizers 1.0, ADC, TDC, etc.)

CONET (Chainable Optical NETwork) indicates the CAEN proprietary protocol for communication on Optical Link [RD1].

It is possible to develop a software for one CAEN VME card with a bus controller different from those proposed by CAEN (such as a VME-SBC); in this case it is necessary to provide a "CAENComm equivalent" library by exporting only the functions used by the software.

### System requirements & installation setup

OS	OS version	CAEN Library required	Third-party software required
Windows	10	CAENVMELib	n/a
Linux	glibc version 2.19 or greater		n/a

Tab. 1.1: Host PC requirements



LabVIEW 2009 (only on windows and for the included LabVIEW VIs)

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The hardware and software layers are reported in the scheme below.

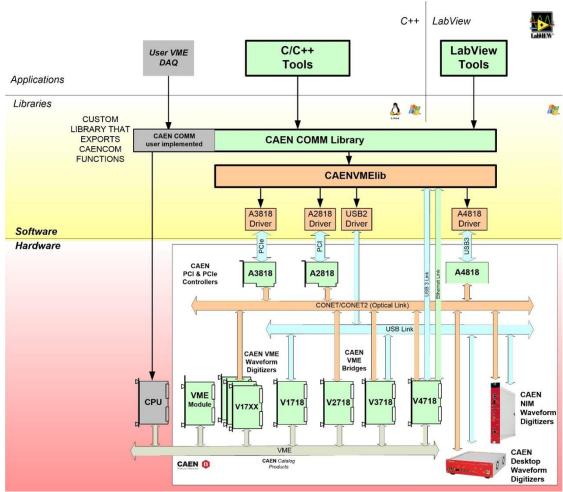


Fig. 1.1: Hardware and Software layers

To install the CAENComm library, follow the steps below:

- Log in to CAEN web site (www.caen.it) and download the installation package for your OS at the CAENComm
  page.
- Unpack on the host PC.
- WINDOWS USERS
  - $-\mbox{ Run}$  the installation setup file and complete the installation wizard.
- LINUX USERS
  - Login as root.
  - Copy and unpack the needed files on your work directory.
  - Go to the library directory.
  - Execute: sh install (for 32-bit installation).
  - Execute: sh install\_x64 (for 64bit installation).
  - The installation copies and installs the library in /usr/lib.

Installation instructions are also in the ReadMe file included in the library package.



**Note:** For Windows OS only, the installation of *CAENComm* also includes a demo program version in Java (*Comm/java/Demo*) and LabVIEW (*Comm/labview/Basic Example Demo*) described in Chap **4**.

Users who developed their own software relying on a CAENComm library version less than 1.2, if they want to upgrade to 1.2 or higher, they must modify the CAENComm\_ConnectionType value accordingly to the new definition!

## 

## 2 Function classification

CAENComm functions are divided into 4 groups:

• Device Initialization/Termination Functions

CAENComm\_OpenDevice

CAENComm\_OpenDevice2

 $CAENComm\_CloseDevice$ 

• Data Transfer Functions

CAENComm\_Write32

CAENComm\_Write16

 $CAENComm\_Read32$ 

CAENComm\_Read16

CAENComm\_MultiRead32

CAENComm\_MultiRead16

CAENComm MultiWrite16

CAENComm\_MultiWrite32

CAENComm\_BLTRead

CAENComm\_MBLTRead

• Interrupt Handling Functions

CAENComm\_IRQDisable

 $CAENComm\_IRQEnable$ 

CAENComm\_IRQWait

 ${\sf CAENComm\_IACKCycle}$ 

CAENComm\_VMEIRQWait

• Information recovery functions

CAENComm\_Info

CAENComm\_SWRelease

CAENComm\_DecodeError

## **CAENComm Data Types**

### CAENComm\_ConnectionType

Code	Value	Description
CAENComm_USB	0	Connected through USB
CAENComm_OpticalLink	1	Connected by optical link
CAENComm_USB_A4818_V2718	2	Connectd through USB -> A4818 -> CONET -> V2718
CAENComm_USB_A4818_V3718	3	Connectd through USB -> A4818 -> CONET -> V3718
CAENComm_USB_A4818_V4718	4	Connectd through USB -> A4818 -> CONET -> V4718
CAENComm_USB_A4818	5	Connectd through USB -> A4818
CAENComm_ETH_V4718	6	Connected by Ethernet to V4718
CAENComm_USB_V4718	7	Connected by USB 3.0 to V4718

Tab. 2.1: Connection Type table

## CAENComm\_ErrorCode

Error code	Value	Description
CAENComm_Success	0	Operation completed successfully
CAENComm_VMEBusError	-1	VME bus error during the cycle
CAENComm_CommError	-2	Communication error
CAENComm_GenericError	-3	Unspecified error
CAENComm_InvalidParam	-4	Invalid parameter
CAENComm_InvalidLinkType	-5	Invalid Link Type
CAENComm_InvalidHandler	-6	Invalid device handler
CAENComm_CommTimeout	-7	Communication Timeout
CAENComm_DeviceNotFound	-8	Unable to Open the requested Device
CAENComm_MaxDevicesError	-9	Maximum number of devices exceeded
CAENComm_DeviceAlreadyOpen	-10	The device is already opened
CAENComm_NotSupported	-11	Not supported function
CAENComm_UnusedBridge	-12	There aren't boards controlled by that Bridge
CAENComm_Terminated	-13	Communication terminated by the Device
CAENComm_UnsupportedBaseAddress	-14	The Base Address is not supported

Tab. 2.2: CAENComm error codes table

### CAENCOMM\_INFO

Code	Value	Description
CAENComm_PCI_Board_SN	0	s/n of the PCI/PCIe board
CAENComm_PCI_Board_FwRel	1	Firmware Release of the PCI/PCIe board
CAENComm_VME_Bridge_SN	2	s/n of the VME bridge
CAENComm_VME_Bridge_FwRel1	3	Firmware Release for the VME bridge
CAENComm_VME_Bridge_FwRel2	4	Firmware Release for the optical chipset inside the VME
		bridge (V2718 only)
CAENComm_VMELIB_handle	5	The VMELib handle used by CAENComm

Tab. 2.3: CAENComm info table

### **IRQ** Levels

Error code	Value	Description
IRQ1	0x01	Interrupt level 1
IRQ2	0x02	Interrupt level 2
IRQ3	0x04	Interrupt level 3
IRQ4	0x08	Interrupt level 4
IRQ5	0x10	Interrupt level 5
IRQ6	0x20	Interrupt level 6

IRQ7	0x40	Interrupt level 7

Tab. 2.4: IRQ levels table

## 3 Function descriptions

## Initialization/termination functions

These functions allow to open and close the connection with a remote board.

To open one board it is necessary to describe the 'logical' path from the PC to the device to access (one of the paths indicated in the introduction). This path is specified by the input parameters of the OpenDevice function. Once the device is opened, the function returns a handle that becomes the unique identifier of that device; any access operation to the device (except for VME cards IRQ management) will take place according to its handle, thus making transparent the physical channel.

### CAENComm\_OpenDevice

#### Description

This function allows to open the device.

### **Synopsis**

```
CAENComm_ErrorCode STDCALL CAENComm_OpenDevice (

CAENComm_ConnectionType LinkType,

int LinkNum,

int ConetNode,

uint32_t VMEBaseAddress,

int *handle
);
```

#### **Arguments**

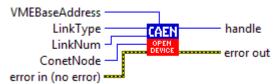
Alguinents		
Name	Description	
LinkType	Indicates the link used by the device. The available values for this parameter are listed in Sec. <b>CAENComm_ConnectionType</b> .	
LinkNum	When using OpticalLink (V2718/V3718), it is the optical link number to be used. When using USB (V1718/V3718), it is the USB device number to be used. When using A4818, it is the PID of the A4818 adapter.	
For OpticalLink, it identifies which device in the daisy-chain is addressed. For USB, it must be 0.		
VMEBaseAddress The VME base address of the board in case you want to access a VME device, 0 otherw *handle Pointer to the handler returned by the open function, to be used for accessing the dev		

#### Return Values

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabVIEW Representation**

CAENComm OpenDevice.vi



### CAENComm\_OpenDevice2

### Description

This function allows to open the device. This function allows to manage the connection to CAEN V4718 VME bridge (as well as to all other CAEN VME bridges).

#### **Synopsis**

```
CAENComm_ErrorCode STDCALL CAENComm_CloseDevice (

CAENComm_ConnectionType LinkType,

void *arg,

int ConetNode,

uint32_t VMEBaseAddress,

int *handle
);
```

### **Arguments**

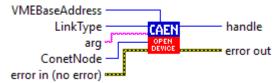
Aiguillelits			
Name	Description		
LinkType	Indicates the link used by the device. The available values for this parameter are listed in Sec. <b>CAENComm_ConnectionType</b> .		
*arg	<ul> <li>The arg pointer can take on different functions depending on the type of connection:</li> <li>Pointer to the link number, in case of an USB connection via V1718 or V3718.</li> <li>Pointer to the optical link number, in case of an optical link connection via V2718, V3718 or V4718.</li> <li>Pointer to the PID, in case of an USB connection to the A4818 or to the V4718.</li> <li>Pointer to the IP address, in case of an Ethernet connection to the V4718.</li> </ul>		
ConetNode	For OpticalLink, it identifies which device in the daisy-chain is addressed. For USB and Ethernet, it must be 0.		
VMEBaseAddress	The VME base address of the board in case you want to access a VME device, 0 otherwise.		
*handle	Pointer to the handler returned by the open function, to be used for accessing the device		

#### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabVIEW Representation**

 ${\tt CAENComm\_OpenDevice 2.vi.}$ 



### CAENComm\_CloseDevice

### Description

This function allows to close the device

#### **Synopsis**

#### Arguments

Alguments	
Name	Description
handle	The handler to use for accessing the device

### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabVIEW Representation**

CAENComm\_CloseDevice.vi



## **Initialization/Termination Examples**

### Example N° 1

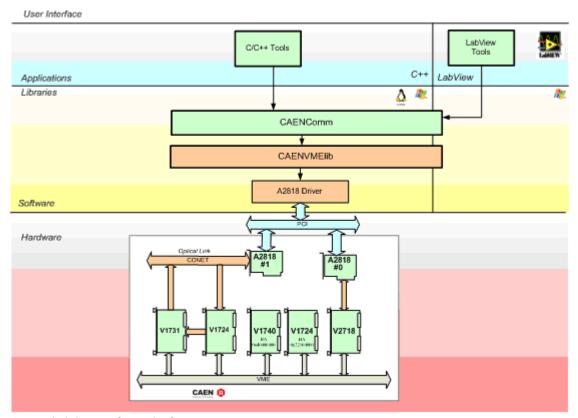


Fig. 3.1: Block diagram of example N° 1  $\,$ 

The host PC houses two CAEN A2818; the VME crate houses the following boards:

- a) Bridge CAEN V2718 physically connected via optical links to the PCI card A2818 N° 0
- b) Two Digitizers CAEN (model V1724 with VME base address 0x32100000 and model V1740 with VME base address 0x40000000)
- c) Two Digitizers CAEN (model V1724 and V1731 model) connected in a daisy chain between them and to the PCI card A2818  $\,\mathrm{N}^\circ\,\mathrm{1}$

The open for the 4 cards to access are:

### Open the V1724 (VME BASE ADDRESS 0x32100000) accessed via VMEbus through the V2718:

### Open the V1740 (VME BASE ADDRESS 0x40000000) accessed via VMEbus through the V2718:

## 

### Open the V1724 (first in daisy chain) directly accessed via Optical Link:

### Open the V1731 (second in daisy chain) directly accessed via Optical Link:

### Example N° 2

User Interface

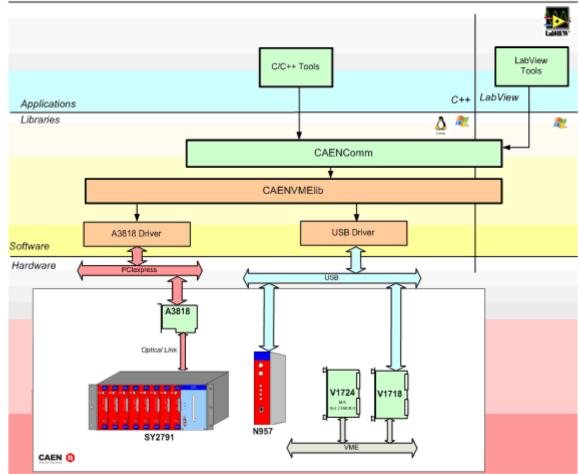


Fig. 3.2: Block diagram of example N° 2

Host PC houses two USB ports and an A3818 card; it is connected to three crates, respectively VME, NIM and SY2791, housing the following boards:

- a) VME Crate
- Bridge CAEN V1718 physically connected to the PC via USB cable
- Digitizer model V1724 with VME base address 0x12340000
- b) NIM Crate
- Multi Channel Analyzer model N957 physically connected to the PC via USB cable
- c) Crate SY2791
- Model A2792 Acquisition module physically connected via optical links to the PCI Express A3818

The open for the 3 cards to access are:

### Open the V1724 accessed from VMEbus through V1718:

### Open the N957 connected via USB cable:

```
CAENComm_OpenDevice(
Physical link CAENComm_USB,
USB link n. 1,
not used 0,
not used 0,
not used
                                  0,
                                  &handleN957
                                 );
```

### Open the SY2792 connected via Optical Link

```
CAENComm_OpenDevice(
Physical link CAENComm_OpticalLink,
PCIE link n. 0,
Device in chain 0,
not used 0,
&handleSY2792
                                                &handleSY2792
```

### **Data Transfer Functions**

### CAENComm\_Write32

#### Description

This function allows to write a 32 bit register of the device

### **Synopsis**

### **Arguments**

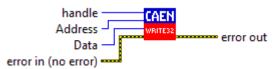
Name	Description
handle	Device handler
Address	Register address offset
Data	New register content to write into the device

#### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabVIEW Representation**

CAENComm\_Write32.vi



### CAENComm\_Write16

### Description

This function allows to write a 16 bit register of the device.

### **Synopsis**

### **Arguments**

0	
Name	Description
handle	Device handler
Address	Register address offset
Data	New register content to write into the device

### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabVIEW Representation**

CAENComm\_Write16.vi



### CAENComm\_Read32

### Description

This function allows to read 32 bit register of the device.

#### **Synopsis**

### **Arguments**

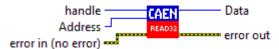
Name	Description
handle	Device handler
Address	Register address offset
Data	The data read from the device

#### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabVIEW Representation**

CAENComm\_Read32.vi



### CAENComm\_Read16

### Description

This function allows to read 16 bit register of the device

### Synopsis

### **Arguments**

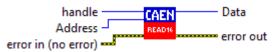
Name	Description
handle	Device handler
Address	Register address offset
Data	The data read from the device

### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabVIEW Representation**

CAENComm\_Read32.vi



## **Multi Read/Write Functions**

MultiRead and MultiWrite Functions have been developed to optimize the time in the individual accesses. In fact, both the USB in the Conet (Optical Link) foresee the exchange of request and response packets for the execution of a single read or write cycle. Because of the latency due to physical channel and protocol, the overhead of the protocol (package management) is very heavy when compared to the amount of data transferred (a 16 or 32-bit word), thus making communication ineffective. This overhead is particularly onerous in the case of the USB protocol which foresees a scheduling of the communication frames that are repeated at fixed intervals of 1ms (USB 1.1) or 125µs (USB 2.0). The purpose of MultiRead and MultiWrite is to place the requests in a single packet transmission from the PC to the device and then receive back the responses in a single package, thereby reducing the impact of latency on the single access.

NOTE: MultiRead and MultiWrite foresee an implementation at physical channel level. If a VME CAEN controller is not used, these libraries must be exported through a loop at software level running a series of individual accesses.

### CAENComm\_MultiRead32

#### Description

The function performs a sequence of single 32bit Read operation.

### **Synopsis**

#### **Arguments**

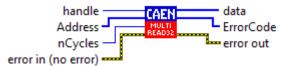
Name	Description
handle	Device handler
Address	Degister address offsets
nCycle	The number of Read to perform
data	The data read from the device
ErrorCode	The error codes relative to each cycle

#### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

CAENComm\_MultiRead32.vi



### CAENComm\_MultiRead16

### Description

The function performs a sequence of single 16 bit Read operation.

#### Synopsis

### **Arguments**

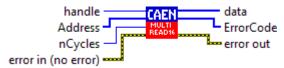
Name	Description
handle	Device handler
Address	Degister address offsets
nCycle	The number of Read to perform
data	The data read from the device
ErrorCode	The error codes relative to each cycle

#### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

CAENComm\_MultiRead16.vi



### CAENComm\_MultiWrite32

### Description

The function performs a sequence of single 32 bit Write operation.

#### Synopsis

### **Arguments**

Name	Description
handle	Device handler
Address	Degister address offsets
nCycle	The number of Write to perform
data	The data to write to the device
ErrorCode	The error codes relative to each cycle

### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

CAENComm\_MultiWrite32.vi



### CAENComm\_MultiWrite16

### Description

The function performs a sequence of single 16 bit Write operation.

#### Synopsis

### **Arguments**

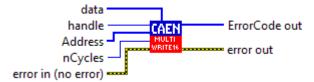
Name	Description
handle	Device handler
Address	Degister address offsets
nCycle	The number of Write to perform
data	The data to write to the device
ErrorCode	The error codes relative to each cycle

#### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

CAENComm\_MultiWrite16.vi



### CAENComm\_BLTRead

### Description

This function allows to read a block of data from the device using a BLT (32-bit) cycle.

### Synopsis

### **Arguments**

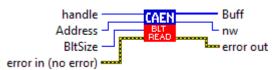
······································	
Name	Description
handle	Device handler
Address	Data space starting address
BltSize	Size of the Block Read Cycle (in longwords, i.e. 32-bit words)
buff	Pointer to the read data buffer
nw	Number of longwords (32 bit) actually read from the device

### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

CAENComm\_BLTRead.vi



## 

### CAENComm\_MBLTRead

### Description

This function allows to read a block of data from the device using an MBLT (64-bit) cycle.

### Synopsis

### **Arguments**

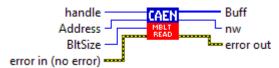
Name	Description
handle	Device handler
Address	Data space starting address
BltSize	Size of the Block Read Cycle (in longwords, i.e. 32-bit words)
buff	Pointer to the read data buffer
nw	Number of longwords (32 bit) actually read from the device

#### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

CAENComm\_MBLTRead.vi



### **Interrupt Handling Functions**

### CAENComm\_IRQDisable

### Description

This function disables the IRQ lines.

#### **Synopsis**

### Arguments

Name	Description
handle	Device handler

### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

CAENComm IRQDisable.vi



### CAENComm\_IRQEnable

#### Description

This function enables the IRQ lines.

#### Synopsis

#### **Arguments**

<b>0</b>	
Name	Description
handle	Device handler

### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

CAENComm IRQEnable.vi



### CAENComm\_IRQWait

### Description

The function waits the IRQ lines specified by Mask until one of them raise or timeout expires.



Note: This function can be used ONLY on board NOT controlled by CAEN VME Bridges.

### **Synopsis**

#### **Arguments**

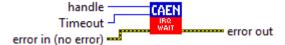
Name	Description
handle	Device handler
Timeout	Timeout in milliseconds

#### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

CAENComm\_IRQWait.vi



### CAENComm\_IACKCycle

### Description

The function performs an interrupt acknowledge cycle.

#### Svnopsis

### Arguments

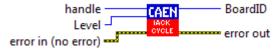
Name	Description
handle	Device handler
Level	The IRQ level to acknowledge
BoardID	The ID of the Board that raised the interrupt

### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

CAENComm\_IACKCycle.vi



### CAENComm\_VMEIRQWait

### Description

The function waits the IRQ until one of them raises or timeout expires.

#### **Synopsis**

```
CAENCOMM_ErrorCode STDCALL CAENCOMM_VMEIRQWait(

CAENCOMM_ConnectionType LinkType,

int LinkNum,

int ConetNode,

uint8_t IRQMask

uint32_t Timeout,

int *VMEHandle

);
```

### **Arguments**

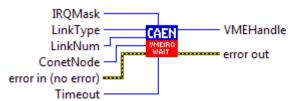
Name Description			
LinkNum	When using Optical Link , it is the optical link number to be used		
	When using USB, it is the USB device number to be used		
LinkType	LinkType: The link used by the device		
	0: CAENComm_USB		
	1: CAENComm_OpticalLink		
ConetNode	The CAEN VME Bridge number in the link		
IRQMask	A bit-mask indicating the IRQ lines		
Timeout	Timeout in milliseconds		
VMEHandle	The CAEN Bridge handle to use in VMEIRQCheck and VMEIACKCycle		

#### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

CAENComm\_VMEIRQWait.vi



### CAENComm\_VMEIRQCheck

#### Description

The function returns a bit mask indicating the active IRQ lines.

### Synopsis

### **Arguments**

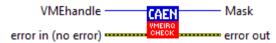
Name	Description
VMEhandle	CAEN Brigde handle
Mask	A bit-mask indicating the IRQ lines

### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

CAENComm\_VMEIRQCheck.vi



### CAENComm\_VMEIACKCycle16

### Description

The function performs a 16-bit interrupt acknowledge cycle.

#### **Synopsis**

### **Arguments**

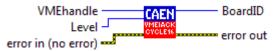
0	
Name	Description
VMEhandle	CAEN Brigde handle
Level	The IRQ level to acknowledge (see IRQLevels enum)
BoardID	The Id of the Board that read the interrupt

### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

CAENComm\_VMEIACKCycle16.vi



### CAENComm\_VMEIACKCycle32

### Description

The function performs a 32-bit interrupt acknowledge cycle.

### **Synopsis**

### **Arguments**

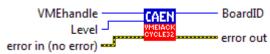
Name	Description
VMEhandle CAEN Brigde handle	
Level	The IRQ level to acknowledge (see IRQLevels enum)
BoardID	The Id of the Board that read the interrupt

### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

CAENComm\_VMEIACKCycle32.vi



### **Details and Examples**

The interrupts management foresees two cases:

- The device you want to broadcast the request is directly connected to the PC.
- 2. The device you want to broadcast the request is accessed via bridge (only with VME).

If a device is directly connected to the optical link, USB or Ethernet the IRQ wait is managed by the function:

The function waits the IRQ until one of them raise or timeout expires.

If a device is accessed via VMEbus through the CAEN Bridge, the IRQ wait is managed by the function:

The function waits either until the bridge that manages the VME boards (specified by the function parameters) raises an IRQ or the timeout to expire.

As this function returns a VMEHandle, it is necessary to use the function CAENComm\_VMEIRQCheck to acknowledge which board raised the IRQ.

EXAMPLES (for a setup like the picture shown in Fig. 3.1):

1) In order to handle the IRQ of boards V1740 (BA 0x40000000) and V1724 (BA 0x32100000).

IRQ management for boards in Fig. 3.1:

```
CAENComm_VMEIRQWait(

Physical link CAENComm_OpticalLink,
PCI board n. 0,
Device in chain 0,
IRQmask 0xff,
Timeout 5000,
Handle of bridge that raises the IRQ &VMEHandle
);
```

Then CAENComm\_VMEIRQCheck and / or CAENComm\_VMEIACKCycle32 can be used to acknowledge the activated IRQ Level.

2) In order to handle the IRQ of boards connected to the A2818#1:

And

## **Utility Functions**

### CAENComm\_Info

#### Description

The function returns information about serial number or firmware release of the device.

#### **Synopsis**

### **Arguments**

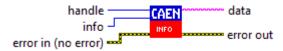
Name	Description
handle	Device handler
info	The interested info (see CAENCOMM_INFO)
data	An array (user defined to 30 bytes) with the requested info

### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

#### **LabView Representation**

CAENComm\_Info.vi



### **CAENComm\_SWRelease**

### Description

The function returns the Software Release of the library.

#### Synopsis

### Arguments

, g		
Name	Description	
SwRel	The Software Release of the library	

#### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

CAENComm\_SWRelease.vi



### CAENComm\_DecodeError

### Description

This function decodes the error code.

### Synopsis

### **Arguments**

Name	Description
ErrCode	The error code
ErrMsg	A string with the error message

#### **Return Values**

0: Success; Negative numbers are error codes (see Tab. 2.2).

### **LabView Representation**

 $CAENComm\_DecodeError.vi$ 



## **4 CAENComm Demos**

Once installed the CAENComm tool, two demos are available for a first approach to the library, provided both as graphical user interfaces ready to use and as source files and projects for the user development:

- Java demo (Comm/java/Demo)
- LabVIEW demo (Comm/labview/Basic Example Demo)

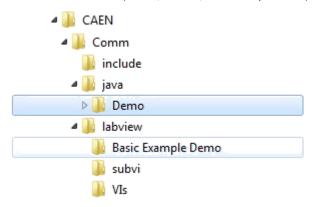
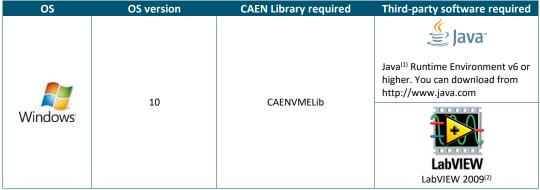


Fig. 4.1: Folder path of the two CAENComm demos.

The following table shows the system and software requirements needed by the demos.



Tab. 4.1: System and software requirements for CAENComm demo programs

- (1) Java™ is a registered trademark of Oracle, Inc.
- (2) LabVIEW™ is a Trademark of National Instruments Corp.

The CAENComm demos can be used with all the CAEN digitizer series running both the standard and DPP firmware.

## **Getting Started with CAENComm Demos**



Fig. 4.2: CAENComm Java and LabVIEW demos

This paragraph describes how to use the functions of the CAENComm library implemented in the demos. The Java version is taken as reference; the LabVIEW version is the same.

All the following steps have been executed on a CAEN desktop digitizer DT5724 running the DPP-PHA firmware with the Optical Link (CONET2) as communication channel (through the A3818 PCIe CAEN Controller).

## 

### **Demo Structure**

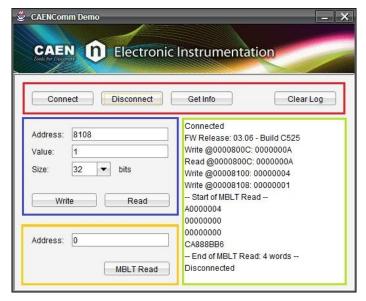
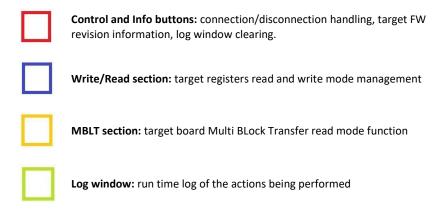


Fig. 4.3: CAENComm demo structure



### **Launch the Demo**

For the Java version: launch the CAENCommDemo.jar file in the CAEN/ Comm/java/Demo path.

For the **LabVIEW** version: **launch** the **Basic Example Demo.vi** file in the CAEN/ Comm/labview/Basic Example Demo path.

Fig. 4.2 shows the GUI and VI being opened.

### **Control and Info Buttons**

• The Connect function is based on CAENComm\_OpenDevice:

Click on the CONNECT button and select the proper connection parameters in the connection window.



Fig. 4.4: Connection funtcion

**Read** in the **Log window** the result of the connection: "Connected" if succeded; an error message in case of failure (refer to **CAENComm\_ErrorCode**).

Here below several connection cases and the relative settings are shown as reference.

Connection chain	Туре	Link	Slave	Address
PC -> <b>USB</b> -> Desktop digitizer	USB	0	0	0
PC -> <b>USB</b> -> Desktop digitizer***	USB	1	0	0
PC -> <b>USB</b> -> V1718/V3718 -> <b>VME</b> -> VME digitizer	USB	0	0	VBA*
PC -> <b>PCIe</b> -> A3818 -> <b>CONET</b> -> V2718/V3718/V4718 -> <b>VME</b> -> VME digitizer	Optical Link	0	0	VBA*
PC -> PCIe -> A3818 -> CONET -> Desktop/NIM/VME digitizer	Optical Link	0	0	0
PC -> PCIe -> A3818 -> CONET -> VME digitizer **	Optical Link	0	1	0
PC -> <b>PCI</b> -> A2818 -> <b>CONET</b> -> V2718/V3718/V4718 -> <b>VME</b> -> VME digitizer	Optical Link	0	0	VBA*
PC -> PCI -> A2818 -> CONET -> Desktop/NIM/VME digitizer	Optical Link	0	0	0
PC -> <b>PCI</b> -> A2818 -> <b>CONET</b> -> VME digitizer **	Optical Link	0	1	0
PC -> <b>USB</b> -> A4818 -> <b>CONET</b> -> Desktop/NIM/VME digitizer	USB_4818	PID****	0	0
PC -> <b>USB</b> -> A4818 -> <b>CONET</b> - > V2718 -> VME digitizer	USB_4818_V2718	PID****	0	VBA*
PC -> <b>USB</b> -> A4818 -> <b>CONET</b> - > V3718 -> VME digitizer	USB_4818_V3718	PID****	0	VBA*
PC -> <b>USB</b> -> A4818 -> <b>CONET</b> - > V4718 -> VME digitizer	USB_4818_V4718	PID****	0	VBA*

PC -> USB -> V4718 -> VME -> VME digitizer	USB_A4718****	0	0	VBA*
PC -> ETH -> V4718 -> VME digitizer	ETH_A4718****			VBA*

Tab. 4.2: Examples of connection settings

- \* For the correct VBA (VME base address) to be used, please refer to the Digitizer documentation.
- \*\* The VME Digitizer is intended to be part of a Daisy chain (see the examples at the end of [RD2]).
- \*\*\* It is supposed that at least two USB ports are used by the PC to communicate with as many digitizers (see the examples at the end of [RD3]).
- \*\*\*\* The PID (Product Identifier) of the A4818.
- \*\*\*\*\* LabVIEW version of the Demo only.
  - The **Get Info** function implements a single read access to the ROC FPGA register of the target board, basing on **CAENComm\_Read32**:

Click on the GET INFO button to read the ROC FPGA firmware release in the Log window.

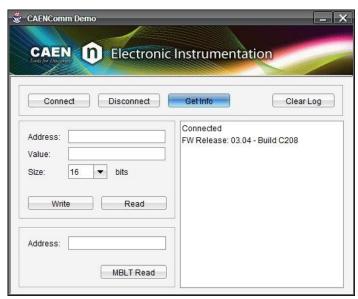


Fig. 4.5: Get Info function

• The Disconnect function is based on CAENComm\_CloseDevice:

**Click** on the **DISCONNECT** button to **get disconnected** from the target (disconnection is confirmed by the "Disconnected" message in the Log window)



Fig. 4.6: Disconnect function

• The **Clear Log** is a software utility and does not base on any CAENComm function:

 ${\bf Click}$  on the  ${\bf CLEAR}$   ${\bf LOG}$  button to  ${\bf clear}$  the  ${\bf Log}$   ${\bf window}.$ 

### **Read/Write Target Board Registers**

• The **Read** function is based on **CAENComm\_Read16** and **CAENComm\_Read32**, allowing a 16-bit and 32-bit single read of the target board registers.

To read the content of a register:

**Type** the **register address** (the low 16 bits of the 32-bit address) in the **ADDRESS** field (in **Fig. 4.7** the 108C address is the AMC FPGA firmware release register).

Select the read access size (32 or 16 bit) in the SIZE field.

Click on the READ button.

Read the value of the register in the VALUE field.

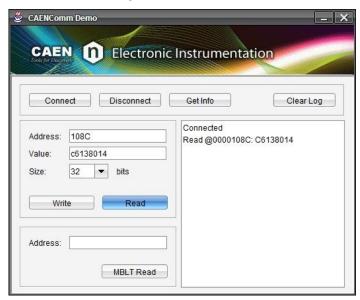


Fig. 4.7: Read function

Refer to the digitizer User Manual to decode the register information.

• The **Write** function is based on **CAENComm\_Write16** and **CAENComm\_Write32**, allowing a 16-bit and 32-bit single write of the target board registers.

To write a register:

Type the register address (the low 16 bits of the 32-bit address) in the ADDRESS field (in Fig. 4.8 the 8100 address is the Acquisition Control register and the written value enable the acquisition run; refer the the digitizer User Manual for details).

Type the value (hexadecimal) to write in the VALUE field.

Select the write access size (32 or 16-bit) in the SIZE field.

Click on the WRITE button (a further read of the same register can be performed to check the correct writing).

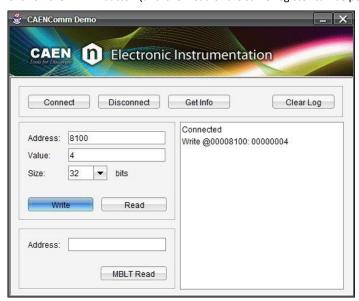


Fig. 4.8: Write function

### **Perform a MBLT Read**

The MBLT Read function is based on **CAENComm\_MBLTRead**, which allows to read a block of data from the targert board using an MBLT (64 bit) cycle.

Here follows a simple example of how to perform a MBLT read:

Use the Write function to enable the acquisition run (i.e. write '4' at the adress 8100).

Use the Write function to send a software trigger to the target board (i.e. write any value at the address 8108).

Type the data space starting address in the ADDRESS field (default value is "0").

Read data in the Log window.



For details about the data format, please refer to the digitizer User Manual (if running the standard firmware) or to the DPP firmware User Manual (if running the DPP firmware).

# **5 Technical Support**

CAEN makes available the technical support of its specialists for requests concerning the software and hardware. Use the support form available at the following link:

https://www.caen.it/support-services/support-form/



## **Electronic Instrumentation**



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