

# ELEC-3500 Lab #9

## Egg Timer Lab

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B1 Fri/B2 Wed/B3 Thu/B4 Tue/B5 Mon

Date: \_\_\_\_\_

Partner 1 Name: \_\_\_\_\_ Student #: \_\_\_\_\_

Partner 1 Name: \_\_\_\_\_ Student #: \_\_\_\_\_

	Partner 1	Partner 2
Attendance		
FPGA implementation Demo Checked By: _____	_____/70	
Code and Questions: Checked By: _____	_____/30	_____/30
Innovation Demo Checked By: _____	_____/70	
Code and Questions: Checked By: _____	_____/30	_____/30
Lab Report	_____/100	
Total:	_____/300	_____/300