ELEC-3500 Lab #8

Verilog lab 10 and Vivado hardware debug tutorial

Date: _____   Section: B1 Fri/B2 Wed/B3 Thu/B4 Tue/B5 Mon

Partner 1 Name: _____   Student #:_____

Partner 2 Name: _____   Student #:_____

	Partner 1	Partner 2
Attendance		
In Lab Demo and utilization: Checked By:_____	_____/75 (total)	
	PMOD tutorial 19-1	_____/25
	Tutorial hardware debug	_____/10
	1-1	_____/20
	1-2	_____/20
Code and Questions: Checked By:_____	_____/15	_____/15
Finished in Lab: Checked By:_____	_____/10	_____/10
Total:	_____/100	_____/100