

ELEC-3500 Lab #7

Verilog lab 9 and Vivado timing tutorial

Date: _____ Section: B1 Fri/B2 Wed/B3 Thu/B4 Tue/B5 Mon

Partner 1 Name: _____ Student #: _____

Partner 2 Name: _____ Student #: _____

	Partner 1	Partner 2
Attendance		
In Lab Demo and utilization: Checked By: _____	_____/77 (total)	
	Custom_IP 9-1	_____/10
	Timing_tutorial 4-2-11	_____/5
	Timing_tutorial 5-3-7	_____/5
	1-1	_____/5
	1-2	_____/5
	2-1 demo and utilization	_____/8
	2-2 demo and utilization	_____/8
	2-3 demo and utilization	_____/8
	2-4 demo and utilization	_____/8
	3-1	_____/5
	3-2	_____/5
	3-3	_____/5
Code and Questions: Checked By: _____	_____/15	_____/15
Finished in Lab: Checked By: _____	_____/8	_____/8
Total:	_____/100	_____/100