

Mixed–Signal_STM_AudioScope_And_Sine_Generator

[1] PDN [2] STMuC



File: PDN_P1_sheet.kicad_sch

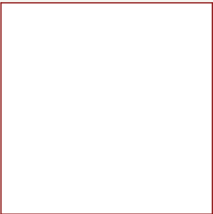


File: untitled.kicad_sch

[3] ADC [4] DAC



File: ADC_P4_sheet.kicad_sch



File: DAC_P5_file.kicad_sch

Mounting Holes no Pad (M3)

- 

H1
MountingHole
- 

H2
MountingHole
- 

H3
MountingHole
- 

H4
MountingHole

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Sheet: /
File: mixed_signal_STM with Kicak backup2.kicad_sch

Title: Mixed_Signal_STM

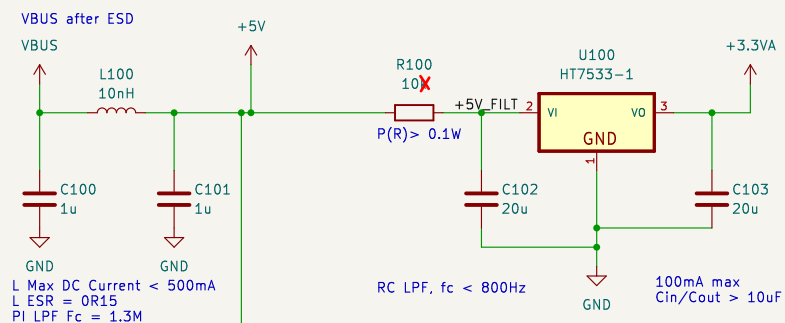
Size: A4 Date: 2023–11–26
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Rev: 1.0
Id: 1/5

PDN

Input filtering, Analog supply and Digital supply

VBUS Power comes from USB Type C (Schematic page2)

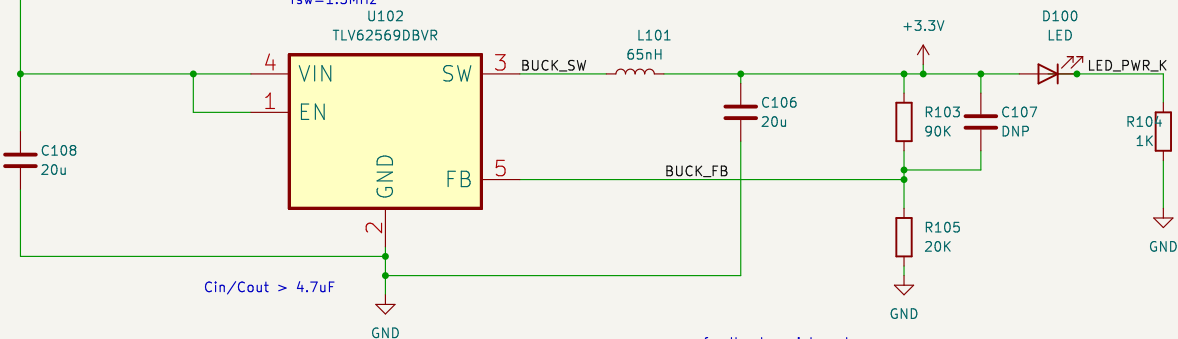


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LBuck > ( Vout(Vin-Vout) ) / ( Vin * fsw * diL)
LBuck > ( 3.3(4.5-3.3) ) / (4.5 * 1.5e6 *0.25*250mA)
LBuck > 12nH
i use light loads => need larger inductance
LBuck = 65nH

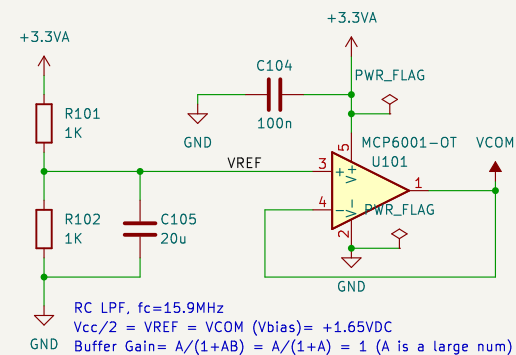
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load max = 250mA
fsw=1.5MHz



feedback resistor calc
 $V_{out} = 0.6 \cdot (1 + R_1/R_2)$
 $3.3 = 0.6 \cdot (1 + R_1/20K) \Rightarrow R_1 = 90K$

Bias voltage Generator



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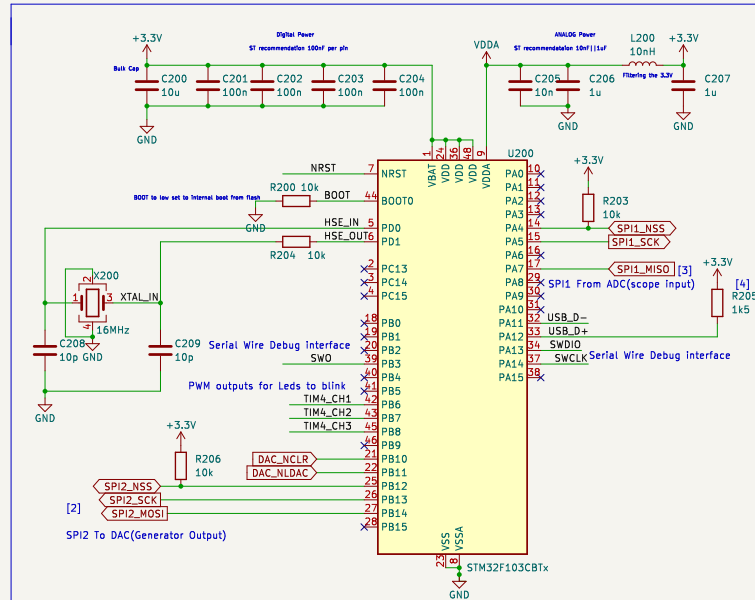
Size: A1	Date: 1
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Rev:

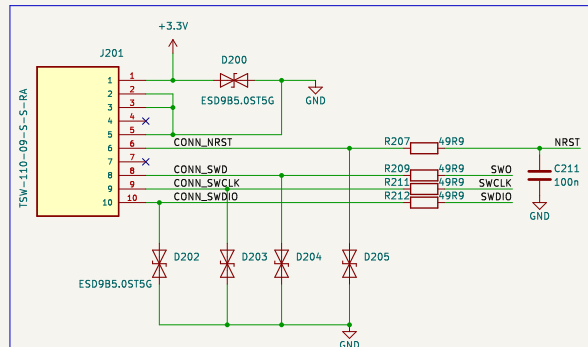
Id: 2/5

STM32 MicroController

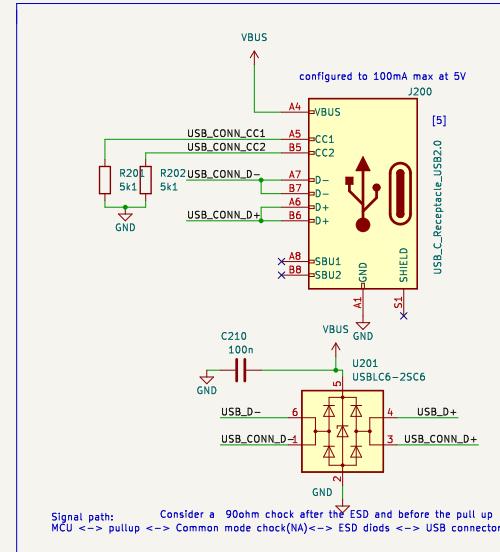
STM uC



SWD Header



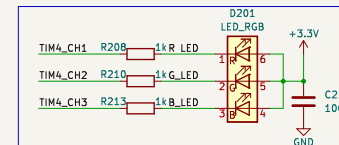
USB 2.0 TypeC connector



Notes

- [1] Xtal: using External Xtal (and not the Internal one) since I want timing accuracy for the USB 2.0 FS. Cap values calculated from the STM AN2867 Application note
- FO=16MHz
CO=10pF
ESR=800ohms
 $R_{ext} = 10\% \text{ of } 1/(2\pi \cdot F_0 \cdot C_0) = 99.47 \Rightarrow 1000\text{ohm}$
Cs is stray capacitance (parasitic) and it's 3-5pF
 $CL = CL1 = CL2 = 2 \cdot (C_0 - C_s) = 10\text{pF} - 14\text{pF}$
using Rext of 0 ohm
- [2] SPI2 will only send data to the DAC
chosen Mode: "Transmit only master" at STM32CubeIDE
NSS is the Chip select ping and was configured as "Hardware NSS Signal" since the STM is the SPI Master
- [3] SPI1 will only receive data from the ADC
chosen Mode: "Receive only master" at STM32CubeIDE
NSS is the Chip select ping and was configured as "Hardware NSS Signal"
- [4] USB Application note AN4879 is request this pull up
- [5] USB typeC Application note TA0357 see section 3.2 - Sink Port (consumer)
This port is able to consume power over VBUS (from 5 V to 20 V and up to 5 A) and must assert a pull-down resistor (Rd) on the CC pins.
Source CC termination (Rp) requirements the table indicating 47k sink on the CC pins but I took 5k1 for BOM considation

RGB Led Traffic Indication



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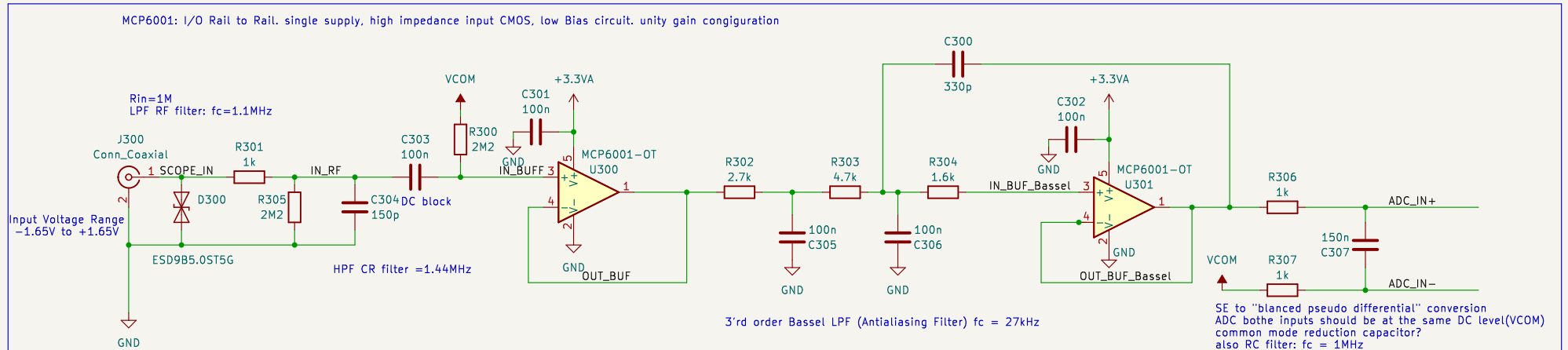
Sheet: /[2] STMuC/
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Title: STM32 uC

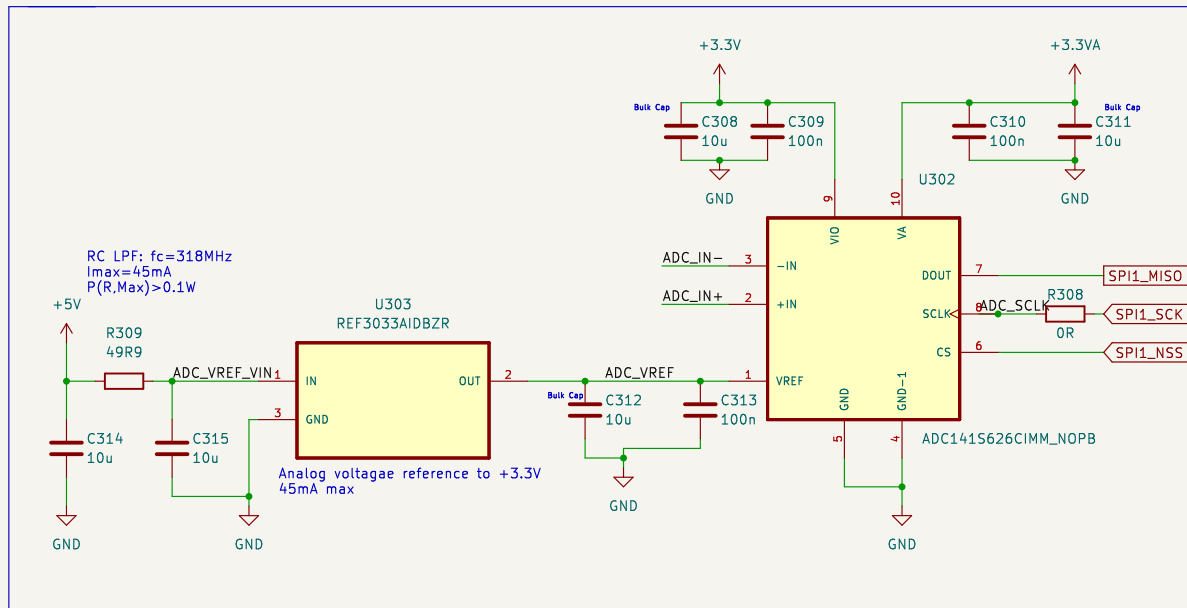
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ADC – Scope



ADC 14-Bit



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Sheet: /[3] ADC/

File: ADC_P4_sheet.kicad_sch

Title: ADC – Scope

Size: A4

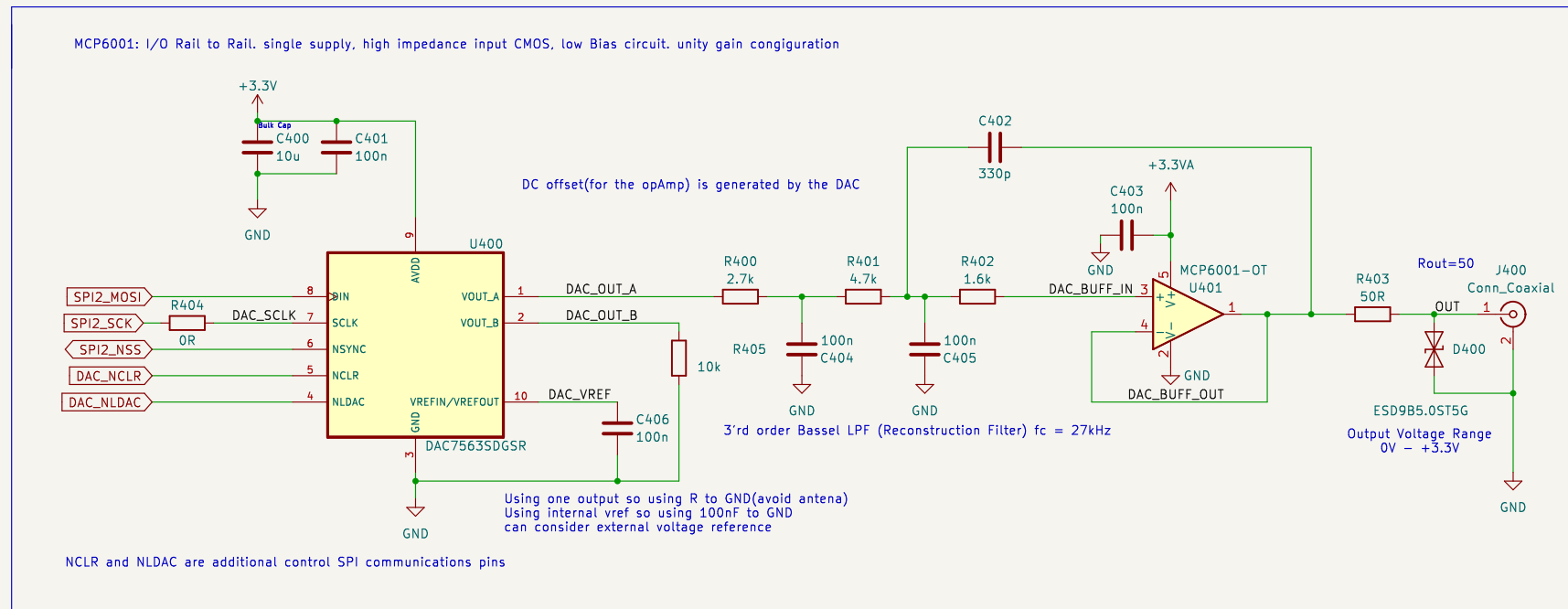
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DAC – Signal generator



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Sheet: /[4] DAC/
File: DAC_P5_file.kicad_sch

Title: DAC – Signal Generator

Size: A4 Date: 2023-11-16

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Id: 5/5