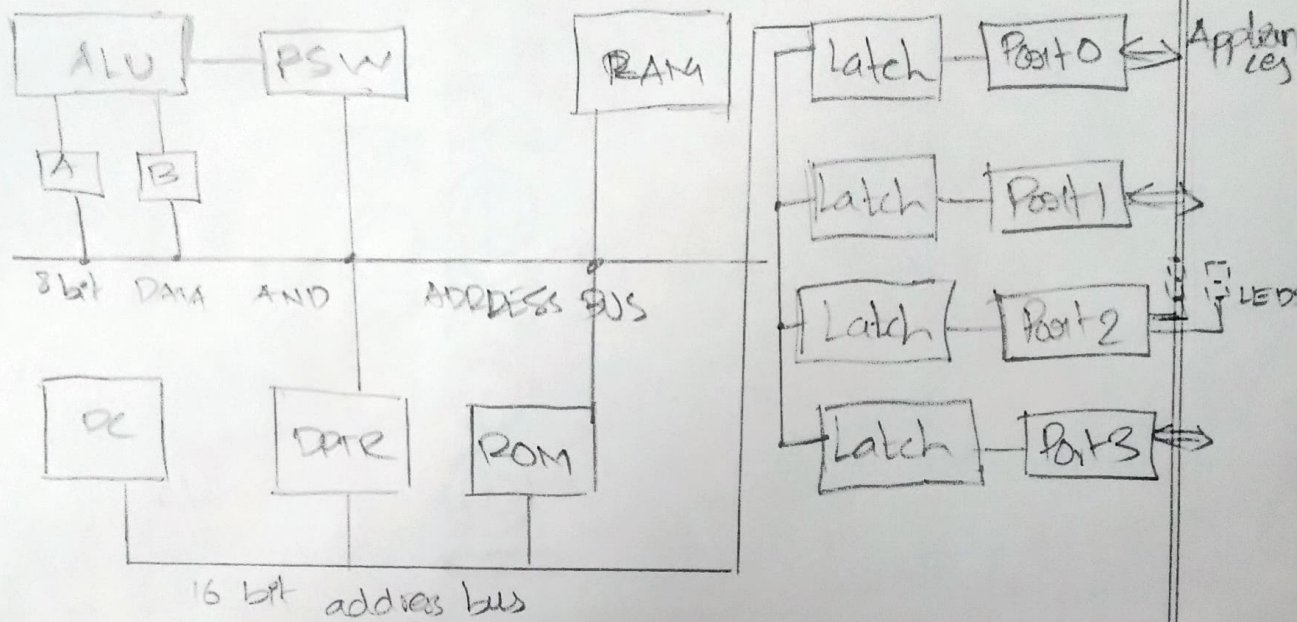


SSN COLLEGE OF ENGINEERING RECORD SHEET

Sheet No.

V. Subasivaran
205001085
MIP I - 2



ALP:

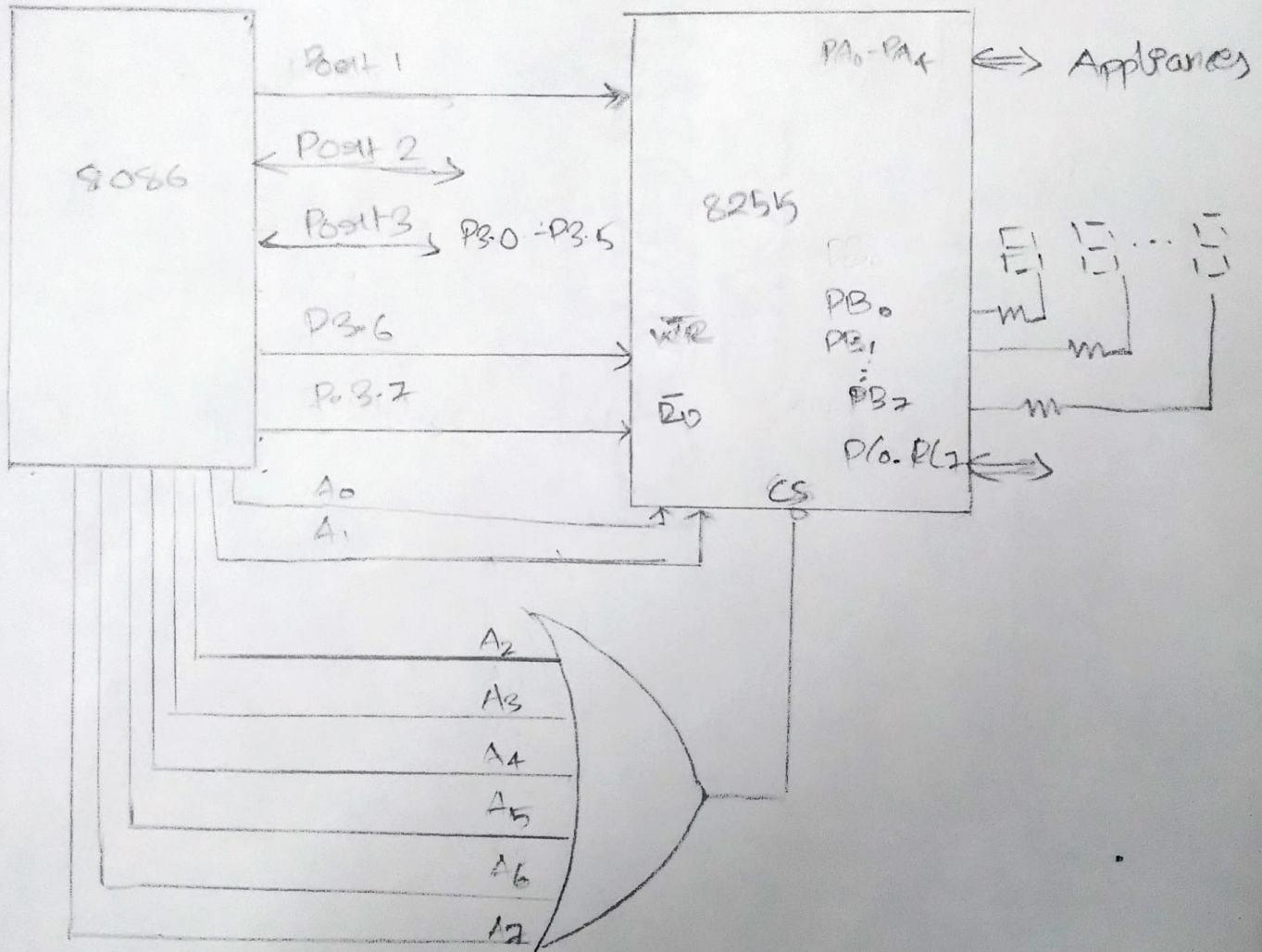
```
MAIN: MOV A, P0
      MOV P2, A
      SJMP MAIN
```


ALP

```

MAIN: MOV A, #90H
      MOV DPTR, #C6H
      MOVX @DPTR, A
      MOV DPTR, #C0H
      MOVX A, @DPTR
      MOV DPTR, #C2H
      MOVX @DPTR, A
      SJMP MAIN
  
```

Port Address
 Control word - C6
 Port A - C0
 Port B - C2
 Port C - C4



While using 8255, Port 1 is used to connect to data lines. Since port 1 is not multiplexed, port 0 is used as address lines given to chip select. Port 3's line, P3.6 and P3.7 are connected to write and read pins. The appliances can then be connected to port A and LEDs can be connected to port B of 8255.