Register Number

Sri Sivasubramaniya Nadar College of Engineering, Kalavakkam – 603 110

(An Autonomous Institution, Affiliated to Anna University, Chennai)

Department of Computer Science and Engineering

Continuous Assessment Test – I

Question Paper

Degree & Branch	B.E. Computer Science and Engineering				**Semester**	V
Subject Code & Name	UCS1502 - Microprocessors and Interfacing				**Regulation: 2018**	
Academic Year	2021-2022	**Batch**	2019-2023	**Date**	**10.08.2021**	**08:30am-10:00am**
Time: 90 Minutes	**Answer All Questions**				**Maximum: 50 Marks**	

Part – B (2×10 = 20 Marks) : Qn No. 23 is compulsory

K2	21. With neat diagrams, explain the architecture of 8086. (10) **OR** 22a. Explain any 5 addressing modes of 8086 with examples. (5) 22b. Explain the memory organization of 8086 microprocessor. (5)	CO1
K3	23. Develop an 8086 MASM ALP to perform 32-bit subtraction along with its algorithm. (Input: two 32-bit numbers; Output: Difference in absolute form and sign indicator; All inputs and outputs should be in memory locations) (10)	CO1