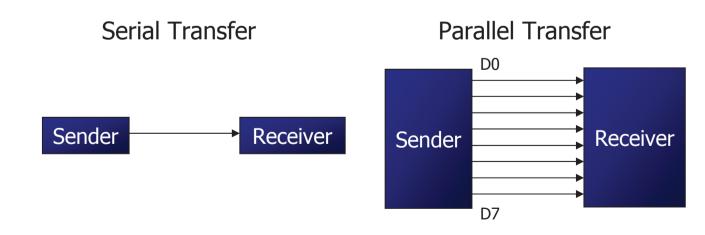
Computers transfer data in two ways:

- Parallel
 - Often 8 or more lines (wire conductors) are used to transfer data to a device that is only a few feet away
- > Serial
 - To transfer to a device located many meters away, the serial method is used
 - The data is sent one bit at a time





BASICS OF SERIAL COMMUNICA-TION (cont')

- At the transmitting end, the byte of data must be converted to serial bits using parallel-in-serial-out shift register
- At the receiving end, there is a serialin-parallel-out shift register to receive the serial data and pack them into byte
- When the distance is short, the digital signal can be transferred as it is on a simple wire and requires no modulation
- If data is to be transferred on the telephone line, it must be converted from 0s and 1s to audio tones
 - This conversion is performed by a device called a *modem*, "Modulator/demodulator"



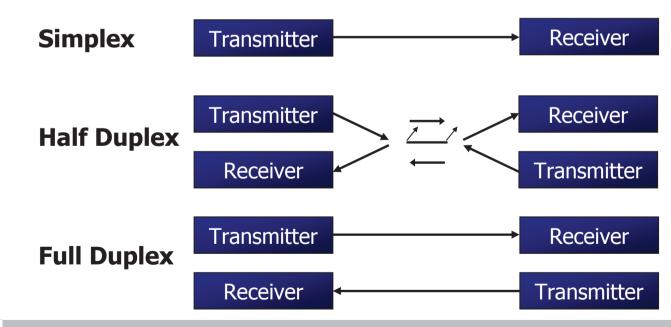
BASICS OF SERIAL COMMUNICA-TION (cont')

- Serial data communication uses two methods
 - Synchronous method transfers a block of data at a time
 - Asynchronous method transfers a single byte at a time
- It is possible to write software to use either of these methods, but the programs can be tedious and long
 - There are special IC chips made by many manufacturers for serial communications
 - UART (universal asynchronous Receivertransmitter)
 - USART (universal synchronous-asynchronous Receiver-transmitter)



Half- and Full-Duplex Transmission

- If data can be transmitted and received, it is a duplex transmission
 - ➤ If data transmitted one way a time, it is referred to as *half duplex*
 - If data can go both ways at a time, it is full duplex
- This is contrast to simplex transmission





Start and Stop Bits

- A protocol is a set of rules agreed by both the sender and receiver on
 - How the data is packed
 - How many bits constitute a character
 - When the data begins and ends
- Asynchronous serial data communication is widely used for character-oriented transmissions
 - Each character is placed in between start and stop bits, this is called *framing*
 - Block-oriented data transfers use the synchronous method
- The start bit is always one bit, but the stop bit can be one or two bits

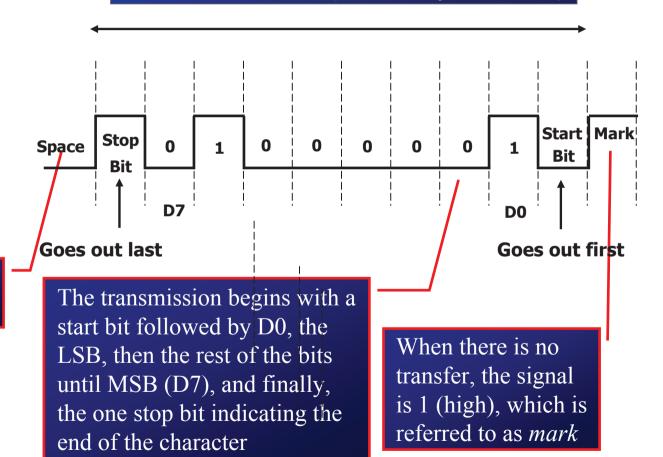


Start and Stop Bits (cont')

The 0 (low) is referred to as *space*

The start bit is always a 0 (low) and the stop bit(s) is 1 (high)

ASCII character "A" (8-bit binary 0100 0001)





HANEL

Start and Stop Bits (cont')

- Due to the extended ASCII characters,
 8-bit ASCII data is common
 - In older systems, ASCII characters were 7bit
- In modern PCs the use of one stop bit is standard
 - In older systems, due to the slowness of the receiving mechanical device, two stop bits were used to give the device sufficient time to organize itself before transmission of the next byte

Start and Stop Bits (cont')

- Assuming that we are transferring a text file of ASCII characters using 1 stop bit, we have a total of 10 bits for each character
 - ➤ This gives 25% overhead, i.e. each 8-bit character with an extra 2 bits
- In some systems in order to maintain data integrity, the parity bit of the character byte is included in the data frame
 - UART chips allow programming of the parity bit for odd-, even-, and no-parity options

Data Transfer Rate

- The rate of data transfer in serial data communication is stated in bps (bits per second)
- Another widely used terminology for bps is baud rate
 - ➤ It is modem terminology and is defined as the number of signal changes per second
 - ➤ In modems, there are occasions when a single change of signal transfers several bits of data
- As far as the conductor wire is concerned, the baud rate and bps are the same, and we use the terms interchangeably

Data Transfer Rate (cont')

- The data transfer rate of given computer system depends on communication ports incorporated into that system
 - IBM PC/XT could transfer data at the rate of 100 to 9600 bps
 - Pentium-based PCs transfer data at rates as high as 56K bps
 - In asynchronous serial data communication, the baud rate is limited to 100K bps

RS232 Standards

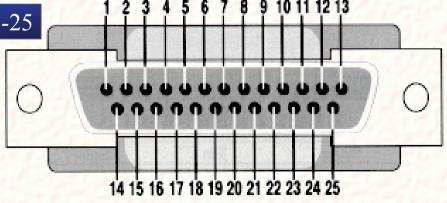
- An interfacing standard RS232 was set by the Electronics Industries Association (EIA) in 1960
- The standard was set long before the advent of the TTL logic family, its input and output voltage levels are not TTL compatible
 - ➤ In RS232, a 1 is represented by -3 ~ -25 V, while a 0 bit is +3 ~ +25 V, making -3 to +3 undefined

RS232 Standards (cont')

RS232 DB-25 Pins

Pin	Description	Pin	Description
1	Protective ground	14	Secondary transmitted data
2	Transmitted data (TxD)	15	Transmitted signal element timing
3	Received data (RxD)	16	Secondary receive data
4	Request to send (-RTS)	17	Receive signal element timing
5	Clear to send (-CTS)	18	Unassigned
6	Data set ready (-DSR)	19	Secondary receive data
7	Signal ground (GND)	20	Data terminal ready (-DTR)
8	Data carrier detect (-DCD)	21	Signal quality detector
9/10	Reserved for data testing	22	Ring indicator (RI)
11	Unassigned	23	Data signal rate select
12	Secondary data carrier detect	24	Transmit signal element timing
13	Secondary clear to send	25	Unassigned



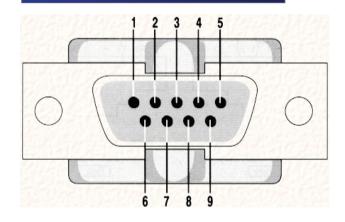




> RS232 Standards (cont')

 Since not all pins are used in PC cables, IBM introduced the DB-9 version of the serial I/O standard

RS232 Connector DB-9



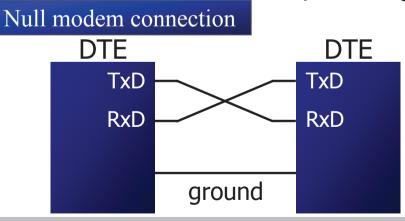
RS232 DB-9 Pins

Pin	Description
1	Data carrier detect (-DCD)
2	Received data (RxD)
3	Transmitted data (TxD)
4	Data terminal ready (DTR)
5	Signal ground (GND)
6	Data set ready (-DSR)
7	Request to send (-RTS)
8	Clear to send (-CTS)
9	Ring indicator (RI)



Data
Communication
Classification

- Current terminology classifies data communication equipment as
 - DTE (data terminal equipment) refers to terminal and computers that send and receive data
 - DCE (data communication equipment) refers to communication equipment, such as modems
- The simplest connection between a PC and microcontroller requires a minimum of three pins, TxD, RxD, and ground





RS232 Pins

DTR (data terminal ready)

When terminal is turned on, it sends out signal DTR to indicate that it is ready for communication

DSR (data set ready)

When DCE is turned on and has gone through the self-test, it assert DSR to indicate that it is ready to communicate

RTS (request to send)

When the DTE device has byte to transmit, it assert RTS to signal the modem that it has a byte of data to transmit

CTS (clear to send)

When the modem has room for storing the data it is to receive, it sends out signal CTS to DTE to indicate that it can receive the data now



RS232 Pins (cont')

DCD (data carrier detect)

The modem asserts signal DCD to inform the DTE that a valid carrier has been detected and that contact between it and the other modem is established

RI (ring indicator)

- An output from the modem and an input to a PC indicates that the telephone is ringing
- It goes on and off in synchronous with the ringing sound

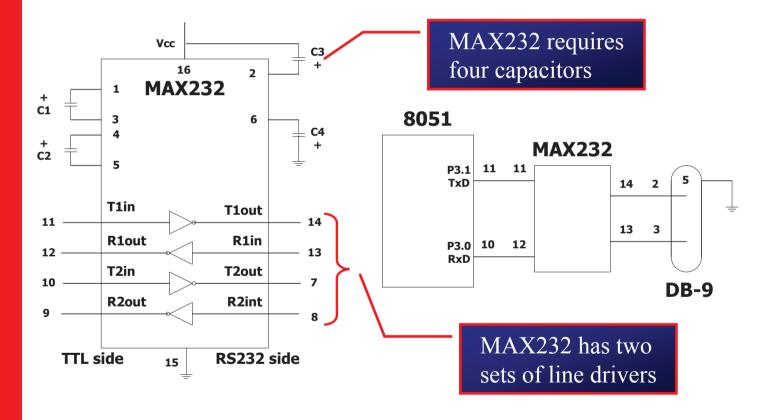
8051 CONNECTION TO RS232

- A line driver such as the MAX232 chip is required to convert RS232 voltage levels to TTL levels, and vice versa
- 8051 has two pins that are used specifically for transferring and receiving data serially
 - ➤ These two pins are called TxD and RxD and are part of the port 3 group (P3.0 and P3.1)
 - These pins are TTL compatible; therefore, they require a line driver to make them RS232 compatible

8051 CONNECTION TO RS232

MAX232

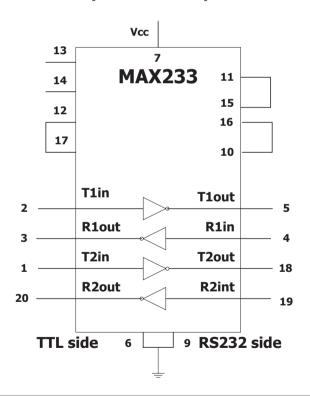
 We need a line driver (voltage converter) to convert the R232's signals to TTL voltage levels that will be acceptable to 8051's TxD and RxD pins

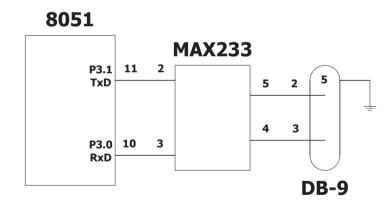


8051 CONNECTION TO RS232

MAX233

- To save board space, some designers use MAX233 chip from Maxim
 - MAX233 performs the same job as MAX232 but eliminates the need for capacitors
 - Notice that MAX233 and MAX232 are not pin compatible







- To allow data transfer between the PC and an 8051 system without any error, we must make sure that the baud rate of 8051 system matches the baud rate of the PC s COM port
- Hyperterminal function supports baud rates much higher than listed below

PC Baud Rates

110	
150	
300	
600	
1200	
2400	
4800	
9600	
19200	

Baud rates supported by 486/Pentium IBM PC BIOS



SERIAL COMMUNICA-TION PROGRAMMING (cont')

With XTAL = 11.0592 MHz, find the TH1 value needed to have the following baud rates. (a) 9600 (b) 2400 (c) 1200

Solution:

The machine cycle frequency of 8051 = 11.0592 / 12 = 921.6 kHz, and 921.6 kHz / 32 = 28,800 Hz is frequency by UART to timer 1 to set baud rate.

(a)
$$28,800 / 3 = 9600$$

where -3 = FD (hex) is loaded into TH1

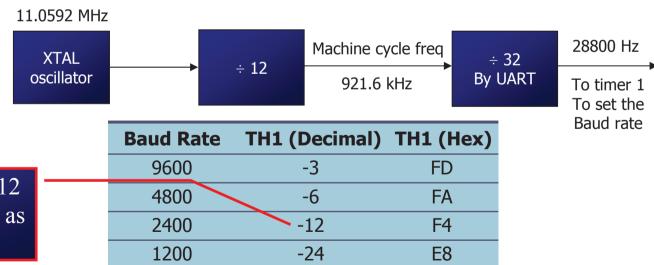
(b)
$$28,800 / 12 = 2400$$

where -12 = F4 (hex) is loaded into TH1

(c)
$$28,800 / 24 = 1200$$

where -24 = E8 (hex) is loaded into TH1

Notice that dividing 1/12 of the crystal frequency by 32 is the default value upon activation of the 8051 RESET pin.



TF is set to 1 every 12 ticks, so it functions as a frequency divider

HANEL



SBUF Register

- SBUF is an 8-bit register used solely for serial communication
 - For a byte data to be transferred via the TxD line, it must be placed in the SBUF register
 - The moment a byte is written into SBUF, it is framed with the start and stop bits and transferred serially via the TxD line
 - SBUF holds the byte of data when it is received by 8051 RxD line
 - When the bits are received serially via RxD, the 8051 deframes it by eliminating the stop and start bits, making a byte out of the data received, and then placing it in SBUF

```
MOV SBUF, #'D' ;load SBUF=44h, ASCII for 'D' MOV SBUF, A ;copy accumulator into SBUF MOV A, SBUF ;copy SBUF into accumulator
```



SCON Register

 SCON is an 8-bit register used to program the start bit, stop bit, and data bits of data framing, among other things

	SM0	SM1	SM2	REN	TB8	RB8	TI	RI
SM0 SCON.7 Serial port mode specifier								
	SCON.	•	Serial por					
SM2	SCON.	.5	Used for		L	nmunicat	ion	
REN	SCON		Set/cleare	-				otion
TB8	SCON	.3	Not wide	ly used			•	
RB8	SCON	.2	Not wide	ly used				
TI	SCON.	.1	Transmit	interrupt	flag. Set	by HW at	the	
	begin of the stop bit mode 1. And cleared by SW							
RI	SCON.	0	Receive i	nterrupt f	lag. Set b	y HW at	the	
			begin of t	he stop b	it mode 1	. And clea	ared by S	W
Note: Make SM2, TB8, and RB8 =0								



SCON Register (cont')

□ SM0, SM1

They determine the framing of data by specifying the number of bits per character, and the start and stop bits

SM0	SM1			
0	0	Serial Mode 0		
0	1	Serial Mode 1, 8-bit 1 stop bit, 1 start b		
1	0	Serial Mode 2	0.1	
1	1	Serial Mode 3	Only mode 1 is of interest to us	
12			01 1111	crest to us

□ SM2

➤ This enables the multiprocessing capability of the 8051

SCON Register (cont')

REN (receive enable)

- > It is a bit-adressable register
 - When it is high, it allows 8051 to receive data on RxD pin
 - If low, the receiver is disable

TI (transmit interrupt)

- When 8051 finishes the transfer of 8-bit character
 - It raises TI flag to indicate that it is ready to transfer another byte
 - TI bit is raised at the beginning of the stop bit

RI (receive interrupt)

- When 8051 receives data serially via RxD, it gets rid of the start and stop bits and places the byte in SBUF register
 - It raises the RI flag bit to indicate that a byte has been received and should be picked up before it is lost
 - RI is raised halfway through the stop bit



Programming Serial Data Transmitting

- In programming the 8051 to transfer character bytes serially
 - 1. TMOD register is loaded with the value 20H, indicating the use of timer 1 in mode 2 (8-bit auto-reload) to set baud rate
 - 2. The TH1 is loaded with one of the values to set baud rate for serial data transfer
 - 3. The SCON register is loaded with the value 50H, indicating serial mode 1, where an 8-bit data is framed with start and stop bits
 - 4. TR1 is set to 1 to start timer 1
 - 5. TI is cleared by CLR TI instruction
 - 6. The character byte to be transferred serially is written into SBUF register
 - 7. The TI flag bit is monitored with the use of instruction JNB TI, xx to see if the character has been transferred completely
 - 8. To transfer the next byte, go to step 5



Programming
Serial Data
Transmitting
(cont')

Write a program for the 8051 to transfer letter "A" serially at 4800 baud, continuously.

Solution:

```
TMOD, #20H ; timer 1, mode 2 (auto reload)
      MOV
           TH1, #-6 ; 4800 baud rate
      VOM
      VOM
           SCON, #50H ;8-bit, 1 stop, REN enabled
      SETB TR1
                  ;start timer 1
AGAIN: MOV SBUF, #"A" ; letter "A" to transfer
      JNB TI, HERE ; wait for the last bit
HERE:
      CLR
           ΤI
                      ; clear TI for next char
                      ; keep sending A
      SJMP AGAIN
```



Programming
Serial Data
Transmitting
(cont')

Write a program for the 8051 to transfer "YES" serially at 9600 baud, 8-bit data, 1 stop bit, do this continuously

Solution:

```
MOV TMOD, #20H ; timer 1, mode 2 (auto reload)
      MOV TH1, \#-3
                      ;9600 baud rate
      MOV SCON, #50H
                      ;8-bit, 1 stop, REN enabled
      SETB TR1
                      start timer 1
AGAIN: MOV A, #"Y"
                      ;transfer "Y"
      ACALL TRANS
                      ;transfer "E"
      MOV A,#"E"
      ACALL TRANS
                      ;transfer "S"
      MOV A, #"S"
      ACALL TRANS
                      ; keep doing it
      SJMP AGAIN
; serial data transfer subroutine
TRANS: MOV SBUF, A ; load SBUF
      JNB TI, HERE
                      ; wait for the last bit
HERE:
      CI<sub>1</sub>R TT
                      ; get ready for next byte
       RET
```



Importance of TI Flag

- The steps that 8051 goes through in transmitting a character via TxD
 - 1. The byte character to be transmitted is written into the SBUF register
 - 2. The start bit is transferred
 - 3. The 8-bit character is transferred on bit at a time
 - 4. The stop bit is transferred
 - It is during the transfer of the stop bit that 8051 raises the TI flag, indicating that the last character was transmitted
 - 5. By monitoring the TI flag, we make sure that we are not overloading the SBUF
 - If we write another byte into the SBUF before TI is raised, the untransmitted portion of the previous byte will be lost
 - 6. After SBUF is loaded with a new byte, the TI flag bit must be forced to 0 by CLR TI in order for this new byte to be transferred



Importance of TI Flag (cont')

- By checking the TI flag bit, we know whether or not the 8051 is ready to transfer another byte
 - > It must be noted that TI flag bit is raised by 8051 itself when it finishes data transfer
 - ▶ It must be cleared by the programmer with instruction CLR TI
 - ➤ If we write a byte into SBUF before the TI flag bit is raised, we risk the loss of a portion of the byte being transferred
- The TI bit can be checked by
 - ➤ The instruction JNB TI, XX
 - Using an interrupt



Programming Serial Data Receiving

- In programming the 8051 to receive character bytes serially
 - 1. TMOD register is loaded with the value 20H, indicating the use of timer 1 in mode 2 (8-bit auto-reload) to set baud rate
 - 2. TH1 is loaded to set baud rate
 - 3. The SCON register is loaded with the value 50H, indicating serial mode 1, where an 8-bit data is framed with start and stop bits
 - 4. TR1 is set to 1 to start timer 1
 - 5. RI is cleared by CLR RI instruction
 - 6. The RI flag bit is monitored with the use of instruction JNB RI, xx to see if an entire character has been received yet
 - 7. When RI is raised, SBUF has the byte, its contents are moved into a safe place
 - 8. To receive the next character, go to step 5



Programming
Serial Data
Receiving
(cont')

Write a program for the 8051 to receive bytes of data serially, and put them in P1, set the baud rate at 4800, 8-bit data, and 1 stop bit

Solution:

```
TMOD, #20H ; timer 1, mode 2 (auto reload)
      MOV
      MOV TH1, \#-6 ; 4800 baud rate
           SCON, #50H ;8-bit, 1 stop, REN enabled
      MOV
      SETB TR1
                start timer 1
HERE: JNB RI, HERE ; wait for char to come in
      MOV A, SBUF
                      ; saving incoming byte in A
      MOV P1,A
                      ; send to port 1
      CLR RT
                      ; get ready to receive next
                      ;byte
                      ; keep getting data
      SJMP HERE
```



Programming
Serial Data
Receiving
(cont')

Example 10-5

Assume that the 8051 serial port is connected to the COM port of IBM PC, and on the PC, we are using the terminal exe program to send and receive data serially. P1 and P2 of the 8051 are connected to LEDs and switches, respectively. Write an 8051 program to (a) send to PC the message "We Are Ready", (b) receive any data send by PC and put it on LEDs connected to P1, and (c) get data on switches connected to P2 and send it to PC serially. The program should perform part (a) once, but parts (b) and (c) continuously, use 4800 baud rate.

Solution:

```
ORG 0
MOV P2,#0FFH ;make P2 an input port
MOV TMOD,#20H ;timer 1, mode 2
MOV TH1,#0FAH ;4800 baud rate
MOV SCON,#50H ;8-bit, 1 stop, REN enabled
SETB TR1 ;start timer 1
MOV DPTR,#MYDATA ;load pointer for message
H_1: CLR A
MOV A,@A+DPTR ;get the character
```



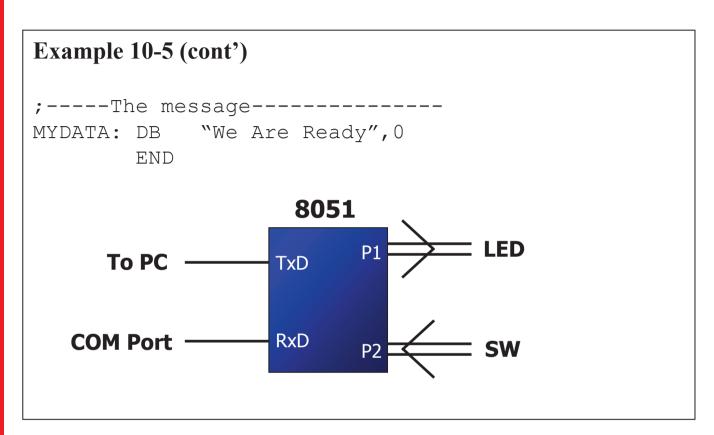
Programming
Serial Data
Receiving
(cont')

```
Example 10-5 (cont')
```

```
JZ B 1 ; if last character get out
      ACALL SEND ; otherwise call transfer
      TNC DPTR
                 ;next one
      SJMP H 1 ;stay in loop
B 1: MOV a, P2 ; read data on P2
     ACALL SEND ; transfer it serially
      ACALL RECV ; get the serial data
      MOV P1, A ; display it on LEDs
      SJMP B 1 ;stay in loop indefinitely
;----serial data transfer. ACC has the data-----
SEND: MOV SBUF, A ; load the data
H 2:
      JNB TI, H 2 ; stay here until last bit
                    ; gone
      CLR TI
                    ; get ready for next char
      RET
                    ; return to caller
;----Receive data serially in ACC-----
      JNB RI, RECV ; wait here for char
RECV:
      MOV A, SBUF ; save it in ACC
      CLR RI ; get ready for next char
      RET
                    return to caller;
```



Programming
Serial Data
Receiving
(cont')





Importance of RI Flag

- In receiving bit via its RxD pin, 8051 goes through the following steps
 - 1. It receives the start bit
 - Indicating that the next bit is the first bit of the character byte it is about to receive
 - 2. The 8-bit character is received one bit at time
 - 3. The stop bit is received
 - When receiving the stop bit 8051 makes RI = 1, indicating that an entire character byte has been received and must be picked up before it gets overwritten by an incoming character

Importance of RI Flag (cont')

(cont')

- 4. By checking the RI flag bit when it is raised, we know that a character has been received and is sitting in the SBUF register
 - We copy the SBUF contents to a safe place in some other register or memory before it is lost
- 5. After the SBUF contents are copied into a safe place, the RI flag bit must be forced to 0 by CLR RI in order to allow the next received character byte to be placed in SBUF
 - Failure to do this causes loss of the received character



Importance of RI Flag (cont')

- By checking the RI flag bit, we know whether or not the 8051 received a character byte
 - If we failed to copy SBUF into a safe place, we risk the loss of the received byte
 - > It must be noted that RI flag bit is raised by 8051 when it finish receive data
 - ➤ It must be cleared by the programmer with instruction CLR RI
 - ▶ If we copy SBUF into a safe place before the RI flag bit is raised, we risk copying garbage
- The RI bit can be checked by
 - ➤ The instruction JNB RI, XX
 - Using an interrupt



Doubling Baud Rate

- There are two ways to increase the baud rate of data transfer

 The system crystal is fix
 - > To use a higher frequency crystal
 - > To change a bit in the PCON register
- PCON register is an 8-bit register
 - > When 8051 is powered up, SMOD is zero
 - We can set it to high by software and thereby double the baud rate

```
SMOD -- -- GF1 GF0 PD IDL
```

It is not a bitaddressable register

```
MOV A,PCON ;place a copy of PCON in ACC SETB ACC.7 ;make D7=1 MOV PCON,A ;changing any other bits
```



57600 Hz SMOD = 111.0592 MHz ÷ 16 To timer Machine cycle freq XTAL 1 To set ÷ 12 oscillator 921.6 kHz the Baud 28800 Hz rate ÷ 32 SMOD = 0

Doubling Baud Rate (cont')

Baud Rate comparison for SMOD=0 and SMOD=1

TH1	(Decimal)	(Hex)	SMOD=0	SMOD=1
	-3	FD	9600	19200
	-6	FA	4800	9600
	-12	F4	2400	4800
	-24	E8	1200	2400



Doubling Baud Rate (cont')

Example 10-6

Assume that XTAL = 11.0592 MHz for the following program, state (a) what this program does, (b) compute the frequency used by timer 1 to set the baud rate, and (c) find the baud rate of the data transfer.

```
MOV A, PCON ; A=PCON
      MOV ACC.7 ; make D7=1
      MOV PCON, A ; SMOD=1, double baud rate
                     ; with same XTAL freq.
      MOV TMOD, #20H ; timer 1, mode 2
      MOV TH1, -3 ; 19200 (57600/3 = 19200)
      MOV SCON, #50H ;8-bit data, 1 stop bit, RI
                     :enabled
      SETB TR1
                     start timer 1
      MOV A, #"B" ; transfer letter B
      CLR TI
               ;make sure TI=0
A 1:
      MOV SBUF, A ; transfer it
      JNB TI, H 1 ; stay here until the last
H 1:
                     ;bit is gone
                     ; keep sending "B" again
      SJMP A 1
```



Doubling Baud Rate (cont')

Example 10-6 (cont')

Solution:

- (a) This program transfers ASCII letter B (01000010 binary) continuously
- (b) With XTAL = 11.0592 MHz and SMOD = 1 in the above program, we have:

```
11.0592 / 12 = 921.6 kHz machine cycle frequency. 921.6 / 16 = 57,600 Hz frequency used by timer 1 to set the baud rate. 57600 / 3 = 19,200, the baud rate.
```

Find the TH1 value (in both decimal and hex) to set the baud rate to each of the following. (a) 9600 (b) 4800 if SMOD=1. Assume that XTAL 11.0592 MHz

Solution:

With XTAL = 11.0592 and SMOD = 1, we have timer frequency = 57,600 Hz.

- (a) 57600 / 9600 = 6; so TH1 = -6 or TH1 = FAH
- (b) 57600 / 4800 = 12; so TH1 = -12 or TH1 = F4H



Doubling Baud Rate (cont')

Example 10-8

Find the baud rate if TH1 = -2, SMOD = 1, and XTAL = 11.0592 MHz. Is this baud rate supported by IBM compatible PCs?

Solution:

With XTAL = 11.0592 and SMOD = 1, we have timer frequency = 57,600 Hz. The baud rate is 57,600/2 = 28,800. This baud rate is not supported by the BIOS of the PCs; however, the PC can be programmed to do data transfer at such a speed. Also, HyperTerminal in Windows supports this and other baud rates.



Doubling Baud Rate (cont')

Example 10-10

Write a program to send the message "The Earth is but One Country" to serial port. Assume a SW is connected to pin P1.2.

Monitor its status and set the baud rate as follows:

SW = 0, 4800 baud rate

SW = 1,9600 baud rate

Assume XTAL = 11.0592 MHz, 8-bit data, and 1 stop bit.

```
BIT P1.2
      SW
      ORG
                     ;starting position
           0Н
MAIN:
      MOV TMOD, #20H
      MOV TH1, \#-6
                     ;4800 baud rate (default)
           SCON, #50H
      MOV
      SETB TR1
      SETB SW
              ; make SW an input
      JNB SW, SLOWSP ; check SW status
S1:
      MOV A, PCON ; read PCON
      SETB ACC.7 ;set SMOD high for 9600
      MOV PCON, A ; write PCON
      SJMP OVER ; send message
```



Doubling Baud Rate (cont')

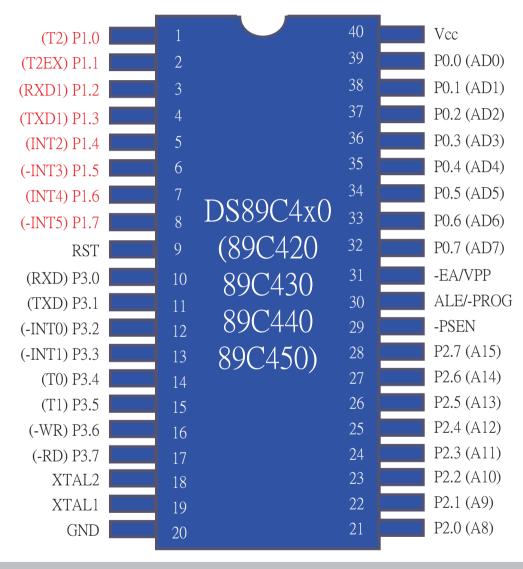
```
SLOWSP:
      MOV A, PCON ; read PCON
      SETB ACC.7 ;set SMOD low for 4800
      MOV PCON, A ; write PCON
      MOV DPTR, #MESS1 ; load address to message
OVER:
     CT<sub>1</sub>R A
FN:
      MOVC A, @A+DPTR ; read value
      J7
           s1 ; check for end of line
      ACALL SENDCOM ; send value to serial port
      INC DPTR ; move to next value
      SJMP FN
              ;repeat
SENDCOM:
      MOV SBUF, A ; place value in buffer
HERE: JNB TI, HERE ; wait until transmitted
      CLR TI
              ;clear
      RET
                     :return
MESS1: DB "The Earth is but One Country", 0
      END
```



- Many new generations of 8051 microcontroller come with two serial ports, like DS89C4x0 and DS80C320
 - ➤ The second serial port of DS89C4x0 uses pins P1.2 and P1.3 for the Rx and Tx lines
 - The second serial port uses some reserved SFR addresses for the SCON and SBUF
 - There is no universal agreement among the makers as to which addresses should be used
 - The SFR addresses of C0H and C1H are set aside for SBUF and SCON of DS89C4x0
 - The DS89C4x0 technical documentation refers to these registers as SCON1 and SBUF1
 - The first ones are designated as SCON0 and SBUF0



DS89C4x0 pin diagram





SFR Byte Addresses for DS89C4x0 Serial Ports

SFR	First Serial Port	Second Serial Port
(byte address)		
SCON	SCON0 = 98H	SCON1 = COH
SBUF	SBUF0 = 99H	SBUF1 = C1H
TL	TL1 = 8BH	TL1 = 8BH
TH	TH1 = 8DH	TH1 = 8DH
TCON	TCON0 = 88H	TCON0 = 88H
PCON	PCON = 87H	PCON = 87H



- Upon reset, DS89c4x0 uses Timer 1 for setting baud rate of both serial ports
 - While each serial port has its own SCON and SBUF registers, both ports can use Timer1 for setting the baud rate
 - SBUF and SCON refer to the SFR registers of the first serial port
 - Since the older 8051 assemblers do not support this new second serial port, we need to define them in program
 - To avoid confusion, in DS89C4x0 programs we use SCON0 and SBUF0 for the first and SCON1 and SBUF1for the second serial ports

Example 10-11

Write a program for the second serial port of the DS89C4x0 to continuously transfer the letter "A" serially at 4800 baud. Use 8-bit data and 1 stop bit. Use Timer 1.

```
SBUF1 EQU OC1H ;2nd serial SBUF addr
      SCON1 EOU OCOH ; 2nd serial SCON addr
      TI1 BIT OC1H ;2nd serial TI bit addr
      RI1 BIT OCOH
                     ;2nd serial RI bit addr
     ORG
          ОН
                      ; starting position
MAIN:
          TMOD, #20H ; COM2 uses Timer 1 on reset
     MOV
          TH1, #-6 ;4800 baud rate
     MOV
     MOV
          SCON1, #50H ;8-bit, 1 stop, REN enabled
                      start timer 1
      SETB TR1
AGAIN:MOV A, #"A"
                     ;send char 'A'
     ACALL SENDCOM2
      SJMP AGAIN
SENDCOM2:
     VOM
          SBUF1, A ; COM2 has its own SBUF
HERE: JNB
          TI1, HERE
                     ; COM2 has its own TI flag
      CLR
          TT1
      RET
      END
```



Example 10-14

Assume that a switch is connected to pin P2.0. Write a program to monitor the switch and perform the following:

- (a) If SW = 0, send the message "Hello" to the Serial #0 port
- (b) If SW = 1, send the message "Goodbye" to the Serial #1 port.

```
SCON1 EQU OCOH
       TT1 BTT OC1H
       SW1 BIT P2.0
       ORG OH
                       ;starting position
       MOV TMOD, #20H
       MOV TH1, \#-3; 9600 baud rate
       MOV SCON, #50H
       MOV SCON1, #50H
       SETB TR1
       SETB SW1
                  ; make SW1 an input
            SW1, NEXT ; check SW1 status
S1:
       JB
       MOV DPTR, #MESS1; if SW1=0 display "Hello"
       CT<sub>1</sub>R A
FN:
       MOVC A, @A+DPTR ; read value
       JZ
            S1
                 ; check for end of line
       ACALL SENDCOM1 ; send to serial port
       TNC DPTR
                       ; move to next value
       SJM FN
```



```
NEXT: MOV DPTR, #MESS2; if SW1=1 display "Goodbye"
I_1N:
      CLR A
       MOVC A, @A+DPTR ; read value
                  ; check for end of line
       JΖ
            S1
       ACALL SENDCOM2 ; send to serial port
       INC DPTR : move to next value
       SJM JN
SENDCOM1:
       MOV SBUF, A ; place value in buffer
HERE: JNB TI, HERE ; wait until transmitted
                      :clear
       CI<sub>1</sub>R TT
       RET
SENDCOM2:
       MOV SBUF1, A ; place value in buffer
HERE1: JNB TI1, HERE1 ; wait until transmitted
       CLR TT1
                      :clear
       RET
MESS1: DB "Hello", 0
MESS2: DB "Goodbye", 0
       END
```



Transmitting and Receiving Data

Example 10-15

Write a C program for 8051 to transfer the letter "A" serially at 4800 baud continuously. Use 8-bit data and 1 stop bit.



Transmitting and Receiving Data (cont')

Example 10-16

Write an 8051 C program to transfer the message "YES" serially at 9600 baud, 8-bit data, 1 stop bit. Do this continuously.

```
#include <req51.h>
void SerTx(unsigned char);
void main(void) {
  TMOD=0x20; //use Timer 1, mode 2
  TH1=0xFD;
                     //9600 baud rate
  SCON=0x50;
 TR1=1:
                     //start timer
  while (1) {
    SerTx('Y');
    SerTx('E');
    SerTx('S');
void SerTx(unsigned char x) {
 SBUF=x; //place value in buffer while (TI==0); //wait until transmitted
  TI=0:
```



Transmitting and Receiving Data (cont')

Example 10-17

Program the 8051 in C to receive bytes of data serially and put them in P1. Set the baud rate at 4800, 8-bit data, and 1 stop bit.



Transmitting and Receiving Data (cont')

Example 10-19

Write an 8051 C Program to send the two messages "Normal Speed" and "High Speed" to the serial port. Assuming that SW is connected to pin P2.0, monitor its status and set the baud rate as follows:

```
SW = 0, 28,800 baud rate

SW = 1, 56K baud rate

Assume that XTAI = 11,0592 \text{ M}
```

Assume that XTAL = 11.0592 MHz for both cases.



Transmitting and Receiving Data (cont')

```
if(MYSW==0) {
  for (z=0; z<12; z++) {
    SBUF=Mess1[z]; //place value in buffer
    while(TI==0); //wait for transmit
    TI=0;
else {
  PCON=PCON|0x80; //for high speed of 56K
  for (z=0; z<10; z++) {
    SBUF=Mess2[z]; //place value in buffer
    while(TI==0); //wait for transmit
    TI=0;
```



C Compilers and the Second Serial Port

Example 10-20

Write a C program for the DS89C4x0 to transfer the letter "A" serially at 4800 baud continuously. Use the second serial port with 8-bit data and 1 stop bit. We can only use Timer 1 to set the baud rate.



C Compilers and the Second Serial Port

Example 10-21

Program the DS89C4x0 in C to receive bytes of data serially via the second serial port and put them in P1. Set the baud rate at 9600, 8-bit data and 1 stop bit. Use Timer 1 for baud rate generation.

