

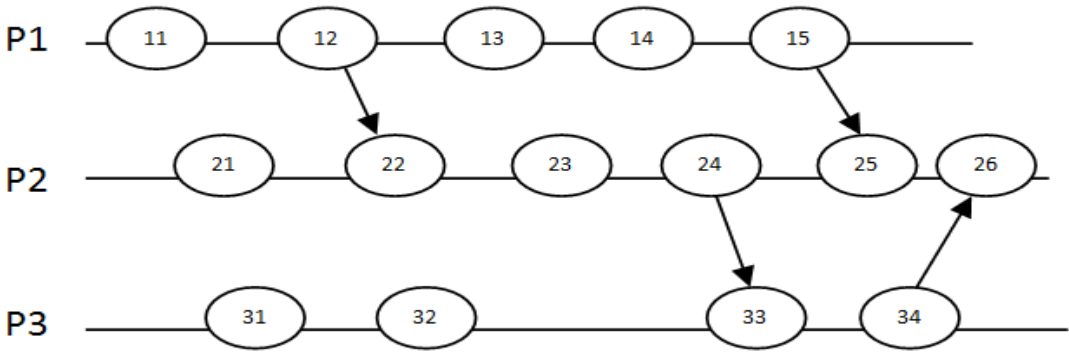
Sri Sivasubramaniya Nadar College of Engineering, Kalavakkam – 603 110

(An Autonomous Institution, Affiliated to Anna University, Chennai)

Department of Computer Science and Engineering**Continuous Assessment Test – I****Question Paper**

Degree & Branch	BE - CSE				Semester	7
Subject Code & Name	UCS1701 - Distributed Systems				Regulation: 2018	
Academic Year	2021-2022	Batch	2018-2022	Date	09-Aug-2021	FN / AN
Time: 90 Minutes	Answer All Questions				Maximum: 50 Marks	

Part – B (20 Marks)**Answer any two questions**

<KL2 > <KL3 >	<p>11. i) State the need for logical clock and explain the working of Lamport's logical clock with the set of inference rules for updating the clock values. [5]</p> <p>ii) Consider the following Time Space diagram and apply Lamport's Logical Clock to compute clock values for all the events. [5]</p> 	<CO2 >
<KL2 > <KL3 >	<p>12. i) State the need for vector clock and explain the working of vector clock with the set of inference rules for updating the clock values. [5]</p> <p>ii) Consider the following Time Space diagram and apply vector clock to compute clock values for all the events. [5]</p>	<CO2 >

	<p> P1 — 11 — 12 — 13 — 14 — 15 — P2 — 21 — 22 — 23 — 24 — 25 — 26 — P3 — 31 — 32 — 33 — 34 — </p>	
<KL2 >	13. i) Outline the reason for physical clocks are not suitable for Distributed systems? [5] ii) Explain about the early physical clock synchronization methods and the effects of Round Trip Time. [5]	<CO1 >

Prepared By	Reviewed By	Approved By
Course Coordinator	PAC Team	HOD