Saber Mahmoud Saber Mahmoud

📞 Mobile: 01119899016 | 📧 -E-mail | 🌐 portfolio

Objective

I am a passionate engineer specializing in digital electronics and embedded systems. Seeking to apply

 technical knowledge through practical, real-world projects in a professional environment.

Education

 Cairo University - Faculty of Engineering.

 B.Sc. in Electronics & Electrical Communications Engineering.

 Graduated: 2025 | Grade: Very Good

Technical Skills

HDL: Verilog, SystemVerilog, SVA, UVM- Simulation.

Tools: QuestaSim, Vivado, SpyGlass.

Personal Skills

- Excellent English, Self-motivated & quick learner- Strong team player with leadership capabilities- Effective presenter and communicator- Highly sociable and adaptive

Courses & Internships

 Digital Design Intern at Si-Vision Jul 2024 – Oct 2024.

Digital Verification Using System Verilog and UVM Under the Supervision of Eng. Kareem Waseem

Aug 2023 – Oct 2023.

Digital Design using Verilog HDL and FPGA flow using Vivado under supervision of Eng. Kareem Waseem

Jul 2023 - Aug 2023

Full Embedded Systems Diploma under supervision of Eng/Mohamed Tarek covering:

Projects

Graduation project Accelerated YOLO-Based computer vision system enhanced with AXI-4 (Mentored by Electronic Research Institute (ERI) and Funded by ITIDA).

Developed the AXI-4 DMA (direct memory access) in Verilog.

Design and Verification for Ultrasonic System to handle sending and receiving ultrasonic waves from the appropriate transducers.

Designed a 32-point FFT using Cooley-Tukey Radix-2 DIT algorithm in Verilog.

UVM Verification of SPI Wrapper with Dual Port RAM Integration| Digital Verification Project.

Developed a comprehensive smart car system integrating multiple subsystems, including lighting control and parking assistance.