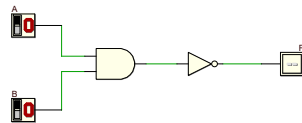


2 Input AND gate

Logic Equation:
 $F = AB$

Input Output

A	B	F
0	0	0
0	1	0
1	0	0
1	1	1

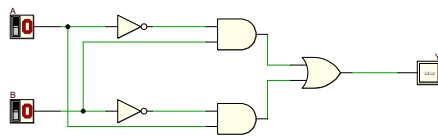


2 Input NAND gate

Logic Equation:
 $F = (AB) \text{bar}$

Input Output

A	B	F
0	0	1
0	1	1
1	0	1
1	1	0



2 Input XOR gate

Logic Equation:
 $Y = A \text{ xor } B = A\text{bar} B + A B\text{bar}$

Input Output

A	B	Y
0	0	0
0	1	1
1	0	1
1	1	0