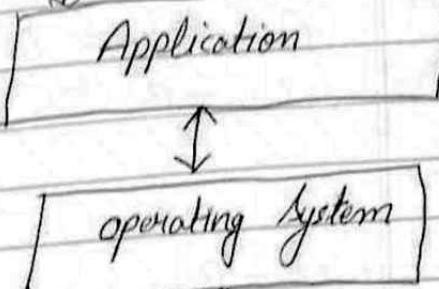


Operating System

Operating System is a system software which acts as an intermediate b/w user & hardware.

If we don't have any operating system than we have to write programs for each and every task.

user1, user2 ... user3



operating system



Hardware

[CPU] [I/O] [RAM]

Functions of operating system

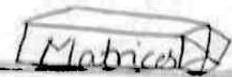
- ① Resource management
- ② Process management (CPU Scheduling)
- ③ Storage management (H.D) → file System
- ④ Memory management (RAM)
- ⑤ Security

Windows uses Kerberos Security Protocol which uses the password to provide safe access of the file to the user.

When any process calls or executes an instruction outside its segment or block, then the process on the whole gets totally blocked.

The privacy & security is maintained even among the processes so that they don't interfere or interchange data with each other.

P. → P_{in}

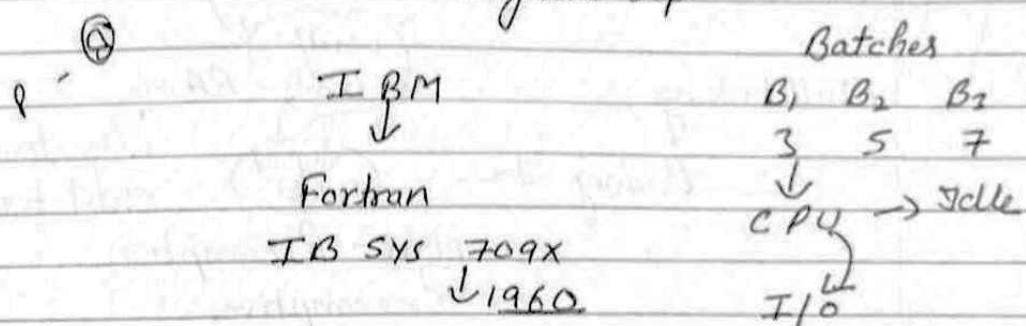


Types of operating system

① Batch

Punch cards

Paper tape → operator
magnetic tape



- Multiprogrammed OS → Non-Preemptive

↓ Silliness Q.A.M

In this type C.P.U exec executes at the process one by one completely & than turn on to next process. Suppose we have 4 processes to execute P_1, P_2, P_3, P_4 . first it executes the P_1 completely & than turn on P_2 then P_3 & so on. but if process P_1 said that I have to go for input output instruction if it happens in that case C.P.U turn on to process P_2 .

• Multitasking → Time sharing

↓ Response time

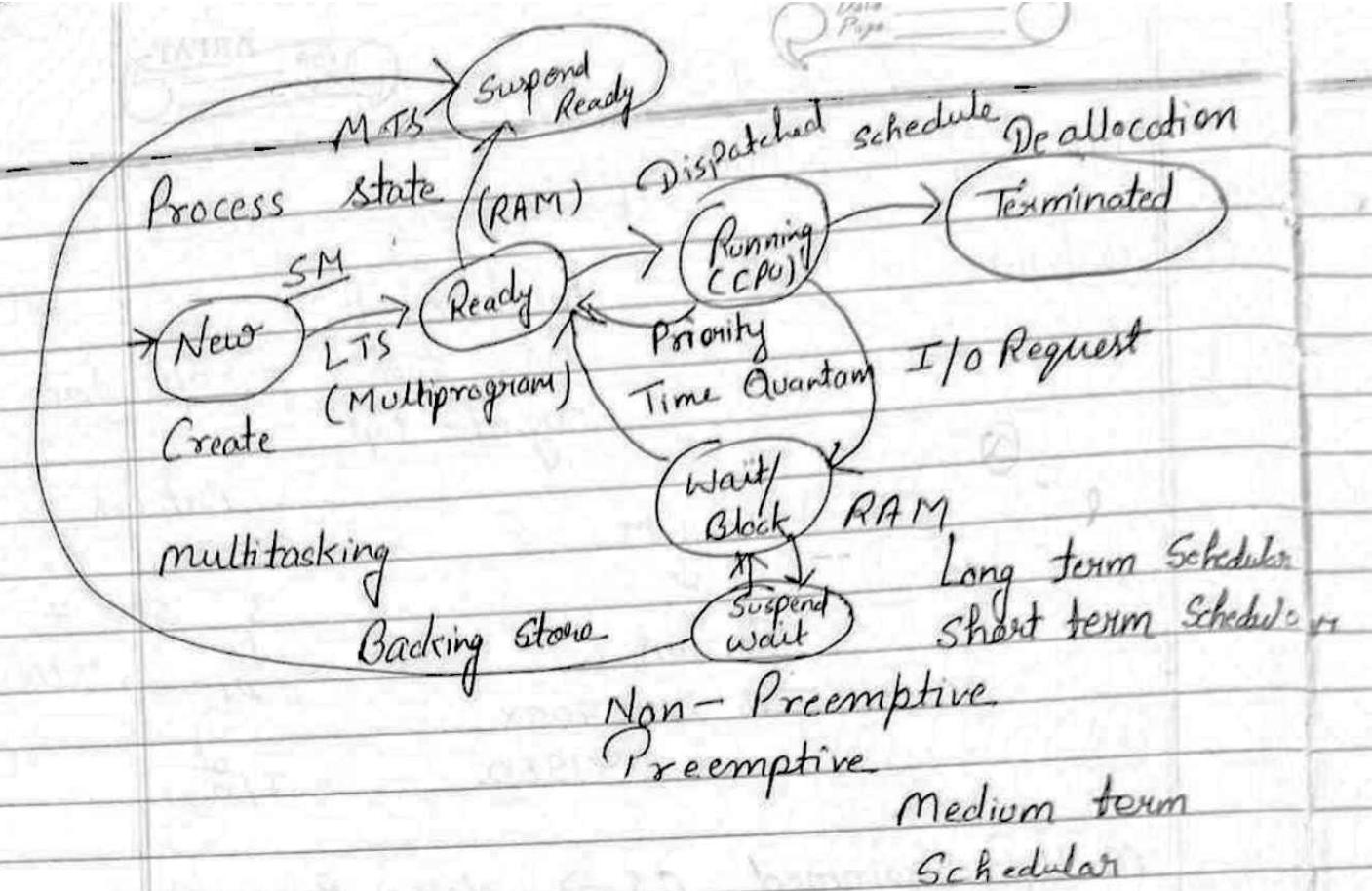
Responsiveness



Distributed →

Cluster →

Embedded →



when a process executes it passes through different stages. These stages may differ in different operating system.

Process Scheduling Algorithm

Pre-Emptive

SRTF (Shortest Remaining time first)

LRTF (longest Remaining time first)

Round Robin

Priority end

Non Pre-Emptive

FCFS (First come First serve)
SJF (Shortest job first)

LJF (longest job first)

HRRN (Highest Response Ratio Next)

Multilevel Queue Priority

C.P.U Scheduling

Arrival time :- The time at which process enters the ready queue or state.

Duration
Burst time :- Time required by a process to get executed by C.P.U.

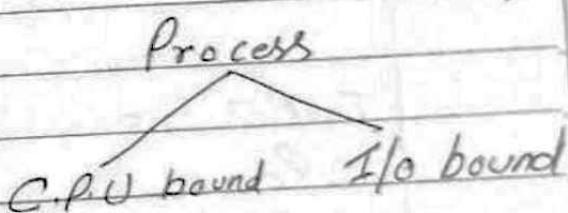
Completion time :- the time at which process complete its execution.

Turn Around Time :- $\{ \text{Completion time} - \text{Arrival time} \}$

Waiting time :- $\{ \text{Turn Around time} - \text{Burst time} \}$

Response time :- $\{ (\text{The time at which a process get CPU first time}) - (\text{Arrival time}) \}$

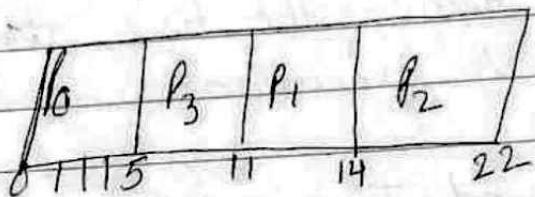
$2^x 3$



Process	AT	B.T
P ₁	1	8 7 5
P ₂	3	2 3
P ₃	6	10 2
P ₄	7	8 10
P ₅	9	8

Priority Scheduling

Priority	AT	BT	Priority
P ₀	0	5	1
P ₁	1	3	2
P ₂	2	8	1
P ₃	3	6	3



$$W.T = R.T - A.T$$

$$P_0 = 0 - 0 = 0$$

$$P_1 = 11 - 1 = 10$$

~~$$P_2 = 14 - 2 = 12$$~~

$$P_3 = 5 - 3 = 2$$

$$T.A.T = C.T - A.T$$

$$P_0 = 5 - 0 = 5$$

$$P_1 = 14 - 1 = 13$$

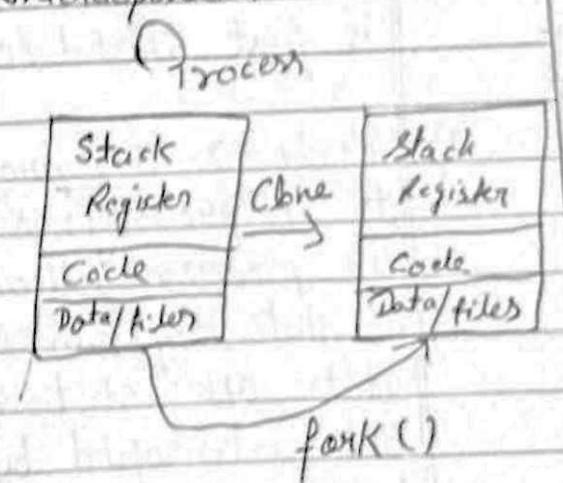
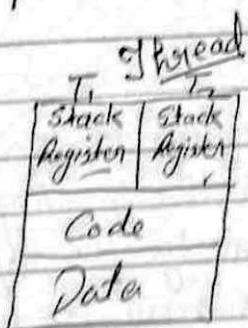
~~$$P_2 = 22 - 2 = 20$$~~

$$P_3 = 11 - 3 = 8$$

Round Robin

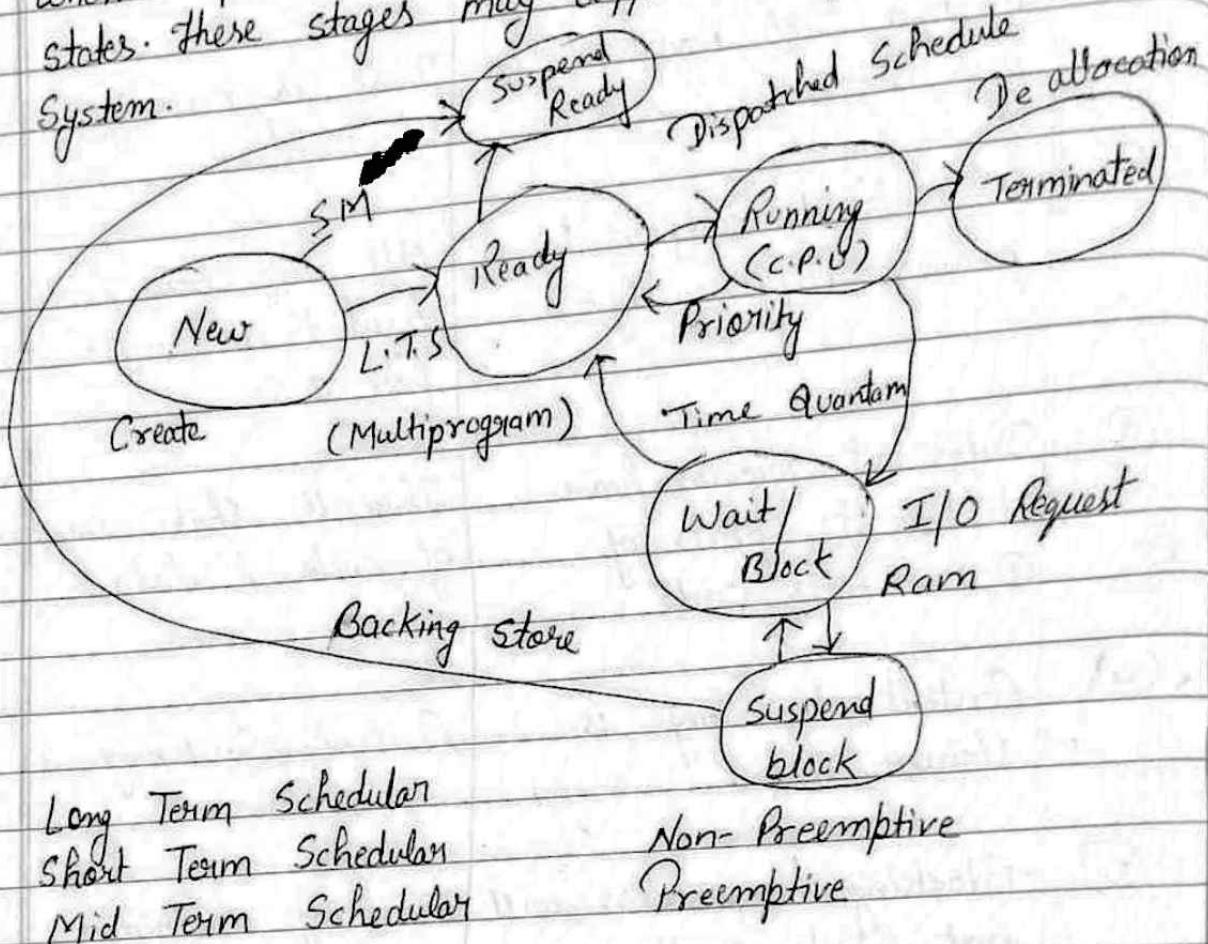
~~Process & Thread~~

Process	Thread
① System call involved in process.	There is no system call involved.
② O.S treats different process differently.	All user level threads treated as single task for O.S.
③ Different process have different Copies of Data, files, code.	Threads share same copy of code & data.
④ Context Switching is slower	Context Switching is faster
⑤ Blocking a process will not block another	Blocking a thread will block entire process
6 Independent	Interdependent



Process State

when a process executes, it passes through different states. These stages may differ in different operating systems.



1) Start → This is initial state when a process is first started / created.

2) Ready → The process is waiting to be assigned to a processor. Ready processes are waiting to have the processor allocated to them by the operating system so that they can run. Process may come into this state after start state or while running it by but interrupted by the scheduler to assign C.P.U. to some other process.

- 3) Running - After Ready state, the process state is set to running and the processor executes its exec. instruction.
- 4) Waiting :- Process moves into the waiting state if it needs to wait for a resources, such as waiting for user input, or waiting for a file to become available.
- 5) Terminated or Exit:- Once the process finishes its execution, or it is terminated by the operating system, it is moved to the terminated state where it is waits to be removed from main memory.

① what is process?

Process is a program in execution including the current values the program counter, Register, & variables. The difference b/w a process the programmers that the program is a group of instruction where as the process is the activity.

we write our program in a text file & when we execute this program, it becomes a process which performs all the task mentioned in the program.

when a program is loaded into the memory and it becomes a process, it can be divide into 4 section - stack, heap, text & data.

- Page _____
- 1) Stack : - The process that contains the temporary data such as Method/ function, parameters, return address & local variables.
 - 2) Heap : - This is dynamically allocated memory to a process during its runtime.
 - 3) Text - This includes the current activity represented by the value of program counter & contents of the processor's Register.
 - 4) Data : - This section contains the all global & static variables.

• Process Table & Process Control Block (PCB):

Q What is process table ?

The operating system manages and control the resources by having a table. Tables are important data structures to store information about every process & resources.

This is the reason the operating system maintain memory tables, input output tables & process table.

The process tables store the ID of every process & corresponding to it, the pointer to its PCB

1. Running

2. Ready

3. Blocked

4. Suspend

Pointers to PCB

Q What is process control Block ? :

A process control block is a data structure maintained by the operating system for every process.

Some operating systems maintain only PCB in that PCB has all entries stored by a process table.

A PCB keeps all the information needed to keep track of a process.

Process ID

State

Pointer

Priority

Program counter

IPU Register

I/O Information

etc

- ① Process State :- Current state of the process i.e., ready, running, or waiting etc.
- ② Process Privileges :- allow / disallow access to the system resources.
- ③ Process Id :- Unique identification of each of the process in the operating system.
- ④ Pointer :- A pointer to parent process.
- ⑤ Program Counter :- Program counter is a pointer to the address of the next instruction to be executed for this process.
- ⑥ CPU Register :- Various C.P.U register where process need to be stored for execution for running state.
- ⑦ C.P.U Scheduling Information
- ⑧ Memory Management Information :- Information of page table, memory limit, Segment table information.
- ⑨ Accounting Information :- It includes the amount of CPU used for process execution, time limit, executing execution ID etc.
- ⑩ Input Output Status Information :- This includes list of input output devices allocated to the process.

The PCB is maintained for a process throughout its lifetime, And is deleted once the process

terminates :

Overview of Inter - Process Communication

IPC = (Inter-Process Communication) is the capability which allows two processes of the computer to communicate with each other.

where will be those process?

It's not important for process to be in the same computer they can be in same computer or in different computer which are connected over the network.

IPC allows one application to control other application, it also allows two applications to share data with each other.

IPC is an important thing in the multiprogramming system, or we can say multiprogramming system depends on this system.

Single process operating system do not use IPC.

Microsoft windows is a Multiprogramming operating system & it uses the IPC. In windows IPC is known as Dynamic Data Exchange System.

Date _____
Page _____

In an OS (Multiprogramming OS) many processes may be running at the same time. In the context of IPC we can divide these processes into two main categories.

1. Independent Process
2. Cooperating Process

Independent Process

As name shows in the multiprogramming OS environment these processes are work independently.

They never ask any other process to share data & never share their own data to some other process.

Although they are in the Multiprogramming OS but they never share data.

Cooperating Process

Name is telling the whole story there are just opposite to the independent process & share the data with other cooperating process into the system.

Different Advantages of process communication

- ① Information sharing
- ② Computation Speed up
- ③ Modularity
- ④ Convenience

Computation speed up

Task get divided into multiple task, all task run parallel & achieve the required result.

Modularity

Divide a function into separate process & threads.

Convenience

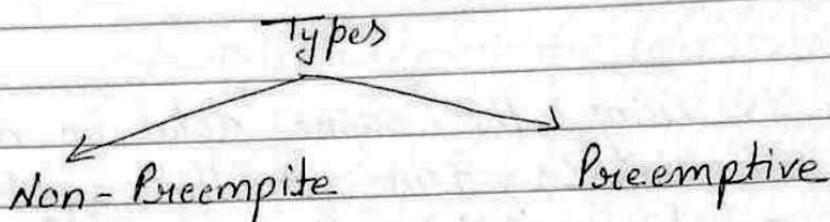
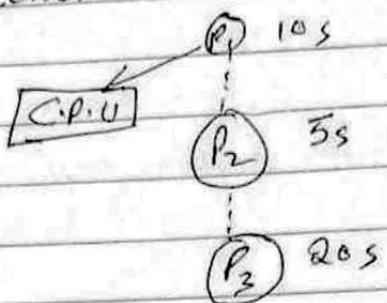
If user is using the same data in different task conflict can be arise so this system try to avoid conflicts.

There are further two memory models in this system.

Shared Memory Model
Message passing System

FCFS (First come first serve) C.P.U Scheduling

C.P.U scheduling is a technique in operating system which handles the removal or selection of processes.



- ① Once a process enters in C.P.U until it terminates other processes have to wait.
- ② Priority is not considered
- ① if a low priority task is currently in the C.P.U and another task with high priority demand C.P.U access. First priority will be removed until high priority task terminated

FCFS Scheduling Algorithm

- works on first come first serve basis.
- It is non-preemptive

Ques Consider following table & calculate Average TAT & Average waiting time using FCFS Algorithm.

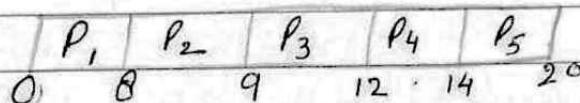
P_i - Execute



Process	Arrival time	Burst time	Priority
P_1	0	8	3
P_2	1	1	1
P_3	2	3	2
P_4	3	2	3
P_5	4	6	4

Ans

Grant chart



P.No.	AT	BT	CT	T.A.T (C.T - AT)	LWT (TAT - CT)
P_1	0	8	8	8	0
P_2	1	1	9	8	7
P_3	2	3	12	10	7
P_4	3	2	14	11	9
P_5	4	6	20	16	10

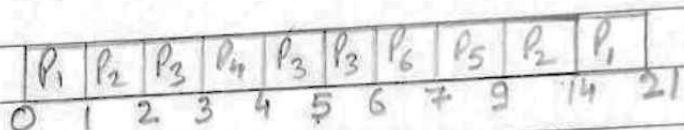
$$\text{Average W.T} = \frac{0+7+7+9+10}{5} = \frac{33}{5} = 6.6$$

$$\text{Average TAT} = \frac{8+8+10+11+16}{5} = \frac{53}{5} = 10.6$$

~~SRTF / SJF~~ (Preemptive)
Shortest remaining time first

P. No.	A.T	B.T	C.T	(CT-AT)	(T.A.T-AT)	W.T
P ₁	0	8	21	21	13	
P ₂	1	6	14	13	7	
P ₃	2	3	6	4	1	
P ₄	3	1	4	1	0	
P ₅	4	2	9	5	3	
P ₆	5	1	7	2	1	

Gantt chart



$$\text{Average T.A.T} = \frac{45}{6} = 7.5$$

$$\text{Average W.T} = \frac{25}{6} = 4.16$$

~~SJF~~ (Non-Preemptive)
(Shortest Job first)

Process No	A.T	Quantum	C.T	T.A.T	W.T
P ₁	0	6	6	6	0
P ₂	2	1	7	5	4
P ₃	4	4	14	10	6
P ₄	5	3	10	5	2

Gantt chart

P_1	P_2	P_3	P_4	P_5
0	6	12	10	14

$$\text{Average TAT} = \frac{26}{4} = 6.5$$

$$\text{Average WT} = \frac{12}{4} = 3$$

Priority Process Scheduling
Round Robin Algorithm (Preemptive.)

Round Robin

Time Quantum : 2 units.

Process Number	Arrival Time	Burst time
P_1	0	3 x 0
P_2	1	3 4 2 0
P_3	2	2 0
P_4	3	2 0
P_5	4	5 3 1 0
P_6	5	4 2 0

Queue $| P_1 P_2 P_3 P_4 P_5 P_6 P_7 P_8 P_9 |$

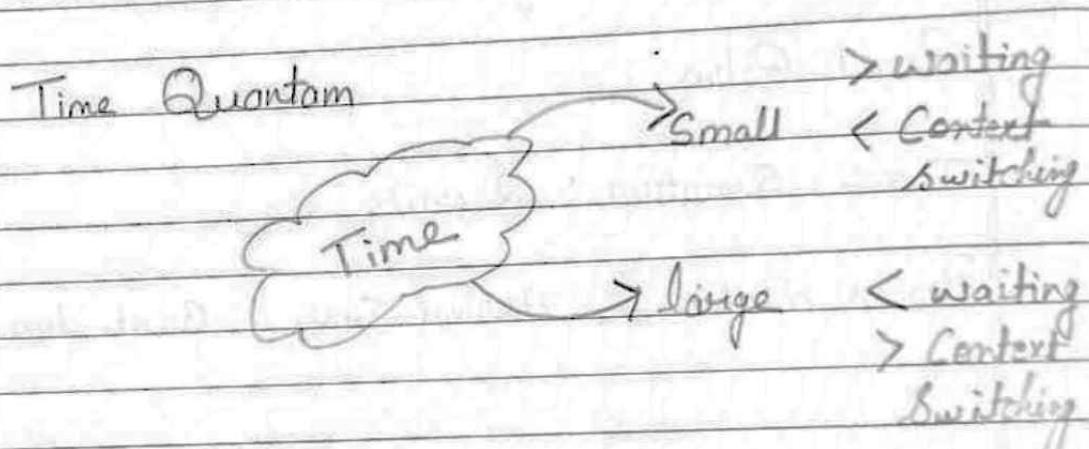
Gantt chart

P_1	P_2	P_3	P_4	P_5	P_6	P_7	P_8	P_9
0	2	4	6	8	9	11	13	15

Process Number	A.T	B.T	C.T	TAT	W.T
1	0	3	7	7	4
2	1	6	19	18	12
3	2	2	6	4	2
4	3	2	9	6	4
5	4	4	22	18	14
6	6	4	21	15	11

Average T.A-T = $\frac{7+18+4+6+18+15}{6} = 11.33$

Average W.T = $\frac{4+12+2+4+14+11}{6} = 7.83$



Priority Process Scheduling

Process No.	A.T	B.T	Priority
P ₁	0	43	2
P ₂	1	210	4
P ₃	2	870	6
P ₄	3	330	10
P ₅	4	40	8
P ₆	5	40	12
P ₇	6	80	9

Gantt chart

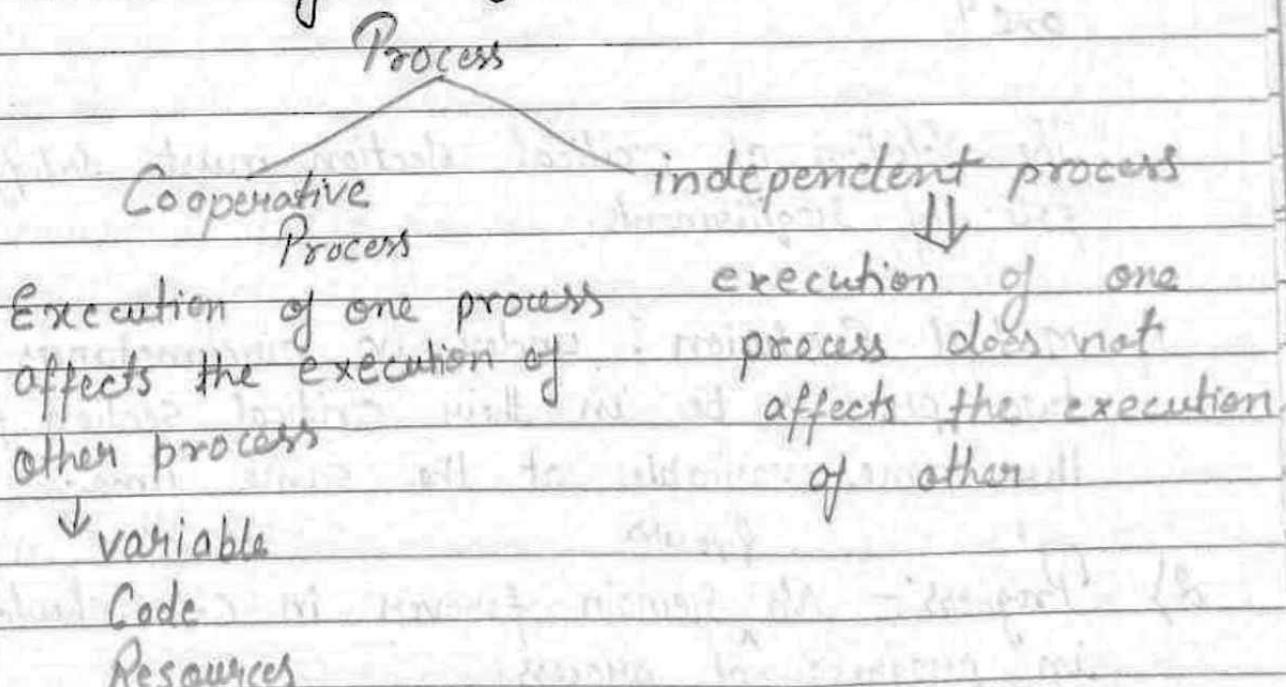
P_1	P_2	P_3	P_4	P_5	P_6	P_7	P_8	P_9	P_{10}	P_{11}	P_{12}
0	1	2	3	5	9	12	18	22	29	30	33

	C.T	T.A.T	W.T
1	33	33	29
2	30	29	27
3	29	27	19
4	12	9	4
5	22	18	14
6	9	4	0
7	18	12	6

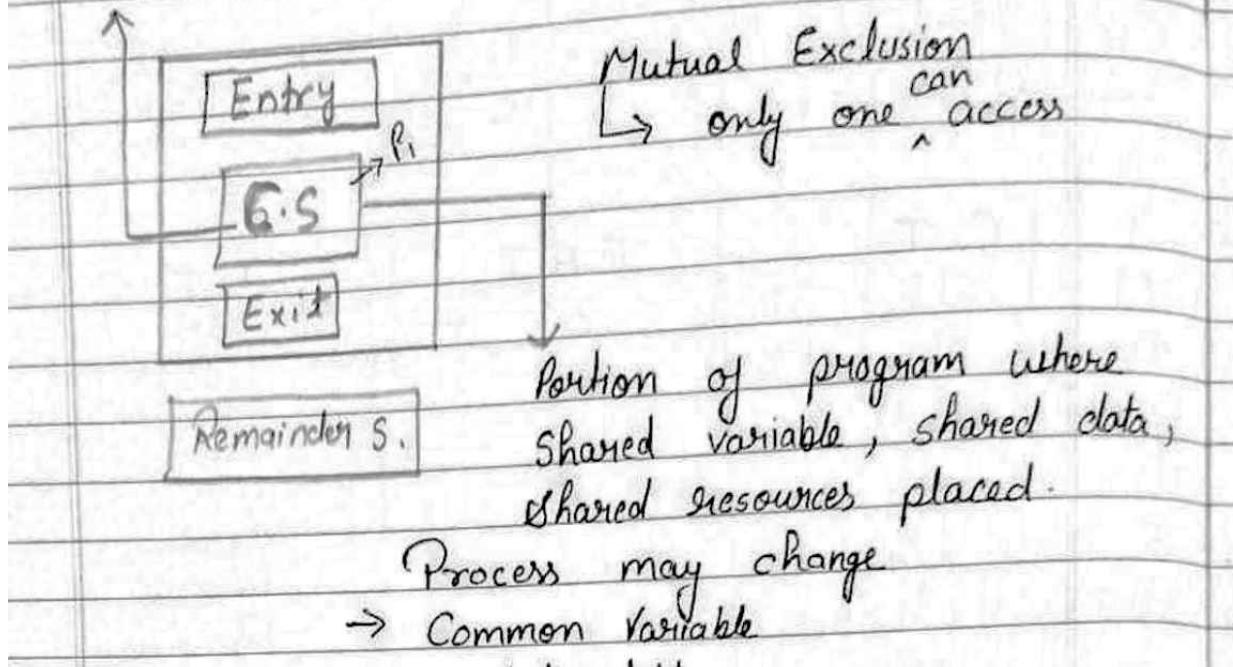
$$\text{Average T.A.T} = \frac{33 + 29 + 27 + 9 + 18 + 4 + 12}{7} = 18.85$$

$$\text{Average W.T} = \frac{29 + 27 + 19 + 4 + 14 + 0 + 6}{7} = 14.14$$

Process Synchronization



~~Critical~~ Section Problem



Two process can have Entry & Exit & Remainder Section but in critical section only one process can occur.

How do C.P.U known that one problem is in the section we do not send the other one?

The solution of critical section must satisfy following requirements.

- 1) Mutual Exclusion : under no circumstances can two process be in their critical section for the same variable at the same time.
- 2) Progress :- No ^ remain forever in c.s. should be in progress of process.
3. Bounded waiting :- Bound on number that times

process should have to wait forever for C.S.

Solution :- S/w Type :- lock variable , strict
Alternation peterson algo

H/w type :- T.S.L / Test & set

O.S type :- Semaphore

Compiler type :- Monitors

Semaphores

Semaphores was proposed by Dijkstra in 1965.
Semaphore is simply a variable which is non-negative & shared between threads. This variable is used to solve the critical section problem & to achieve process synchronization in the multiprocessor environment.

Semaphores are of two types:-

1. Binary Semaphore \Rightarrow In

- This is also known as mutex lock. It can have only two values - 0 & 1. Its value is initialized to 1. It is used to implement the solution of critical section problem with multiple processes.

2. Counting Semaphore :-

It's value can range over an unrestricted domain. It is used to control access to a resource that has multiple instances.

Process Synchronization

In this task the execution of processes in such a way that no ~~two~~^{shared} processes can have access to the same data & resources. It is a procedure that is involved in order to preserve the appropriate order of execution of cooperative processes.

Deadlock

when any process P_1, P_2, P_3 wanted to access single resource R. Then there deadlock occur. Because all the process wanted to use it.

Necessary & Sufficient conditions for Deadlock

1) Mutual Exclusion:-

Resources \rightarrow Not sharable

$P_i \rightarrow$ Resource

Only one can hold at a time.
After release another one can

2 Hold & wait

process holding a resources & waiting for additional resources

Held by another process

3. No preemptive Condition:- Resources cannot be removed from the processes are used to completion or released voluntarily by the process holding it.

Priority Ex. :- Higher priority
lower priority
preempted \rightarrow C.P.U

Circular wait :- Each process wait for next process in circular manner.

Deadlock Prevention :-

\rightarrow One of the necessary condition can never hold

① Mutual Exclusion :- Not always possible to prevent deadlock by preventing M.E
(Making all Resources Shareable)

② Hold & wait :-

before wait :- all required resources acquired.
reduce throughput :- those not needed even get earlier.

All required resources might not be free always.

③ No preemption :-

Resources can be preempted.
Request \rightarrow Resource { all other resources which are held preempted
held by another waiting resource.

④ Circular wait :- Resources may be ordered
(increasing order)

Deadlock Avoidance

How resources are to be requested.

Currently available, the resources currently allocated to each process & the future requests & releases of each process.

Examine → Resource allocation

Circular → wait can never exist.

Resource allocation state
↓

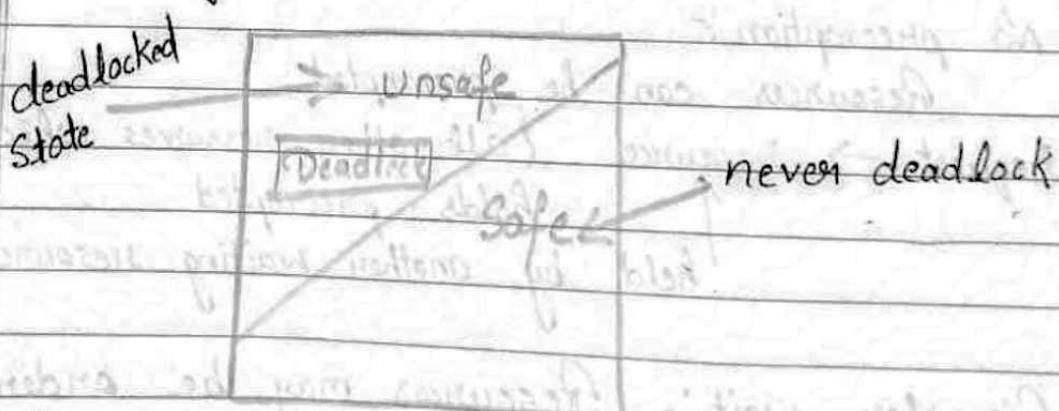
no. of available resources maximum demands of the processes.

Safe - State :-

Safe Sequence :→ allocated to peek of resource

No Deadlock

$\langle P_1, P_2, \dots, P_n \rangle$ for each P_i satisfied by the currently.



Not all unsafe states are deadlocks.
behaviour of the processes controls unsafe states.
To avoid deadlock → Safe State (always)

Deadlock Recovery

How to recover from deadlocks?

Killing Process

Killing all process involved in deadlocks.

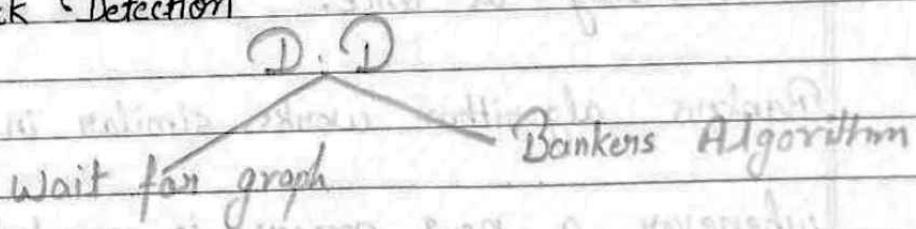
Killing process one by one.

After killing each process check for deadlock again } ← Repeat steps.

Resource Preemption

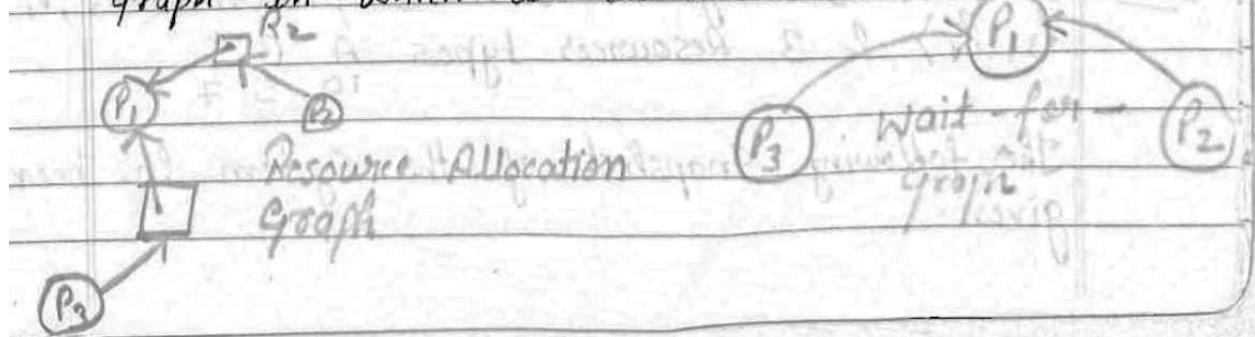
Resources are preempted from the processes involved in deadlocks, preempted resources are allocated to other processes, so that there is a possibility of recovering the system from deadlock ← Starvation

Deadlock Detection



Wait for graph

It is an enhanced version of Resource allocation graph in which we do not show the resources.



If all the resources have single instances & cycle is being formed, then the system is in Deadlock. If cycle is there, but resources have more than 1 instance, then the deadlock may or may not be present.

Bankers Algorithm

Bankers algorithm is a deadlock avoidance algorithm. It is named so because this algorithm is used in banking system to determine whether a loan can be granted or not.

Every time a loan has to be granted by the bank. it subtracts the loan amount from the total money the bank has. Then it checks if that difference is greater than . It is done because only then, the bank would have enough money even if all the account holders draw all their money at once.

Bankers algorithm works similar in computer

whenever a new process is created. it must specify the maximum instances of each resource type that it needs, exactly.

Ques Consider the System with 5 process $\langle P_0, P_1, P_2, P_3, P_4 \rangle$ & 3 Resources types A B C
 $\begin{array}{ccc} 10 & 5 & 7 \end{array}$

The following Snapshot of the System has been given.

Allocation			Max			Available			
	A	B	C	A	B	C	A	B	C
P ₀	0	1	0	7	5	3	3	3	2
P ₁	2	0	0	3	2	2	5	3	2
P ₂	3	0	2	9	0	2	7	4	3
P ₃	2	1	1	2	2	2	7	4	5
P ₄	0	0	2	4	3	3	7	5	5

a) find need matrix

b) is the system in a safe state if yes find safe sequence.

① find need matrix

$$\text{need matrix} = (\text{max} - \text{Allocation})$$

	A	B	C
P ₀	7	4	3
P ₁	1	2	2
P ₂	6	0	0
P ₃	0	1	1
P ₄	4	3	1

Safety Algorithm

if
need \leq available

then

Execute Process

new available = Available + Allocation

Else

Do not Execute go forward

$P_0 \rightarrow \text{need} > \text{available}$
 $7, 4, 3 > 3, 3, 2$
do not execute P_0

$P_1 \rightarrow \text{need} \leq \text{available}$
 $1, 1, 2 \leq 3, 3, 2$
Execute P_1

$$\begin{aligned}\text{new Available} &= \text{Available} + \text{Allocation} \\ &= 3, 3, 2 + 2, 0, 0 \\ &= 5, 3, 2\end{aligned}$$

$P_2 \rightarrow \text{need} > \text{available}$
 $= 6, 0, 0 > 5, 3, 2$
= do not execute P_2

$P_3 \rightarrow \text{need} \leq \text{available}$
 $0, 1, 1 \leq 5, 3, 2$
Execute P_3
new available = $5, 3, 2 + 2, 1, 1$
= $7, 4, 3$

$P_4 \rightarrow \text{need} \leq \text{available}$
 $= 4, 3, 1 \leq 7, 4, 3$
Execute P_4
new available = $7, 4, 3 + 0, 0, 2$
= $7, 4, 5$

$P_0 \rightarrow \text{need} \leq \text{available}$
 $7, 4, 3 \leq 7, 4, 5$
Execute P_0
new available = $7, 4, 5 + 0, 1, 0$
= $7, 5, 5$

$P_2 \rightarrow \text{need} \leq \text{available}$

$$6, 0, 0 \leq 7, 5, 5$$

Execute P_2

$$\begin{aligned}\text{new available} &= 7, 5, 5 + 3, 0, 2 \\ &= 10, 5, 7\end{aligned}$$

Safe Sequence will be

$$\langle P_1, P_3, P_4, P_0, P_2 \rangle \quad P_i \rightarrow R_i$$

Memory management

Memory \rightarrow Storage

written in the form
of $2^n \times m$

Byte Addressable

Bit Addressable

$n = \# \text{ Address lines}$

$m = \# \text{ data lines}$

$A \cdot L = \text{Capacity}$

$D \cdot L = \text{Size of data}$

Always starts
with 0.

$2^n \times m$

$2^3 \times 1$

Ex - Memory addressing in memory

① $8 = 2^3 \times 1$

0 - 7

$2^4 - 1$

② $16 = 2^4 \times 1$

0 - 15

$2^5 - 1$

Memory Management Techniques

Continuous

→ Fixed - Partition Scheme

→ Variable " "

 ↳ First fit

 ↳ Best fit

 ↳ Worst fit

 ↳ Next fit

fixed partition Scheme

No. of partition : fixed :

Size of partition : Not fixed

One block holds only one partition

wastage of memory inside the partition

 ↳ Internal fragmentation

Variable Partitioning Technique

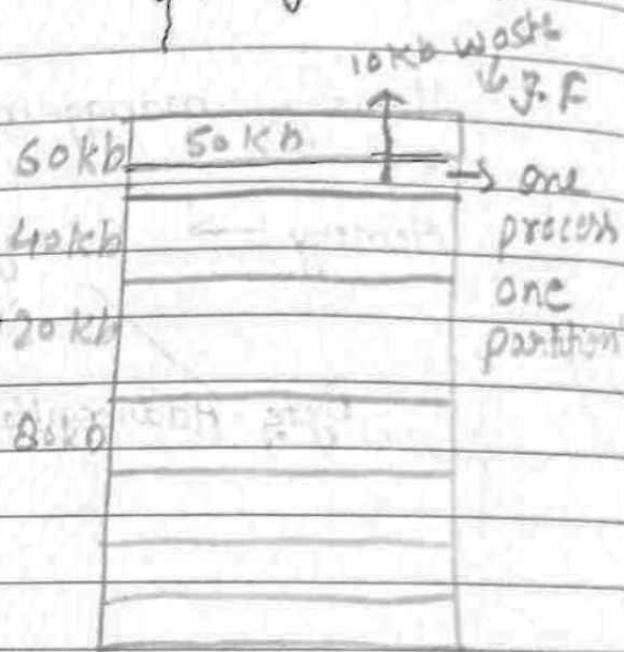
whatever / whenever process block comes in accordingly it assigns block. (at the time partition will be made)

When process finish its execution it move out from the memory and a free partition / hold will

Non- Continuous
 ↳ Paging

 → Multilevel paging
 → Inverted Paging

 ↳ Segmentation



1 + E3

1 + E3

be made.

Decision making for accommodation of partition fit
Algo use.

① first fit

Ques → 357, 210, 468, 491

→ Partition :- 200, 400, 600, 500, 300, 250

200	
400	357
600	210
500	468
300	
250	

In this method, first job claims the first available memory with space more than or equal to it's size. The operating system doesn't search for appropriate partition but just allocate the job to the nearest memory partition available with sufficient size.

Remain → 491

② Best fit

200	
400	357
600	491
500	468
300	
250	210

The best fit deals with allocating the smallest free partition which meets the requirement of the requesting process. It then tries to find a hole which is close to actual process size needed.

worst fit external fragmentation

200	
400	
600	357
500	210
300	
250	

worst fit allocates a process to the partition which is largest sufficient among the freely available partitions available in the main memory. if a large process comes at a later stage then memory will not have space to accommodate it.

variable partition scheme suffers from external fragmentation. To avoid fragmentation two techniques will be used.

dots

Compaction :- To move all the processes towards the top / down & free space in a single continuous placed called compaction.

disturb / interrupt all running process in the memory.

Implement non continuous technique

Non Continuous Scheme

1) Paging :- logical memory breaks into pages.
physical memory " " frames.

page size = frame size

Page Table
Pages

Page no. as
Index

	0	7	frame no.
Page 0	0	1	
Page 1	1	1	
Page 2	2	9	
Page 3	3	5	

Logical memory

Page table (mapping)
 \Rightarrow Page no. \rightarrow Index

frame no. \rightarrow base address of
 each page in physical
 memory.

frames

1	Page 1	-
2		
3		
4		
5	Page 5	
6		
7	Page 0	7
8		
9	Page	9

logical Address space \rightarrow L.A.S Logical Address (L.A)
 (byte / word) (bits)

Physical Address space \rightarrow P.A.S Physical Address (P.A)
 (byte / word) bits

$$L.A = 32 \text{ bits}$$

$$L.A.S = 2^{32} = 4 \text{ G.W}$$

$$L.A.S = 128 \text{ M.W}$$

$$= 2^7 \cdot 2^{20}$$

$$L.A = 27 \text{ bits}$$

$$P.A = 28 \text{ bits}$$

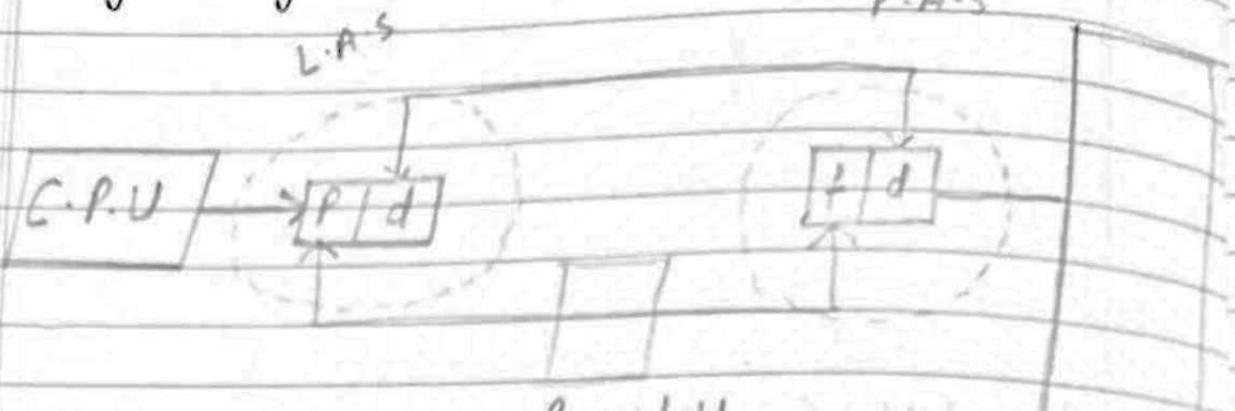
$$P.A.S = 2^{28} = 256$$

$$P.A.S = 32 \text{ M.W}$$

$$= 2^5 \cdot 2^{20}$$

$$P.A = 25 \text{ bits}$$

Page diagram



Page table

P = no. of bits required
to represent Page no.

d = page offset

Physical
memory

Technique of mapping processor generated address to physical Address is called Paging

$$\# \text{ Pages} = \frac{\text{L.A.S}}{\text{Page Size}/\text{f.s}} \quad \# \text{ frame} = \frac{\text{P.A.S}}{\text{P.S}/\text{frame size}}$$

Ex :-

System where L.A = 13 bits

P.A = 12 bits

Page Size = 1 k.w

L.A = 13 bit

P.A = 12 bit

L.A.S = 2^{13} w

P.A.S = 2^{12} w

No. of pages ?

No. of frame - ?

$$\text{No. of pages} = \frac{\text{L.A.S}}{\text{Page Size}} = \frac{2^{13}}{2^{10}} = 2^3 = 8 \text{ pages}$$

$$\begin{aligned}
 \text{No. of frames} &= \frac{P \cdot A \cdot S}{P.S / \text{frame size}} \\
 &= \frac{2^{12}}{2^{10}} \\
 &= 2^2 \\
 &= 4 \text{ frames}
 \end{aligned}$$

Performance of paging

Page table placed in main memory

Effective Memory Access time (EMAT) = $M + M = 2M$
 (M < Main memory Access time)

(one $M \rightarrow$ Page table reference)

Another $M \rightarrow$ Actual memory reference

TLB (Translation Look a Side Buffer)

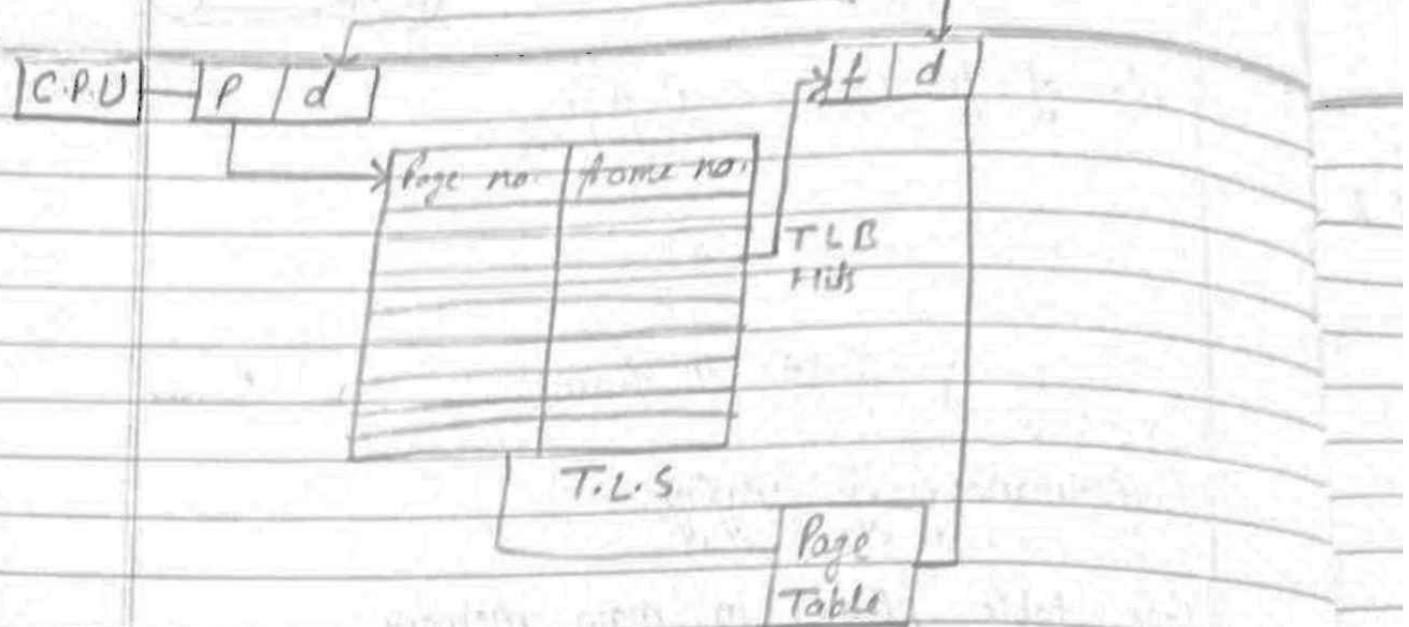
→ To improve performance TLB is added just before page table.

T.L.B contain frequently referred page no. and frame no.

if

if reference found = TLB Hit
else

T. L. B. Miss



T.L.B Access time = C

Hit ratio = H

With T.L.B

$$EMAT = H[C + M] + [1 - H][C + 2M]$$

Without T.L.B

$$EMAT = 2M$$

Multilevel Paging

To avoid the overhead of maintaining the large page table \rightarrow multilevel page is implemented.

P is divided into two parts $\Rightarrow P_1 \& P_2$

P_1 : outer table / no. of bit required
of represent page of
page table

P_2 : inner table / no. of bit for page size



Inverted Paging :- Only one table for all the process.

Searching :- Process ID & Page no. should match

C.P.U	L.A	P Id	L F I d	
		Page No.	Page No.	
1	P ₁	Process 1		
2	P ₂	2		
3	P ₃	4		
4				
5				
6				
7				
8				

Physical memory

Inverted page table

or frame table

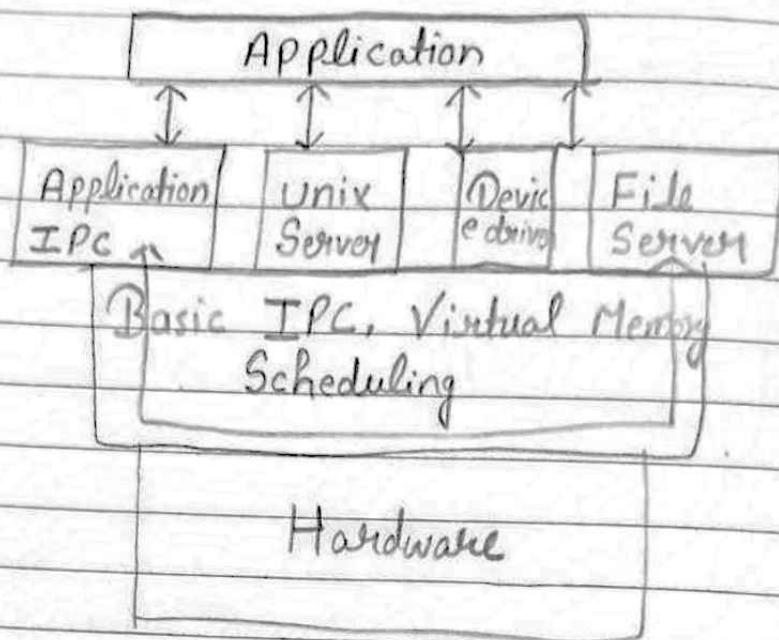
No. of Entry in Inverted page = No. of frame in P.A.S

Segmentation :- To achieve user view of Memory

C.P.U	S Id	L.A	Base Address	limit	Segment	Segment	Segment	Segment
S.T = Segment Table	0	3600	300		S ₂			
	1	3900	400		S ₁			
	2	4200	500		S ₀			
	3	5800	600					
	4	1500	1000					
		9000	500	OS	S ₅			

L·A·S divide into various segments & no. of bit required for no. of segments & Segment size.

What is Microkernel



Microkernel is one of the classification of the Kernel. Being a Kernel it manages all system resources. But in a microkernel, the user services & kernel services are implemented in different address space. The user services are kept in user address space & kernel services are kept under kernel address space, thus also reduces the size of kernel & size of operating system as well.

In this architecture only the most important services are inside kernel & rest of the OS services are present inside system API program. Thus users are able to interact with those.

not so important services within the system application. the microkernel is responsible for the most important services of operating system.

- I P C
- M . M
- C . P . U scheduling