Name

Email:

M: +91-

| ACADEMIC QUALIFICATIONS |
|-------------------------|
|-------------------------|

| Year | Degree | Institute | CGPA /Percentage |
|------|---------------|---|------------------|
| 2017 | B.Tech(ECE) | MallaReddy Engineering college, Hyderabad | |
| 2013 | XII(AP Board) | Narayana junior college, Hyderabad | |
| 2011 | X(AP Board) | Vignan Vidyalayam ,Hyderabad | |

INTERNSHIP/TRANING

Nuclear Fuel Complex, Hyderabad

March 2016 - June 2016

Analog Communication: Undergone three months training in analog and digital communication, learnt working principles behind A/D converters and LCD using Xilinx technologies and implemented on arduino board. Also done practical in industry regarding technologies of communications. **Project**: Done LCD &A/D interfacing converting the analog signal to digital signal by interfacing LCD with 8051 micro controller.

E-learning, (MEITY)

May 2016-July 2016

EUVM: During this course I have learnt the basic modules UVM and FDP and the various technologies that works at different in verification and different in testbench. Practical implementation is done on virtual software system Verilog.

SKILLS

Programming: c,java, python

Softwares: Verilog, Mentor graphics, synopsis, MSEXCEL

POSITIONS OF RESPONSIBILITY

Team leader in project,

2016-2017

• Class Representative,

2010-2011

EXTRA CURRICULAR ACHIEVEMENTS

Community Service

Volunteer at Election Commission

Achievements/ Participation

- Certification course in Internet of things (IOT).
- Best project awarded at
- Published my project paper in

LANGUAGE PROFICIENCY

1. English 2. Hindi 3. Telugu

PERSONAL DETAILS

Date of Birth :

Nationality : Indian

Address :