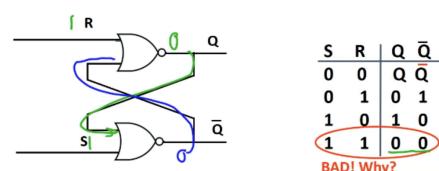
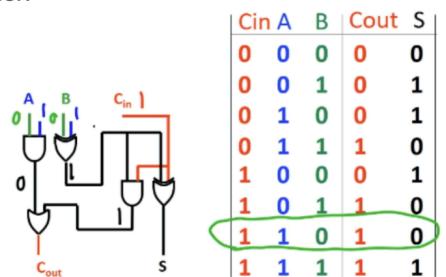


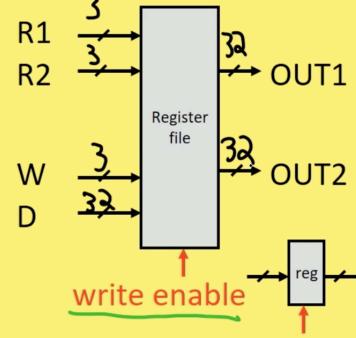
XOR = not (A nor (A nor B) nor (B nor (A nor B)))

Mux: S = 0 时选择 A(上) 的值, S = 1 时选择 B(下) 的值

Full adder:



Register File or Register

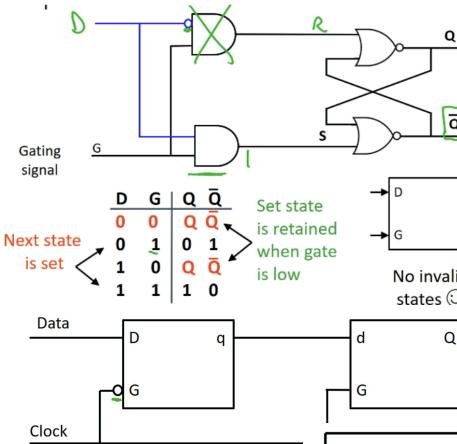
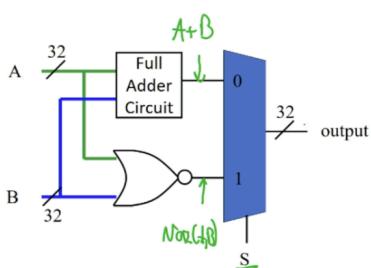


overall delay 就是 delay 最大的一个串联路线的 delay

sizeROM = $2^{\{\text{input size}\}} * \text{output size}$

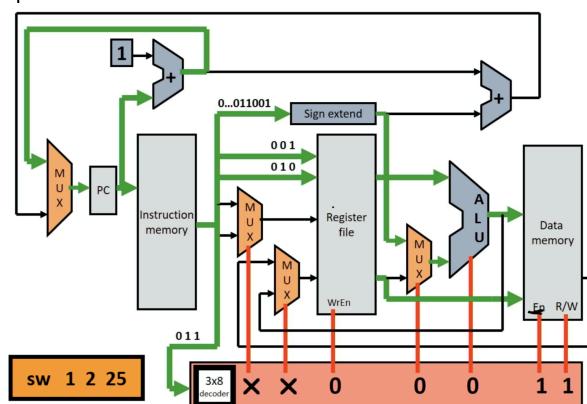
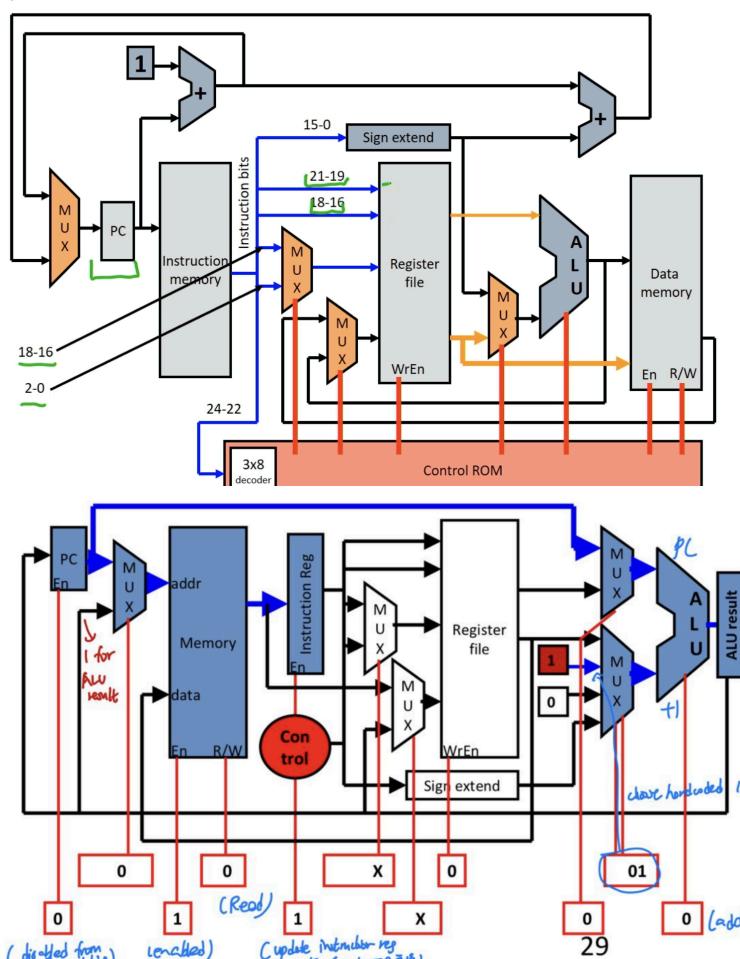
Execution time = CPI * #insts * clockPeriod

CPI 即 average number of clock cycles per instruction

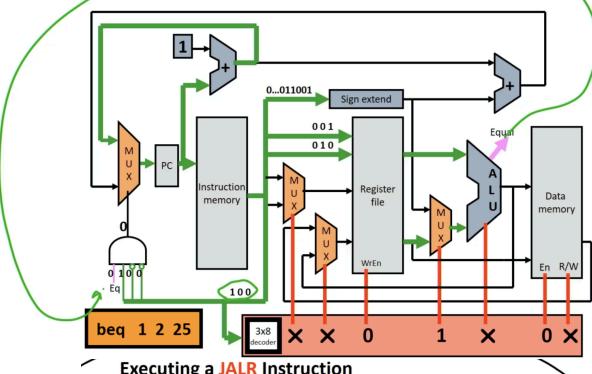


Next state is set

No invalid states



Executing "not taken" BEQ Instruction on LC2K Datapath



Executing a JALR Instruction

