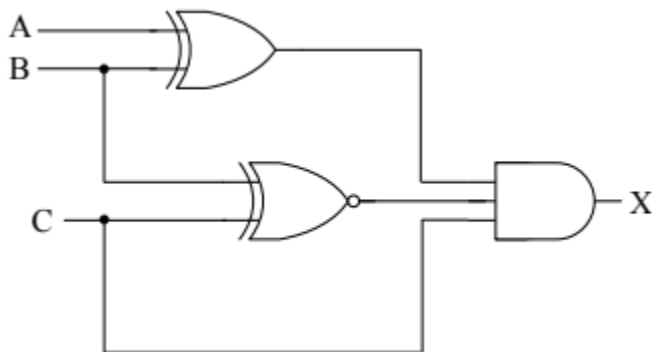# Computer Architecture Tutorial Test 01

01. A bulb in a staircase has two switches, one switch being at the ground floor and the other one at the first floor. The bulb can be turned ON and can be turned OFF by and one of the switches irrespective of the state of the other switch.

    a. Draw the truth table for above situation.
    b. Draw the most suitable logic circuit/ gate for this.

02. The Boolean function Y= AB + CD is to be realized using only 2 input NAND gates.

    a. Extract the Boolean function for NAND gates using the above formula
    b. Draw the logic circuit for the extracted Boolean function.

03. What is the Boolean expression for the give logic circuit below.

04. Complete the following Karnaugh map, according to the values found in the given truth table:

A	B	C	D	Out
0	0	0	0	0
0	0	0	1	1
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	1
0	1	1	0	0
0	1	1	1	0
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	1
1	1	1	0	0
1	1	1	1	1

05. Minimize the given Boolean Expression by using the four-variable K-Map.

    $F (A, B, C, D) = \Sigma m (1, 5, 6, 12, 13, 14) + d (2, 4)$