

# DATA PATH

CSE 317- Computer Architecture

Presented by

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Sec : A

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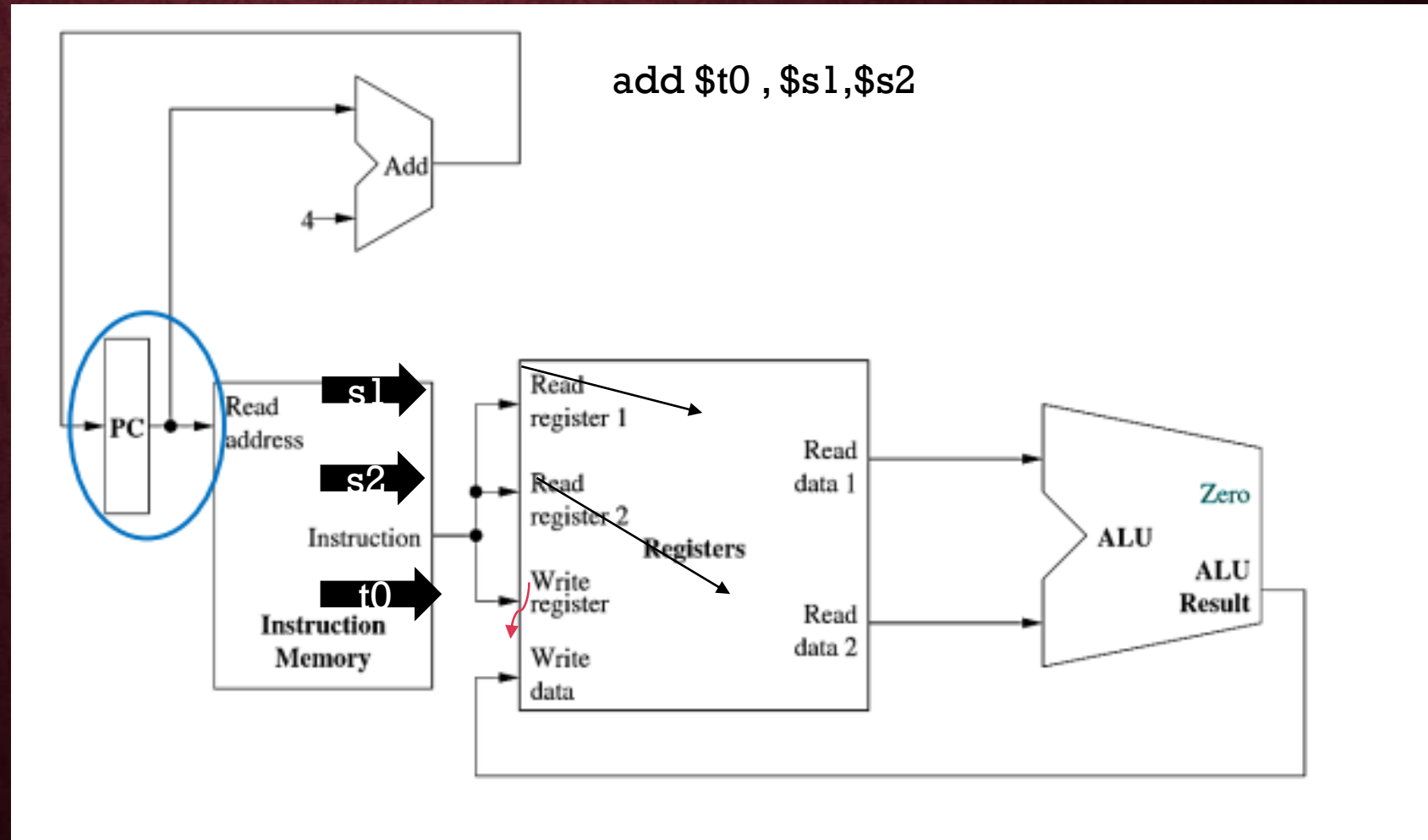
# CONTENT

- R type
- I type
- R+I type

# R-TYPE DATA PATH

R-format instructions. In our limited MIPS instruction set, these are add, sub, and, or, and slt.

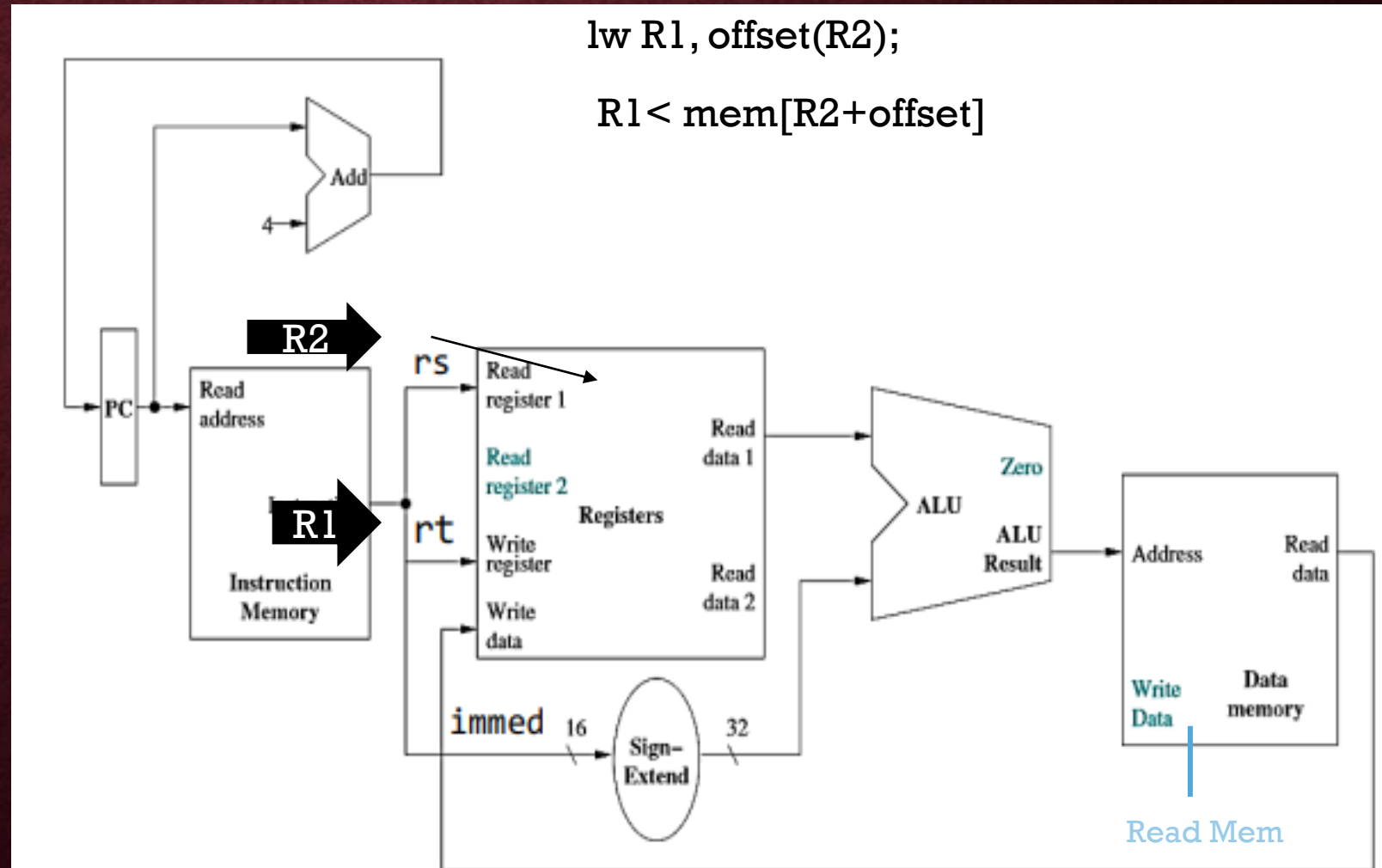
All R-format instructions read two registers, rs and rt, and write to a register rd.



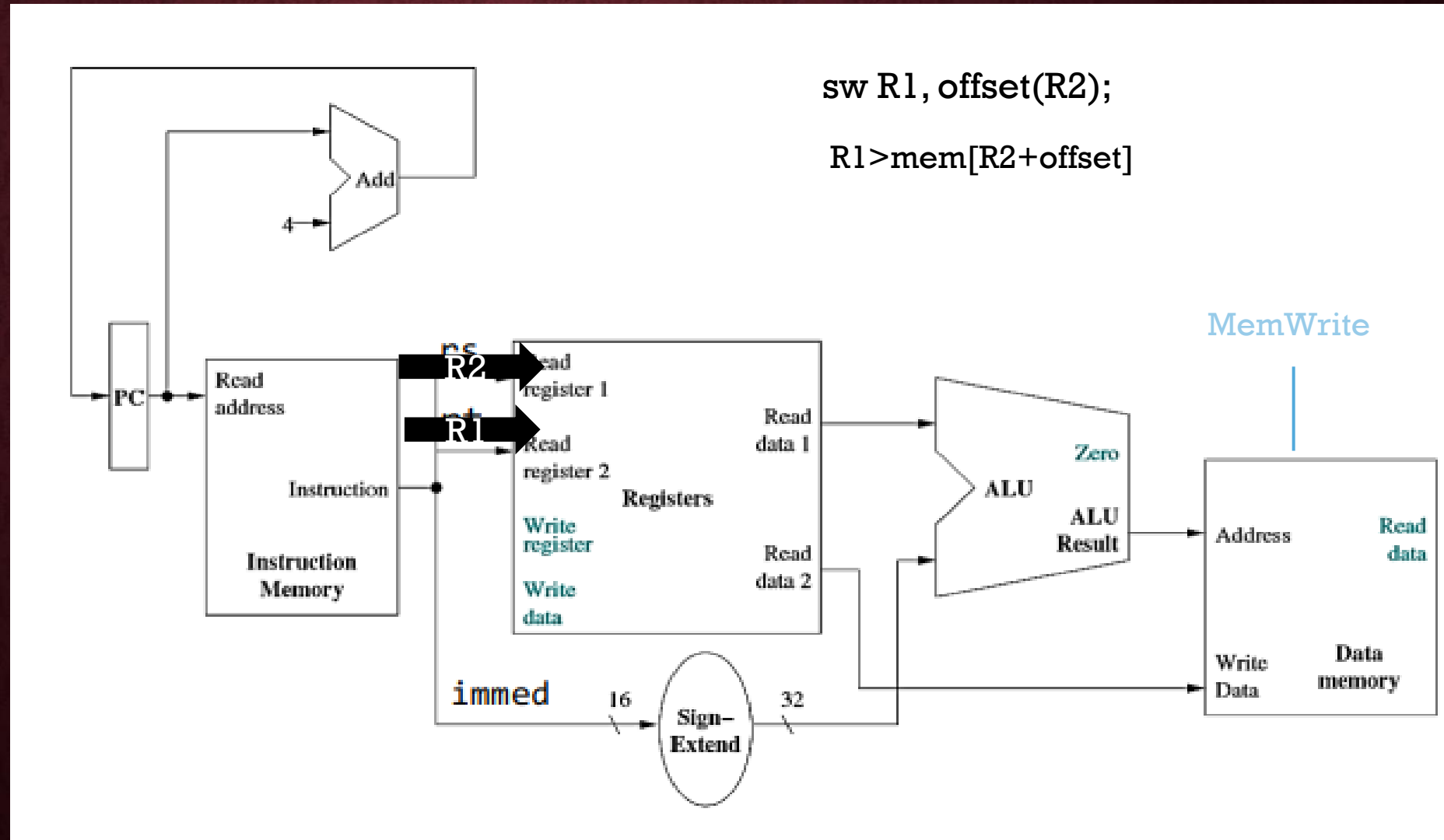


# I TYPE DATA PATH

In the accommodating the data transfer. For lw and sw, we have the I type format.



# I TYPE DATA PATH

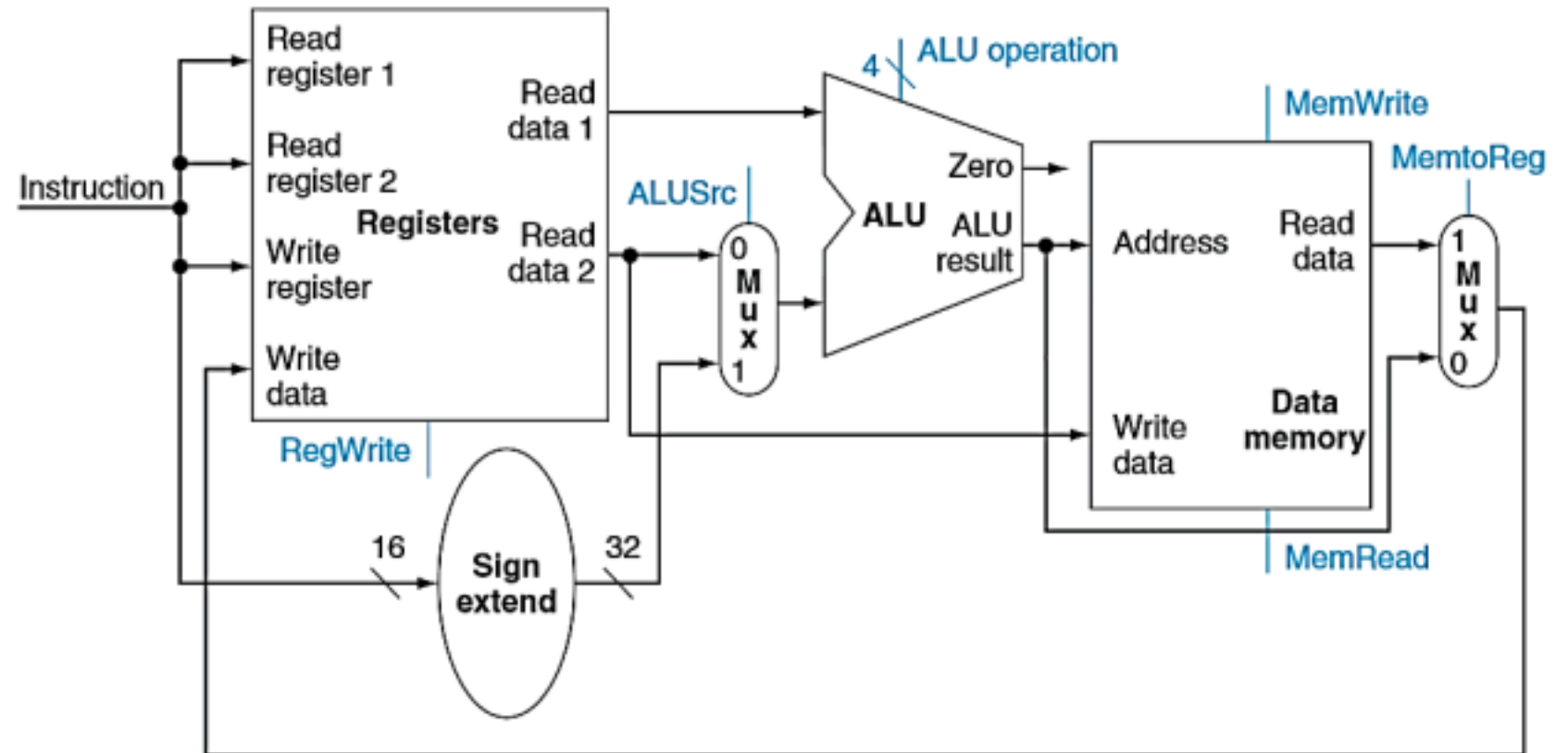


# R TYPE AND I TYPE COMBINED

add \$rd, \$rs, \$rt

lw \$rt, immmed(\$rs)

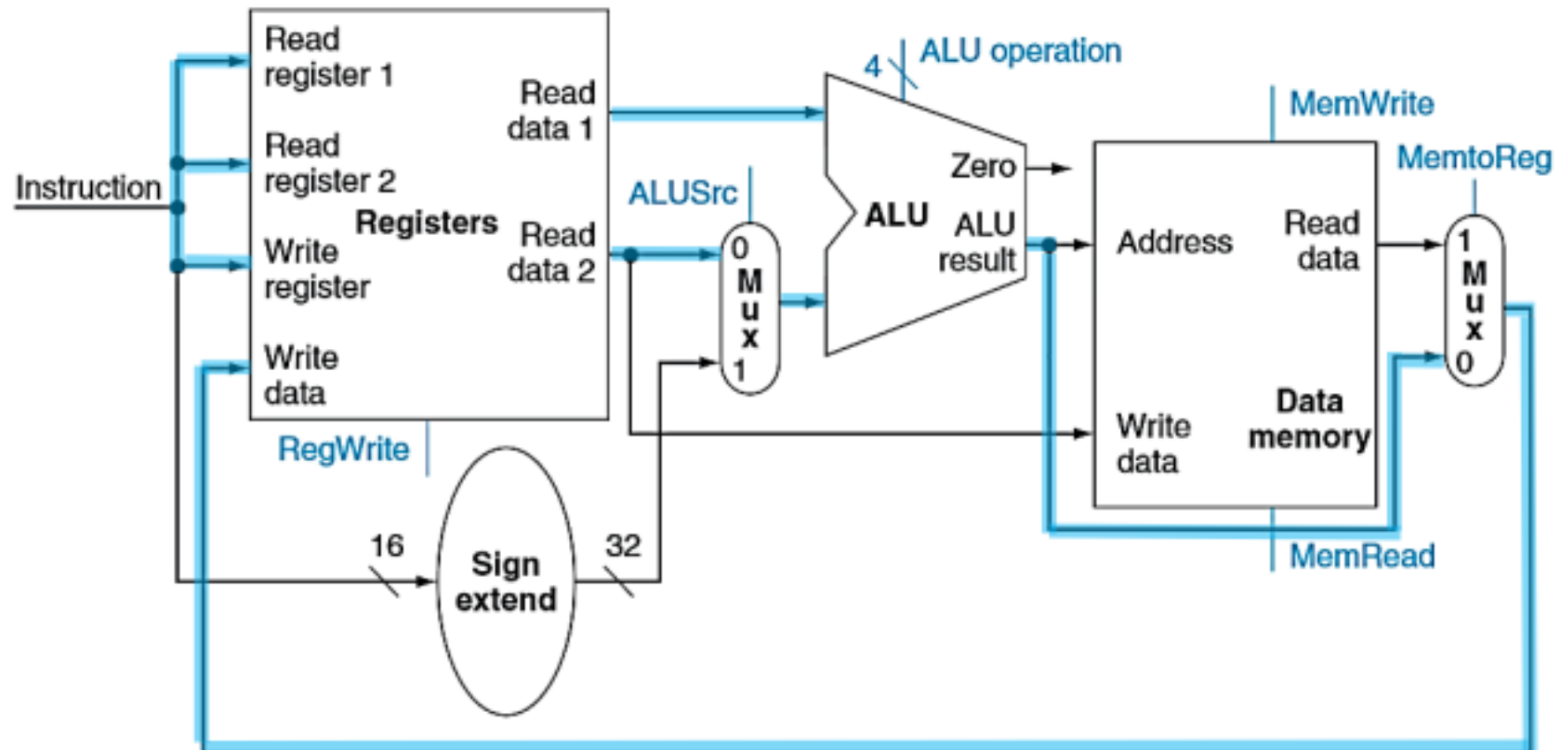
sw \$rt, immmed(\$rs)



# R TYPE AND I TYPE COMBINED

add \$rd, \$rs, \$rt

R type

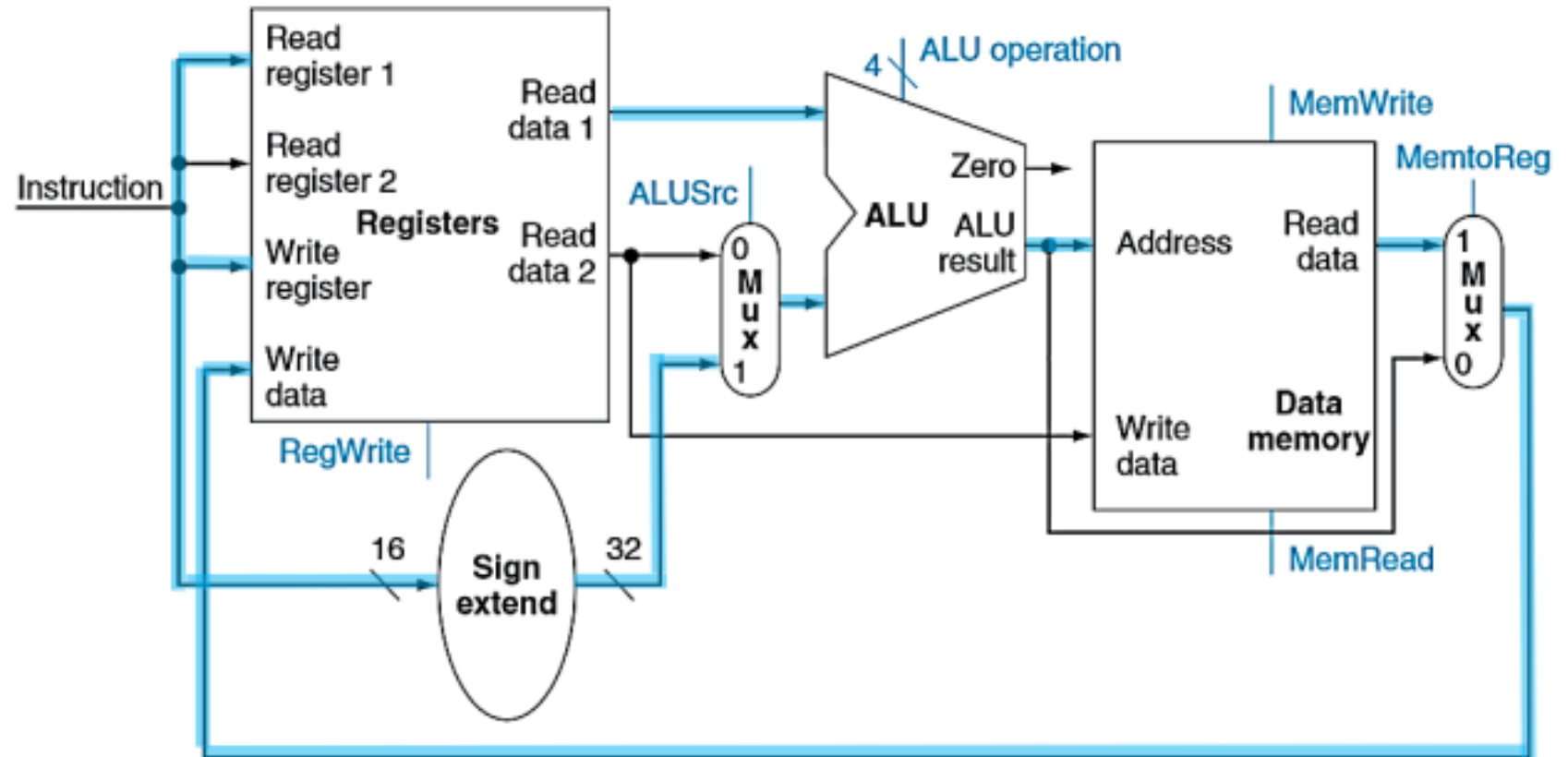




# R TYPE AND I TYPE COMBINED

lw \$rt, imm(\$rs)

I type





# R TYPE AND I TYPE COMBINED

sw \$rt, immmed(\$rs)

I type

