

Department of Computer Science and Engineering

Assignment 1

Course Code : CSE-317

Course Title: Computer Architecture

Submitted By:

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Department: CSE

Submitted To:

Shammi Akhtar Assistant Professor Department of CSE, UAP Girmen: 0,8,0,6,8,25,13,9,8,0

1. Direct mapped: A direct mapped cache with 16-blocks can only hold one block per set.

Man	Hi+/						Bloc	k									1
Memory Address	Min	0	1	2	3	4	5	6	7	8	9	lo	tı	12	13	19	15
0	Mass	Mento															
8	Miss	MAS								MIST							
0	Misshi	MEOJ															
6	Mars							ME									2,
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25	min										M[25	_			ME	No. 191	+
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9	Mess										MIT	_					
8	het	P4[8	7	9						MES			1				
0	Meske	+ MEO	7		13.1												-

2 way set associatives but = 16 - 28, Now mod block address.

Block address	Cacho Sot
0	(0 mod g) = 0
8	(8 mod 8) = 0
6	(6 mod 8) = 6
25	(25 mod 8) = 1
13	(13 mod 8) = 5 (9 mod 8) = 1

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	0	men	MIST		2 3	31	6	6	87	8 96	10 112	12 19	14 15	7
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1	0	net	MEOJ											
:	6	Màr.										MIGT		
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2		Mins	1		M25]	•					•			
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8		net-	MBI,	MOST										**
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4 way set ansociative ? set = 16 = 9, Now med block address by 9

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coche set
(omod 84) =0
(8 mod 4) = 0
(25 mod 1) =1
(13 mod 4) = 1

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8	Min	M[9]	MB1		\top				+					1			
0	hit	MG]	14[8]		+									+			
6	Men				1				14	16]				1			
8	hit	MEJ	MES		1				1								
25	Mess	1			MIQS	1			1								,
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	Block address	cache set]	1			1 71
	8	(8 mod 2) =0				1	
	6	(6 mod 2) = 0					
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	9	$(13 \mod 2) = 1$ $(9 \mod 2) = 1$					

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