c:/Files/VHDLProjects/CAD\_HW03\_SadraSamadi/my\_workspace/my\_workspace/src/wave.asdb untitled.awc Signal name Value 8 16 24 32 40 48 56 64 72 80 88 96 ns - clk 1 to 0 . reset 0 0 **□**- X 0 -⊳y s3 x s4 x s5 x s2 x s3 x s4 x s5 x s3 x лர state s0 s0 Cursor 1 100 ns