C:/Files/VHDLProjects/CAD_HW02_SadraSamadi/shift_register/shift_register/src/wave.asdb untitled.awc Signal name Value 20 60 80 100 120 лгclk 1 to 0 лг reset 1 to 0 лாshr 0 0 лгshl лπ load 0 to 1 ⊞ лг data_in 6C39 _ ா output 0000 0000 6C39 FFFF 0000 6C39 0 лг О... Cursor 1