C:/Files/VHDLProjects/CAD_HW03_SadraSamadi/my_workspace/my_workspace/src/wave.asdb untitled.awc Signal name Value 8 16 24 32 40 48 56 64 72 80 88 96 ns - clk 1 to 0 . reset 0 0 **□**- X 1 -⊳y s3 s1 s3 x s1 x s2 x s3 x s0 πr state s0 s1 X Cursor 1 100 ns