Part 4. "是" is an instruction that land variable while adjusting the roffset' between 16 and 32 bits as all rem recessary "LEA (16 bit register), (32-bit address) " load the lower 16 bit of the address into the regular and "LGA (32-bit "register) i'(16-bit otherens)" load the 32-bit register
With the address 7000 extended to 32-2 WHY THE THE THE THE THE THE THE offset: offset is an assembler. directive in ×86 assembly language It actually means, address and is a way of shan ding, the over loads of the imovial instruction.

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programin non. given in have to present CS: (60 B MACH Language NO 17 ter8 assome "DATA" is the name 95. Dalt segment and code Segment different purposa 50 wre different Assembly ASSU WAE in Cool さい、こ parta gren ナンカウ for the

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6. set 243 let a bar

9. To accent hemory, we can use tuse four registern: Bx, SI, DI, BP, Combining ture registers inside [] symbol, we can get different memory locations.

(B) +5I +08) [51]. [BX +SI] 1 t3x + D1+ 18] (DI) t BX + DI] TBP+51+08) 916 EBP+ 51) [BX] TBP+ DITAS [BP+DI] [BX+SI +d16] (51+08) (31+916) [BX + DI +d16] (01 + d8) tolt die) TBP+ SITO16) CBO+48) [BP+dLb] CBP+ D1+d167 (tBx + do) LBX+ des7