Noah Saffer

linkedin.com/in/noahsaffer | noah.saffer@wustl.edu | 48 Winthrop Road, Short Hills, NJ 07078 | (973) 220-2114

EDUCATION

Junior | **Double Major in Computer Science and Computer Engineering**

Washington University in St. Louis | *B.Sc.* May 2020
Dean's List Honors — Cumulative GPA of 3.4

Washington University in St. Louis | *M.S.* May 2021
I have applied into the Computer Engineering program at my alma mater and other colleges as well

EXPERIENCE

Software Development Engineering Intern *Amazon.com, Inc.*
Seattle, WA Summer 2018
Worked on creating and deploying production software for Amazon Prime Video. Used a directed acyclic graph to perform a pipelined workflow. Skills used include dependency injection and object mapping in Java with model creation in XML.

Head Teaching Assistant *Computer Design I and II*
St. Louis, MO 2016 - 2018
CSE 260M: After outperforming the rest of the class in a course meant for Juniors and Seniors as a Freshman, I was hired as a TA, and subsequently rehired for Spring 2018 as the head TA.
CSE 362M: After finishing with the highest class average as a Sophmore, I was hired as the head TA.

Teaching Assistant *Data Structures and Algorithms*
St. Louis, MO 2017 - 2018
CSE 247: Hired as a TA due to my excellent performance relative to the rest of the class in a course meant for Sophomores and Juniors, rehired for Spring 2018.

Lead Programming Instructor *Zatna LLC*
Martinsville, NJ Summer 2017
Taught high school and middle school children Intro to Electrical Engineering, Data Structures and Algorithms in Java, Python, C#, Unity, Tynker and GameSalad. I was promoted to the lead instructor position after two weeks on the job.

SKILLS

1. Java 2. C 3. VHDL 4. Python 5. C++
6. Verilog 7. SQL 8. C# 9. Visual Basic 10. LaTeX

ACHIEVEMENTS AND PROJECTS

- Created a 32-bit CPU using an FPGA that was based on a Simple RISC with microprogramming and expanded it in my free time (VHDL, Verilog).
- Created a difference engine based on a Mealy-model finite state machine to calculate the peak of a polynomial function (VHDL, Verilog).
- Worked on 5 apps that were published to the Apple App Store (iOS Development).
- First place in the Hardware portion of HackMHSII, the hackathon at Millburn High School.

REFERENCES AVAILABLE BY REQUEST — MADE IN LaTeX