

# Compilers-II (CS3423)

## Mini Assignment - I

### An Introduction to the LLVM Infrastructure, AST, IR and Compiler Options

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August 28, 2019

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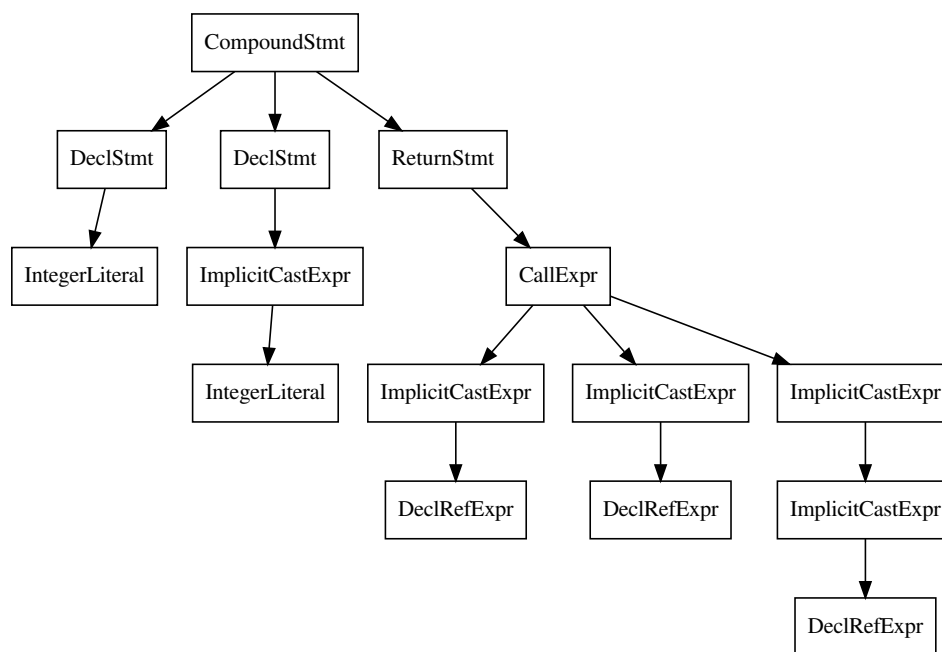
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# Clang AST

Clang is essentially a library that allows us to convert a C program into an Abstract Syntax Tree and then manipulate the tree in some ways. After viewing the ast for several non-trivial programs the following are a few observations:

1. The ast for any program can be printed using:  
`clang -Xclang -ast-dump -fsyntax-only <filename>`. Here `Xclang` option is used to pass the arguments to the clang compiler, `fsyntax-only` is used so that no object code is generated and `ast-dump` is used to print the abstract syntax tree.
2. The dump begins with a bunch of `TypedefDecl` nodes followed by the ast for the user code.
3. We can use the option `ast-list` to get all a list of all the *Declaration* nodes, this is an alternative option to look at the contents of the ast.
4. The `ast-view` option lets us look at an the entire ast graphically and is very easy to understand compared to the dump. Internally it creates a dot file which can be viewed by any dot file renderer. Example:



5. `ast-print` option allows us to pretty print the ast with as much resemblance to the original code as possible.
6. We can also use `clang-check` to filter and print only the ast of a subset of the Declaration nodes:  
`clang-check <filename> -ast-dump -ast-dump-filter=main --`

## Observations about the AST structure

- In the tree that clang creates, every node is an instance of one of the two classes: **Decl** or **Stmt** class. Nodes usually have a name followed by a type. For example, the main function begins with a **FunctionDecl** node which would have a name *main* and a type(say `int` or `void`).
- Functions start with **FunctionDecl** node which has the function name and type along with it. These are always followed by the **ParamVarDecl** nodes which have information about the function parameters and their types.
- Most of the nodes in the body of the functions belongs to the **Stmt** class in the form of either of the following:
  - **CompoundStmt**
  - **DeclStmt**
  - **ImplicitCastExpr**
  - **CallExpr**
  - **ReturnStmt**
  - Literals, etc.
- Nodes like **ForStmt** have multiple children (5 for **ForStmt**) which are themselves nodes of different kind.
- Nodes like **IntegerLiteral** contain the value of the node as well along with the type.
- A point to note would be that the nodes themselves do not contain too much information but using the nodes we can get information from the AST context where most of the information is stored.

# Clang AST Traversal

The observations made about the AST traversal follows from this [tutorial](#).

## FrontendAction

The class `ASTFrontendAction` is provided to us to be able to interact with the ast while compilation. We must create a class which inherits from `FrontendAction` to be able to execute user specific actions on the ast. We must implement a virtual method `CreateASTConsumer` in this class which returns a type of `std::unique_ptr<clang::ASTConsumer>`, what we return is essentially our implementation of how we would like to consume the ast provided to us.

## ASTConsumer

The clang class `ASTConsumer` is used to create the consumers for the ast. We must create a class that inherits from this class to be able to use the ast. We can have multiple different ways to enter the ast for example `HandleInlineFunctionDefinition`, `HandleTranslationUnit`, `HandleTopLevelDecl`, etc. We can override any of these methods to read the ast. This class must also have an implementation of `RecursiveASTVisitor` as its member, this implementation is defined in the following section. We can provide our consumer with the `&Compiler.getASTContext()` to give it information like source locations which are not stored in the ast nodes themselves.

## RecursiveASTVisitor

We must implement a class which inherits from `RecursiveASTVisitor` to be used by the consumer class during the traversal. Everytime a new node is discovered by the DFS this is run on it. In this class we have methods of the type `VisitNodeType(NodeType *)` for different types of ast nodes. These methods return `bool` where *true* implies that we want to continue the traversal and *false* stops the traversal. We can also traverse in the opposite direction by overriding other methods of the class.

# LLVM Error Messages

## Programmatic Errors

The observations on error messages have been made from [here](#). In LLVM all the errors are classified into two types; *programmatic* and *recoverable*. Assertions are used heavily in the llvm-project. An assertion can be made to check if any code invariant or condition is being broken.

For example:

In `clang/lib/Analysis/CallGraph.cpp` we can find the following:

```
assert(*CI != Root && "No one can call the root node.")
```

We know that in a cfg there can be no calls to the root node, this assert is placed in the code to ensure this condition and also holds a message so that the user can know why the program has stopped execution.

## Recoverable Errors

Recoverable errors are mostly the errors which occur because of reasons other than mistakes in the source code. These errors should be reported to the user as well. Reported errors are handled using the error scheme provided by LLVM. The error class can be used for any user defined errors and we can also specify information regarding the error in it. Template functions like `make_error` are provided to construct failure values for the error class created by us which inherit from `ErrorInfo`.

# LLVM IR

LLVM IR is one of the most important components of LLVM if not the most important. An IR has the distinction of being lower level than a user programming language but also being able to express all high level constructs clearly. LLVM IR is present through all the phases of the LLVM compilation process. The following are a few observations about the LLVM IR:

1. LLVM IR is an SSA (static single assignment) based representation.
2. A defining feature about the IR is that it is *typed*.
3. LLVM resembles assembly but is capable of a lot more than in. For example, in LLVM IR we are allowed to use an infinite number of registers.
4. The LLVM IR can be represented in three equivalent formats: a human readable assembly like format, a bitcode representation and an in-memory IR. All the three formats are inter-convertible and lossless. The following commands are useful:  

```
clang -emit-llvm <filename> -S  
clang -emit-llvm <filename> -c  
llvm-as <filename.ll>  
llvm-dis <filename.s>
```
5. The LLVM IR programs consist of modules which is considered as one translation unit of the program.
6. Each program begins with the meta data like module name, source name, etc. It also specifies a lot of information about the target.
7. LLVM also has phi nodes which are instructions used to select values depending on the predecessor of the current block.
8. Some LLVM instructions end with an `align` which informs the compiler on how the variable is to be stored in memory.
9. Most common constructs like classes, structs, etc are lowered down into LLVM by converting them into LLVM derived types.
10. The LLVM type system consists of *Primitives* (integer, label, etc) and *Derived* (pointer, array, etc) types.
11. LLVM programs contain four types of structures:
  - Module
  - Function
  - Basic Block
  - Instruction

12. There is explicit load and store instructions and explicit stack allocation. Space on the stack is allocated used `alloca`.
13. Global variables are names with like: `@<name>` .
14. Functions definitions consist of the `define` keyword. Functions also have attribute list is in the form of `#X`.



# Assembly Language

We can generate the assembly code for a program in a host of different ways:

```
clang -S foo.c
```

```
gcc -S foo.c
```

```
llc bar.ll
```

Assembly code is architecture specific and must use instructions similar to the ones in the respective ISA.

## Name mangling

Assembly code does not have complex constructs like classes, namespaces, virtual functions, etc which are used by cpp to support polymorphism like cpp, so it cannot have multiple entities (like functions) with the same values. Thus, the compiler resorts to distorting the names of such functions according to some rules known as *name mangling*.

I conducted a simple experiment in which I made two classes and defined a function in both the classes with the same name. Then I used the function from both the classes in main. Converting this code into assembly, as expected showed name mangling. With class names `ca` and `cb` and function name `foo` I got the following mangled names in the assembly code `_ZN2ca3fooEi` and `_ZN2cb3fooEi`.

After some further research online the name mangling for C++ follows the **Itanium C++ ABI**. The complete set of rules can be found [here](#). Some general rules are like: mangled names must begin with `_Z`, followed by the encoding which uses the function name, namespace, type, etc.

# Compiler Toolchain

Other tools and options from the LLVM toolchain explored are:

- **llvm-dis**: Allows us to convert from IR bitcode(.bc) version to IR human-readable(.ll).
- **llvm-as**: Allows us to convert from .ll to .bc.
- **llc**: A static compiler which can convert files from IR to assembly.
- **lli**: This works as an interpreter for the llvm IR using which we can directly execute the llvm IR.
- **llvm-stress**: This is a very useful tool when we would like to test any kind of llvm component, we can use this to generate a random .ll file which can be used in testing/-analysis.
- We can use options like **-O3**, **-O2**, etc with clang to get different amounts of optimisations, for example using **-O3** reduces constant summation to the direct answer while **-O0** performs the constant summation during runtime.
- On many occasions clang can be used as a drop in for gcc directly, for example we can generate assembly code using **clang -S**.
- **opt**: This can be used to run various transformations on the code and can be extended by user.  
We can use **opt --<optimisation> foo.ll** with provided optimisations.  
We can also use **opt -load /path/foo.so -registeredPass bar.ll** for custom passes.
- Most of the code in LLVM passes is written in *anonymous namespaces*, it may be the case to avoid conflicts between same names utilities in different passes.
- We can use the option **-time-passes** to get information about time taken by the passes, time taken by IR parsing, etc.
- **--scalar-evolution** is an option provided in **opt** which makes updates of values in loops much faster by using techniques like recurrence arithmetic.
- We can use various iterators to iterate over function, loops and even basic blocks.
- Going all the way down we can even get the opcodes used in every instruction of a basic block.

# Kaleidoscope

Kaleidoscope is an example language used in the tutorial for LLVM. It displays the power of the llvm infrastructure to develop compiler tools. The following are a few characteristics of the language:

1. The language has only one data type, a 64 bit floating point type.
2. Type declaration are not required.
3. The language is so simple that the lexer has just 5 different types of tokens.
4. The parser we make for this language is a simple one, it uses a combination of Recursive Descent Parsing and Operator-Precedence Parsing. We use operator precedence only for the binary operators. There are only four binary operator: `<`, `+`, `-`, `*` in increasing order of precedence,
5. Top level functions are parsed as zero argument functions.
6. The nodes are represented as classes which inherit all the members of the children. For this purpose `std::move` is used very often.
7. The conversion from the Abstract syntax tree to LLVM IR is made possible by adding virtual code generation methods to all the classes of the ast.
8. Using classes like `Builder`, `Function`, `ConstantFP` we can convert most of our ast into ir code.
9. We do not need to worry about code optimisations as once we have the ir we can use all of the available llvm optimisations with `FunctionPassManager`.
10. It is also not too much work to add JIT support once we have already got the ast generators working.

## Appendix

### References

- <https://llvm.org/docs/LangRef.html>
- <https://llvm.org/docs/tutorial/index.html>
- <http://llvm.org/pubs/2004-09-22-LCPCLLVMTutorial.html>
- <https://clang.llvm.org/docs/index.html>
- <https://itanium-cxx-abi.github.io/cxx-abi/abi.html#mangling>
- <http://swtv.kaist.ac.kr/courses/cs453-fall13/Clang%20tutorial%20v4.pdf>

## Code

### .ll files

Program to check is a given number is a palindrome.

```
1 ; ModuleID = 'palindrome.c'
2 source_filename = "palindrome.c"
3 target datalayout = "e-m:e-i64:64-f80:128-n8:16:32:64-S128"
4 target triple = "x86_64-unknown-linux-gnu"
5
6 @.str = private unnamed_addr constant [19 x i8] c"Enter an integer: \00",
    align 1
7 @.str.1 = private unnamed_addr constant [3 x i8] c"%d\00", align 1
8 @.str.2 = private unnamed_addr constant [20 x i8] c"%d is a palindrome
    .\00", align 1
9 @.str.3 = private unnamed_addr constant [24 x i8] c"%d is not a
    palindrome.\00", align 1
10
11 ; Function Attrs: noinline nounwind optnone uwtable
12 define dso_local i32 @main() #0 {
13 entry:
14     %retval = alloca i32, align 4
15     %n = alloca i32, align 4
16     %reversedInteger = alloca i32, align 4
17     %remainder = alloca i32, align 4
18     %originalInteger = alloca i32, align 4
19     store i32 0, i32* %retval, align 4
20     store i32 0, i32* %reversedInteger, align 4
21     %call = call i32 @__printf(i8* getelementptr inbounds ([19 x
        i8], [19 x i8]* @.str, i64 0, i64 0))
22     %call1 = call i32 @__isoc99_scanf(i8* getelementptr inbounds
        ([3 x i8], [3 x i8]* @.str.1, i64 0, i64 0), i32* %n)
23     %0 = load i32, i32* %n, align 4
24     store i32 %0, i32* %originalInteger, align 4
25     br label %while.cond
26
27 while.cond:                                ; preds = %while.body,
    %entry
28     %1 = load i32, i32* %n, align 4
29     %cmp = icmp ne i32 %1, 0
30     br i1 %cmp, label %while.body, label %while.end
31
32 while.body:                                ; preds = %while.cond
33     %2 = load i32, i32* %n, align 4
34     %rem = srem i32 %2, 10
35     store i32 %rem, i32* %remainder, align 4
36     %3 = load i32, i32* %reversedInteger, align 4
37     %mul = mul nsw i32 %3, 10
38     %4 = load i32, i32* %remainder, align 4
39     %add = add nsw i32 %mul, %4
40     store i32 %add, i32* %reversedInteger, align 4
41     %5 = load i32, i32* %n, align 4
42     %div = sdiv i32 %5, 10
```

```

43 store i32 %div, i32* %n, align 4
44 br label %while.cond
45
46 while.end:                                ; preds = %while.cond
47 %6 = load i32, i32* %originalInteger, align 4
48 %7 = load i32, i32* %reversedInteger, align 4
49 %cmp2 = icmp eq i32 %6, %7
50 br i1 %cmp2, label %if.then, label %if.else
51
52 if.then:                                    ; preds = %while.end
53 %8 = load i32, i32* %originalInteger, align 4
54 %call3 = call i32 @i8*, ... @printf(i8* getelementptr inbounds ([20 x
    i8], [20 x i8]* @.str.2, i64 0, i64 0), i32 %8)
55 br label %if.end
56
57 if.else:                                    ; preds = %while.end
58 %9 = load i32, i32* %originalInteger, align 4
59 %call4 = call i32 @i8*, ... @printf(i8* getelementptr inbounds ([24 x
    i8], [24 x i8]* @.str.3, i64 0, i64 0), i32 %9)
60 br label %if.end
61
62 if.end:                                    ; preds = %if.else, %if
    .then
63 ret i32 0
64 }
65
66 declare dso_local i32 @printf(i8*, ...) #1
67
68 declare dso_local i32 @__isoc99_scanf(i8*, ...) #1
69
70 attributes #0 = { noinline nounwind optnone uwtable "correctly-rounded-
    divide-sqrt-fp-math"="false" "disable-tail-calls"="false" "frame-
    pointer"="all" "less-precise-fpmad"="false" "min-legal-vector-width"="
    0" "no-infs-fp-math"="false" "no-jump-tables"="false" "no-nans-fp-math"
    ="false" "no-signed-zeros-fp-math"="false" "no-trapping-math"="false"
    "stack-protector-buffer-size"="8" "target-cpu"="x86-64" "target-
    features"="+cx8,+fxsr,+mmx,+sse,+sse2,+x87" "unsafe-fp-math"="false"
    "use-soft-float"="false" }
71 attributes #1 = { "correctly-rounded-divide-sqrt-fp-math"="false" "
    disable-tail-calls"="false" "frame-pointer"="all" "less-precise-fpmad"
    ="false" "no-infs-fp-math"="false" "no-nans-fp-math"="false" "no-
    signed-zeros-fp-math"="false" "no-trapping-math"="false" "stack-
    protector-buffer-size"="8" "target-cpu"="x86-64" "target-features"="+
    cx8,+fxsr,+mmx,+sse,+sse2,+x87" "unsafe-fp-math"="false" "use-soft-
    float"="false" }
72
73 !llvm.module.flags = !{!0}
74 !llvm.ident = !{!1}
75
76 !0 = !{i32 1, !"wchar_size", i32 4}
77 !1 = !{!"clang version 10.0.0 (git@github.com:llvm/llvm-project.git 2
    bebe19708c7bb3feb5288d0d0657b5be2fe5fce)"}

```

**Shell Sort with -O1**, clearly the code size has blown up after optimisation.

```

1 ; ModuleID = 'shellSort.c'
2 source_filename = "shellSort.c"
3 target datalayout = "e-m:e-i64:64-f80:128-n8:16:32:64-S128"
4 target triple = "x86_64-unknown-linux-gnu"
5
6 @__const.main.data = private unnamed_addr constant [10 x i32] [i32 9, i32
    12, i32 54, i32 90, i32 0, i32 100, i32 65, i32 32, i32 54, i32 81],
    align 16
7 @.str = private unnamed_addr constant [4 x i8] c"%d\0A\00", align 1
8
9 ; Function Attrs: nofree norecurse nounwind uwtable
10 define dso_local void @shellsort(i32* nocapture %v, i32 %n)
    local_unnamed_addr #0 {
11 entry:
12     %cmp61 = icmp sgt i32 %n, 1
13     br i1 %cmp61, label %for.cond1.preheader, label %for.end25
14
15 for.cond.loopexit:                                ; preds = %for.inc21, %
    for.cond1.preheader
16     %cmp = icmp sgt i32 %gap.062.in, 3
17     br i1 %cmp, label %for.cond1.preheader, label %for.end25
18
19 for.cond1.preheader:                                ; preds = %entry, %for.
    cond.loopexit
20     %gap.062.in = phi i32 [ %gap.062, %for.cond.loopexit ], [ %n, %entry ]
21     %gap.062 = sdiv i32 %gap.062.in, 2
22     %cmp257 = icmp slt i32 %gap.062, %n
23     br i1 %cmp257, label %for.cond4.preheader, label %for.cond.loopexit
24
25 for.cond4.preheader:                                ; preds = %for.cond1.
    preheader, %for.inc21
26     %i.058 = phi i32 [ %inc, %for.inc21 ], [ %gap.062, %for.cond1.preheader
    ]
27     %j.053 = sub nsw i32 %i.058, %gap.062
28     %cmp554 = icmp sgt i32 %j.053, -1
29     br i1 %cmp554, label %land.rhs, label %for.inc21
30
31 land.rhs:                                            ; preds = %for.cond4.
    preheader, %for.body9
32     %j.056 = phi i32 [ %j.0, %for.body9 ], [ %j.053, %for.cond4.preheader ]
33     %i.0.pn55 = phi i32 [ %j.056, %for.body9 ], [ %i.058, %for.cond4.
    preheader ]
34     %idxprom = sext i32 %j.056 to i64
35     %arrayidx = getelementptr inbounds i32, i32* %v, i64 %idxprom
36     %0 = load i32, i32* %arrayidx, align 4, !tbaa !2
37     %idxprom6 = sext i32 %i.0.pn55 to i64
38     %arrayidx7 = getelementptr inbounds i32, i32* %v, i64 %idxprom6
39     %1 = load i32, i32* %arrayidx7, align 4, !tbaa !2
40     %cmp8 = icmp sgt i32 %0, %1
41     br i1 %cmp8, label %for.body9, label %for.inc21
42
43 for.body9:                                            ; preds = %land.rhs

```

```

44   store i32 %1, i32* %arrayidx, align 4, !tbaa !2
45   store i32 %0, i32* %arrayidx7, align 4, !tbaa !2
46   %j.0 = sub nsw i32 %j.056, %gap.062
47   %cmp5 = icmp sgt i32 %j.0, -1
48   br i1 %cmp5, label %land.rhs, label %for.inc21
49
50 for.inc21:                                ; preds = %for.body9, %
    land.rhs, %for.cond4.preheader
51   %inc = add nsw i32 %i.058, 1
52   %exitcond = icmp eq i32 %inc, %n
53   br i1 %exitcond, label %for.cond.loopexit, label %for.cond4.preheader
54
55 for.end25:                                ; preds = %for.cond.
    loopexit, %entry
56   ret void
57 }
58
59 ; Function Attrs: argmemonly nounwind willreturn
60 declare void @llvm.lifetime.start.p0i8(i64 immarg, i8* nocapture) #1
61
62 ; Function Attrs: argmemonly nounwind willreturn
63 declare void @llvm.lifetime.end.p0i8(i64 immarg, i8* nocapture) #1
64
65 ; Function Attrs: nounwind uwtable
66 define dso_local i32 @main() local_unnamed_addr #2 {
67 entry:
68   %data = alloca [10 x i32], align 16
69   %0 = bitcast [10 x i32]* %data to i8*
70   call void @llvm.lifetime.start.p0i8(i64 40, i8* nonnull %0) #4
71   call void @llvm.memcpy.p0i8.p0i8.i64(i8* nonnull align 16 %0, i8* align
    16 bitcast ([10 x i32]* @__const.main.data to i8*), i64 40, i1
    false)
72   br label %for.body
73
74 for.body:                                  ; preds = %for.body, %
    entry
75   %indvars.iv18 = phi i64 [ 0, %entry ], [ %indvars.iv.next19, %for.body
    ]
76   %arrayidx = getelementptr inbounds [10 x i32], [10 x i32]* %data, i64
    0, i64 %indvars.iv18
77   %1 = load i32, i32* %arrayidx, align 4, !tbaa !2
78   %call = tail call i32 @printf(i8* getelementptr inbounds ([4
    x i8], [4 x i8]* @.str, i64 0, i64 0), i32 %1)
79   %indvars.iv.next19 = add nuw nsw i64 %indvars.iv18, 1
80   %exitcond20 = icmp eq i64 %indvars.iv.next19, 10
81   br i1 %exitcond20, label %for.end, label %for.body
82
83 for.end:                                  ; preds = %for.body
84   %arraydecay = getelementptr inbounds [10 x i32], [10 x i32]* %data, i64
    0, i64 0
85   call void @shellsort(i32* nonnull %arraydecay, i32 10)
86   br label %for.body3
87
88 for.body3:                                ; preds = %for.body3, %

```

```

    for.end
89 %indvars.iv = phi i64 [ 0, %for.end ], [ %indvars.iv.next, %for.body3 ]
90 %arrayidx5 = getelementptr inbounds [10 x i32], [10 x i32]* %data, i64
    0, i64 %indvars.iv
91 %2 = load i32, i32* %arrayidx5, align 4, !tbaa !2
92 %call6 = tail call i32 @i8*, ... @printf(i8* getelementptr inbounds
    ([4 x i8], [4 x i8]* @.str, i64 0, i64 0), i32 %2)
93 %indvars.iv.next = add nuw nsw i64 %indvars.iv, 1
94 %exitcond = icmp eq i64 %indvars.iv.next, 10
95 br i1 %exitcond, label %for.end9, label %for.body3
96
97 for.end9:                                ; preds = %for.body3
98   call void @llvm.lifetime.end.p0i8(i64 40, i8* nonnull %0) #4
99   ret i32 0
100 }
101
102 ; Function Attrs: argmemonly nounwind willreturn
103 declare void @llvm.memcpy.p0i8.p0i8.i64(i8* nocapture writeonly, i8*
    nocapture readonly, i64, i1 immarg) #1
104
105 ; Function Attrs: norecurse nounwind
106 declare dso_local i32 @printf(i8* nocapture readonly, ...)
    local_unnamed_addr #3
107
108 attributes #0 = { norecurse nounwind uwtable "correctly-rounded-
    divide-sqrt-fp-math"="false" "disable-tail-calls"="false" "frame-
    pointer"="none" "less-precise-fpmad"="false" "min-legal-vector-width"=
    "0" "no-infs-fp-math"="false" "no-jump-tables"="false" "no-nans-fp-
    math"="false" "no-signed-zeros-fp-math"="false" "no-trapping-math"=
    "false" "stack-protector-buffer-size"="8" "target-cpu"="x86-64" "target-
    features"="+cx8,+fxsr,+mmx,+sse,+sse2,+x87" "unsafe-fp-math"="false"
    "use-soft-float"="false" }
109 attributes #1 = { argmemonly nounwind willreturn }
110 attributes #2 = { nounwind uwtable "correctly-rounded-divide-sqrt-fp-math"
    ="false" "disable-tail-calls"="false" "frame-pointer"="none" "less-
    precise-fpmad"="false" "min-legal-vector-width"="0" "no-infs-fp-math"=
    "false" "no-jump-tables"="false" "no-nans-fp-math"="false" "no-signed-
    zeros-fp-math"="false" "no-trapping-math"="false" "stack-protector-
    buffer-size"="8" "target-cpu"="x86-64" "target-features"="+cx8,+fxsr,+
    mmx,+sse,+sse2,+x87" "unsafe-fp-math"="false" "use-soft-float"="false"
    }
111 attributes #3 = { norecurse nounwind "correctly-rounded-divide-sqrt-fp-math"
    ="false" "disable-tail-calls"="false" "frame-pointer"="none" "less-
    precise-fpmad"="false" "no-infs-fp-math"="false" "no-nans-fp-math"=
    "false" "no-signed-zeros-fp-math"="false" "no-trapping-math"="false"
    "stack-protector-buffer-size"="8" "target-cpu"="x86-64" "target-
    features"="+cx8,+fxsr,+mmx,+sse,+sse2,+x87" "unsafe-fp-math"="false"
    "use-soft-float"="false" }
112 attributes #4 = { nounwind }
113
114 !llvm.module.flags = !{!0}
115 !llvm.ident = !{!1}
116
117 !0 = !{i32 1, !"wchar_size", i32 4}

```



```
118 !1 = !{!"clang version 10.0.0 (git@github.com:llvm/llvm-project.git 2
    bebe19708c7bb3feb5288d0d0657b5be2fe5fce)"}
119 !2 = !{!3, !3, i64 0}
120 !3 = !{!"int", !4, i64 0}
121 !4 = !{!"omnipotent char", !5, i64 0}
122 !5 = !{!"Simple C/C++ TBAA"}
```