

# Saha Kuljit Shantanu

✉ +880 184 235 2155 | @ sahakuljitshantanubuet@gmail.com | LinkedIn | GitHub | Portfolio | Dhaka, Bangladesh

## EDUCATION

---

### Notre Dame College

Dhaka, Bangladesh

HSC

2017 – 2019

GPA: 5.00

### Bangladesh University of Engineering Technology

Dhaka, Bangladesh

*B.Sc. in Computer Science and Engineering*

Feb 2020 – Mar 2025

CGPA: 3.54

## SKILLS

---

**Programming Languages:** C/C++, Java, Python, JavaScript, TypeScript, SQL

**Communication:** English(IELTS 7.5), Bengali(Native), German(A<sub>1</sub>), French(Elementary)

**Technologies:** Flask, Express.js, Node.js, Sveltekit, Oracle, Git, Vercel, Azure, SSLCmmerz, LTSpice, AutoCAD, OpenCV, PyTorch, TensorFlow, Postman

**Methodologies:** OOP, Functional Programming, DevOps

## EXPERIENCE

---

### Lecturer, Department of CSE

*Jul 2025 – Present*

*International University of Business, Agriculture and Technology*

#### Fall 2025 Classes ( October 6, 2025 Onwards )

- Software Engineering
- C Programming

#### Summer 2025 Classes ( July 9, 2025 - September 29, 2025 )

- Compiler Design
- Web Engineering

#### Ongoing Projects

- Conducting a study on the impact of a legal Chatbot on general people of Bangladesh

## PUBLICATION

---

### EduPresenta: A Conversational AI Agent for pedagogically Sound Presentation Generation for Instructors

*Proposal Accepted*

*28<sup>th</sup> International Conference on Human Computer Interaction (HCII'2026)*

1<sup>st</sup> Author

### Extending Feature Selection Strategies in VGG16:

*Accepted*

### Convolutional Feature Aggregation for Content-Based Image Retrieval

*IEEE 5<sup>th</sup> International Conference on Computing and Machine Intelligence (ICMI'2026)*

1<sup>st</sup> Author

## PROJECTS

---

### Context-Based Image Retrieval | [Github](#)

- This project is an evaluation framework of my undergraduate thesis related to computer vision, where the system takes an image as a query and outputs images prior to its relevance.
- **STACK:** [ svelte, flask ]

### Computer Architecture Projects | [Github](#)

- A collection of my Computer Architecture projects. It includes the structural design of a 4 bit ALU and a 32 bit floating point adder. Additionally, there is a fully functional 4 bit Computer driven by MIPS instructions
- **STACK:** [ Logisim, ATMega ]