PVG'S COLLEGE OF ENGINEERING & TECHNOLOGY AND G.K. PATE (WANI) INSTITUTE OF MANAGEMENT, PUNE 411009

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION

411009/PVGC/ET

Certificate of Participation

EMBEDDED SYSTEM DESIGN WORKSHOP

This is to certify that

Sahil Ambekar

has participated in the **3 day Embedded System Design Workshop** organized by Institution Of Engineer Student's Chapter, E&TC Department at PVG's COET & GKPIM, Pune in association with AMOTECH LABS held on 9,10 & 11 August 2024.

Prof. S. K. Bavkar
Staff co-ordinator

Dr. Y.B. Thakare
IEI & Skill Dev. Co-ordinator

Dr. P.G. Shete
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Dr. M.R. Tarambale
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