- Binding of ids.

CC

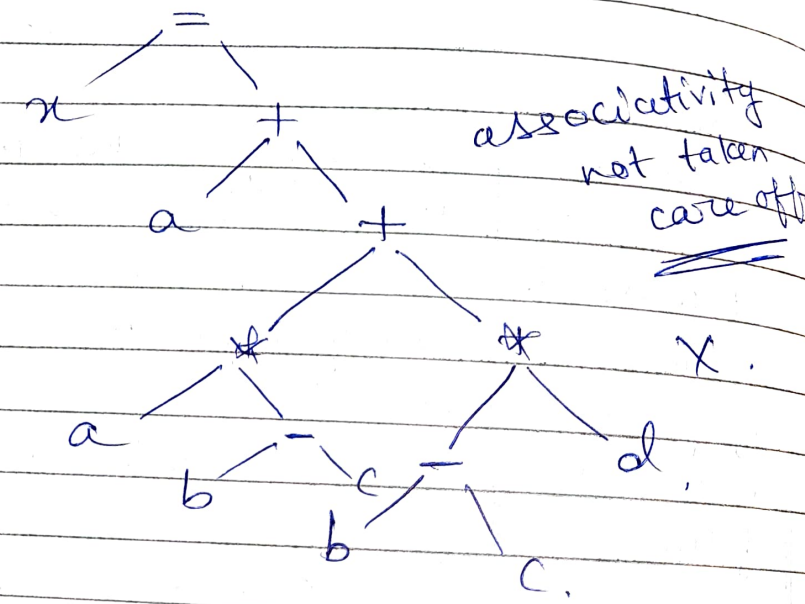
4- tokens → syntax → syntax tree → type checking / semantic analysis → Intermediate Code Representation

- Intermediate Code Representation -

1) Syntax tree. 2). DAGs (Directed Acyclic Graphs)

ex :- x = a + a * (b-c) + (b-c) * d

AST

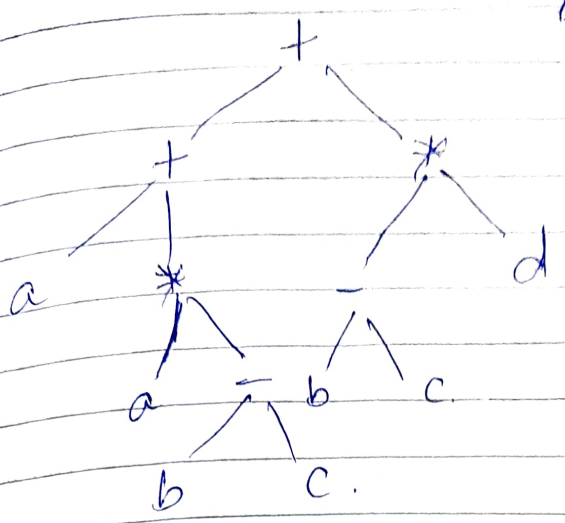


associativity not taken care of!!

X.

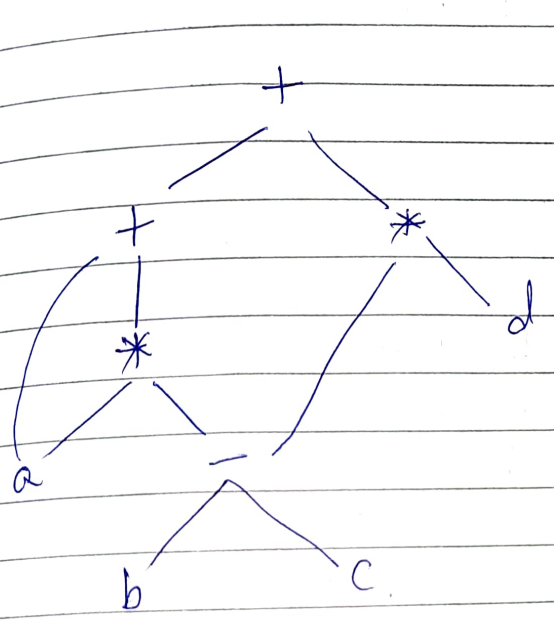
DAG

* '+' left associative.



AST

DAG.



common subexpressions removed.

3AC (3 address code) {at most 3 addresses}

- quadruples
- triples
- indirect triples

ex:- a = y + x + z.

$t_1 = y + x$

$t_2 = t_1 + z$.

$a = t_2$

} 3AC for given expression

if (x < y) then addr1 else addr2 } more than 3 addresses

C me IR nhi hota.
Java has ICG thing!!

→ (a) quadriples - → 4 columns / 4 cells

	operator	arg1	arg2	result
0	−	c		t_1
1	*	b	t_1	t_2
2	−	c		t_3
3	*	b	t_3	t_4
4	+	t_2	t_4	t_5
5	=	t_5		a
6				

Why * rehe than +

ex :- a = b * − c + b * − c

→ triples

	op	arg1	arg2
0	−	c	
1	*	b	(0)
2	−	c	
3	*	b	(2)
4	+	(1)	(3)
5	=	a	(4)

'−' → Unary
hence Priority than Binary

→ Indirect triples

35	(0)
36	(1)
37	(2)
101	(3)
108	(4)
109	(5)
⋮	⋮

ass:- ICG with quadruples & triples, take any expression.

ass- SLR parser generator

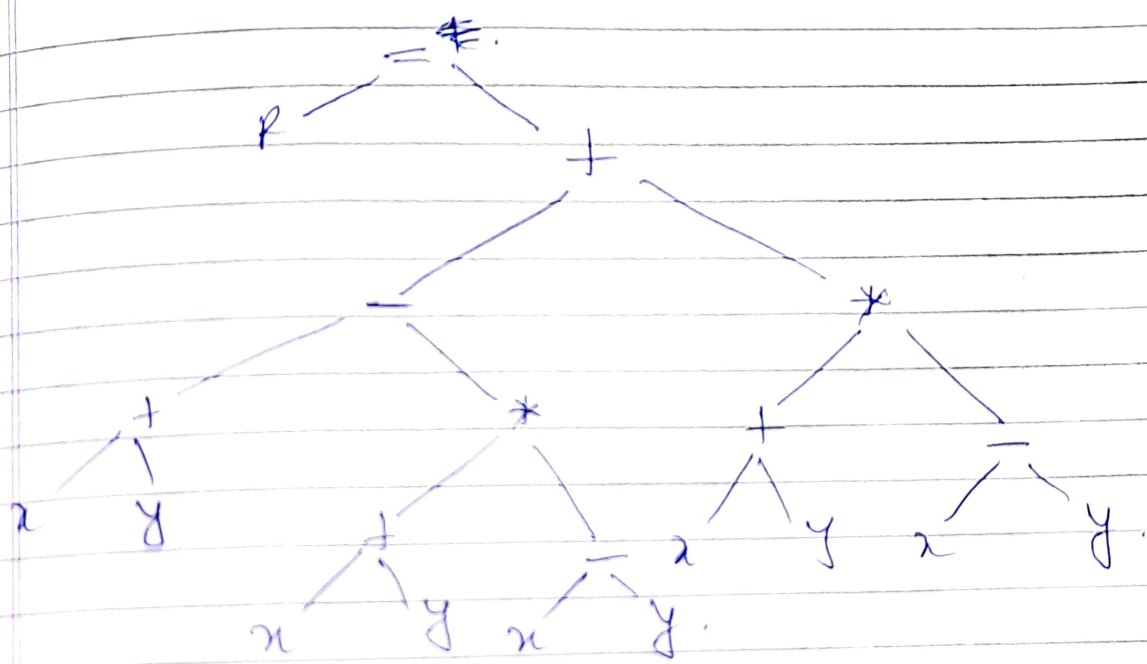
Low level virtual machine

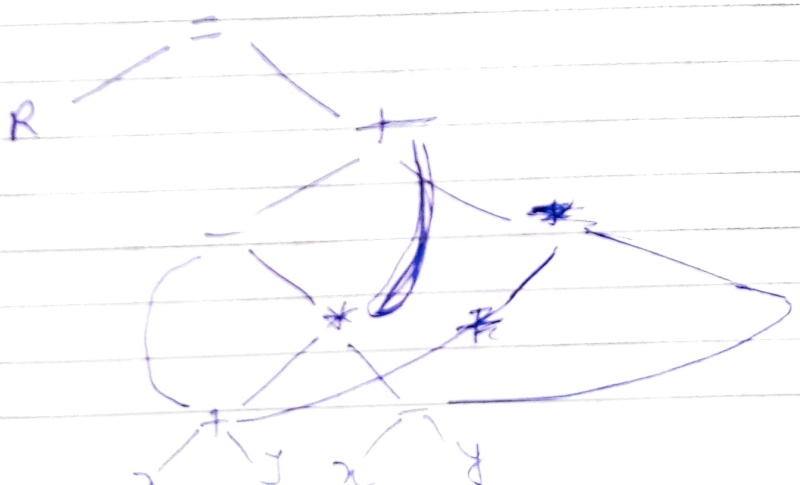
- SSA (Single Static Assignment) - [LLVM uses this]

$p_1 = a + b$
$q_1 = p_1 - c$
$p_2 = q_1 * d$
$p_3 = e - p_2$
$q_2 = p_3 + q_1$

useful for Dataflow Analysis, at the time of optimizat", or CG.

ex:- $R = ((x+y) - ((x+y) * (x-y))) + ((x+y) * (x-y))$





$((x+y)-((x+y)*(x-y)))+((x+y)*(x-y))$

	operator	org1	org2	result
0	+	x	y	t_1
1	−	x	y	t_2
2	*	t_1	t_2	t_3
3	+	x	y	t_4
4	−	t_4	t_3	t_5
5	+	x	y	t_6
6	−	x	y	t_7
7	*	t_6	t_7	t_8
8	+	t_5	t_8	t_9
9	=	t_9		R

	operator	org1	org2
0	+	x	y
1	−	x	y
2	*	(0)	(1)
3	+	x	y
4	−	(3)	(2)
5	+	x	y
6	−	x	y
7	*	(5)	(6)
8	+	(4)	(7)
9	=	(8)	R

Finding IP address

→ You can use ping command at your prompt.

→ This command is

5) Hybrid cloud migration -

techniques

1) lift & shift.

2) cloud bursting.

Process :

Pros : cost optimization, flexibility, managing

Cons : Network latency.

CC

3AC

Arrays 1D $X = a[i]$

base + i x w

$t_1 = i \times w$

$t_2 = a[t_1]$ { base+t1

3AC

To find the address of any element in a 2-Dimensional array there are the following two ways:
Row Major Order
Column Major Order

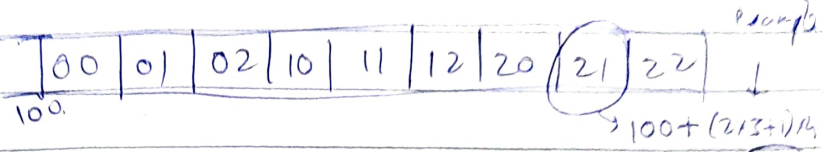
2D Arrays

00	01	02
10	11	12
20	21	22

3X3

X = a[i][j]
a[row][col]

row major



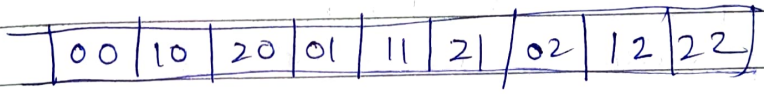
L = a[i][j] = base + (i x c + j) * w.

col. width of each element.

3AC
$t_1 = i \times c$
$t_2 = t_1 + j$
$t_3 = t_2 \times w$
$t_4 = a[t_3]$
$X = t_4$

NOT $a + t_3$

col major



base + (j x r + i) * w.
row

3D Arrays a[i][j][k]. a[r][c][b].

LOC a[i][j][k] = base + (i x c x b + j x b + k) * w
col. last dim. width

$t_1 = i \times c$
$t_2 = t_1 \times b$
$t_3 = j \times b$
$t_4 = t_2 + t_3$
$t_5 = t_4 + k$
$t_6 = t_5 * w$
$t_7 = a[t_6]$
$X = t_7$

Q. $t_0 = i \times 1024$
$t_1 = j \times 32$
$t_2 = k \times 4$
$t_3 = t_1 + t_0$
$t_4 = t_3 + t_2$
$t_5 = X[t_4]$
what is c & b?

Ans $t_5 = X[i \times 1024 + j \times 32 + k \times 4]$
$= X\,4(i \times 256 + j \times 8 + k)$
b, integer array

$b = 8$,
$(i \times 8 \times 32 + j \times 8 + k) \times 4$
$c =$

$c = 32, b = 8$

- **Boolean expression-**
operator - &, |, !
precedence - not > and > or

ex:- a and b or not c
$t_1 = \text{not } c$
$t_2 = a \text{ and } b$
$t_3 = t_2 \text{ or } t_1$
} 3AC

- **Conditional statements -**
→ if, while, for etc.
→ Jump or goto statements having addresses

if (a<b) then 1 else 0

2 ways of doing it

way ① 3AC

100 : if (a<b) goto 103
101 : $t_1 = 0$
102 : goto 104
103 : $t_1 = 1$
104 :

→ else part 1st
→ after else, exit

way ② 3AC

L1 : if (a<b) goto L_t
$t_1 = 0$
goto exit
L_t : $t_1 = 1$.
exit: ~~exit~~

ex:2. if (a<b) then X = Y+1 else X = Y-1

3AC

100 : if (a<b) goto 104
101 : ~~X = Y+1~~ $t_1 = Y-1$
102 : ~~goto 105~~ X = t_1.
103 : ~~X = Y+1~~ goto 106
104 : ~~[exit or blank]~~ $t_1 = Y+1$
105 : X = t_1
106 :

Eg value
X = a+b
↓ add^n
↓ SO
$t_1 = a+b$
X = t_1

ex3 - while (c<d) do
if (a<b) then X = y+1 else X = Y-1.

100 : if (c<d) goto 102
101 : goto ~~103~~ → 109
102 : if (a<b) goto 106
103 : $t_1 = Y-1$
104 : X = t_1
105 : goto ~~108~~ 100
106 : $t_1 = Y+1$
107 : X = t_1
108 : goto 100
109 : exit