**CAI Contact**

Tommy Tompkins

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### <PleasE insert Candidate’s Name hERE>

### Skills

• big data, C, C++, charts, Circuit design, hardware, Data acquisition, data analyst, data collection, data mining, databases, database, dBm, DTI,

• design software, documentation, functional, GSM, innovation, Java, LabView, Team leader, Logic Analyzer, Mac, manufacturing processes,

• Matlab, Excel, Microsoft office, Office, Microwave, Radar, NCs, Network, dB, packaging, pivot tables, Programming, project design, proposals,

• Publication, Python, quality, requirement, research, SAS, self-starter, Spectrum analyzer, SPSS, SQL, SSL, statistics, surveys, system design,

• troubleshooting, validation

Please use this table to list the skills noted in the **Required/Desired** section of the VectorVMS requirement. In addition, please respond with the years of experience for each skill **and** the last time each skill was used. Add or delete rows as necessary.

|  |  |  |
| --- | --- | --- |
| Skill | Years Used | Last Used |
| big data | 2+ years | 2013 |
| C | 11+ years | Present |
| C++ | 1+ years | Recent |
| charts | 2+ years | 2013 |
| Circuit design | 1+ years | Recent |
| hardware | 11+ years | Present |
| Data acquisition | 1+ years | Recent |
| data analyst | 11+ years | Present |
| data collection | 2+ years | 2013 |
| data mining | 2+ years | 2013 |
| databases | 2+ years | 2013 |
| database | 2+ years | 2013 |
| dBm | 1+ years | Recent |
| DTI | 1+ years | Recent |
| design software | 1+ years | Recent |

### Employment History

• Working RF Systems Engineer

• May 2014 to Current Company Name

• Qualification Â· Multidisciplinary background: RF hardware designs, manufacturing operations and data analyst.

• Summary Â· Experienced in developing hardware's DFM procedures, checklists and requirements to subcontractors Â· Perform EVT,

• DVT, PVT verifications and utilize FA process to drive root cause from system to unit level Â· Team leader on multiple end-to-end

• technical project design, development, testing and validation Â· Manage Test/Production readiness reviews and drives quality requirement

• for post-ramp qualification Â· Ability to manage multiple projects simultaneously, self-starter with innovation and sharp attention to details

• Experiences - New RF systems introduction on satellite communication architecture designs and proposals - Define RF hardware unit DFM

• procedures and requirements for Antenna, receivers, filters, and amplifiers - Lead suppliers by providing technical design specifications and

• testing requirements to meet SSL standards - Utilize FA process to monitor overall project field performance.

Please list the candidate’s relevant employment history.