HIGH PERFORMANCE COMPUTING PROJECT REPORT

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**Parallelization of Lattice Boltzmann Method Solver and 3D Mean Blur Stencil**

# 1. Problem Statement

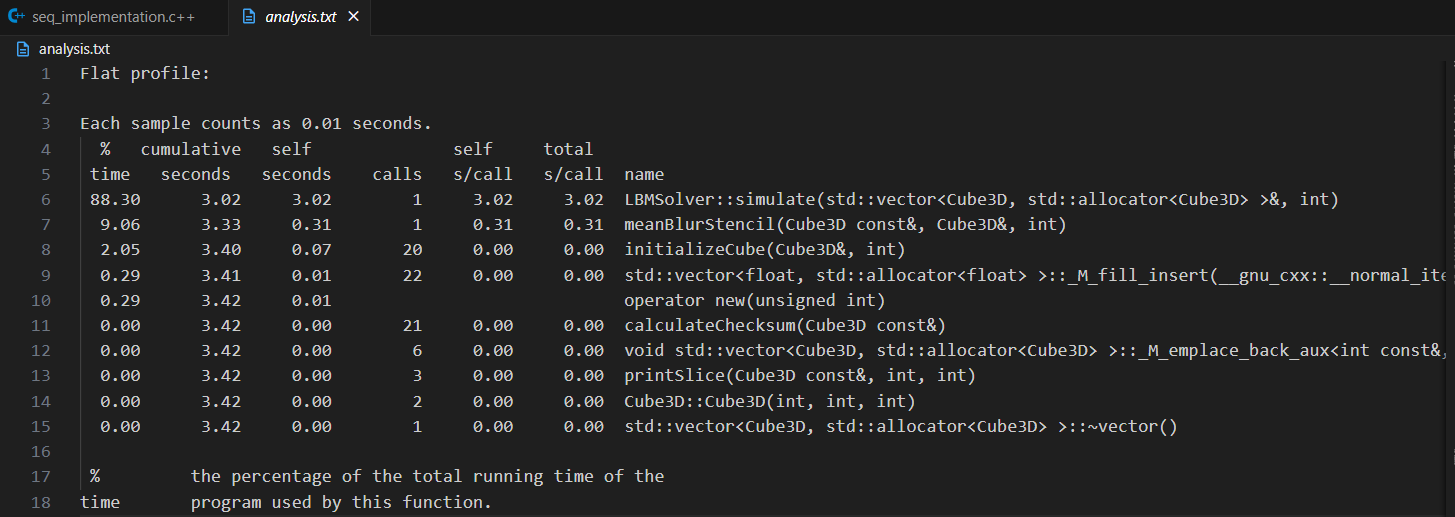
This project addresses the parallelization of the Lattice Boltzmann Method (LBM) solver and a 3D mean blur stencil. Both are stencil-based computations with strong data-parallel characteristics, making them suitable for High Performance Computing (HPC) platforms.The objective is to analyze performance bottlenecks in the sequential version, identify parallelizable hotspots, and implement parallelization using OpenMP and CUDA.

# 2. Sequential Implementation

A sequential C++ implementation was developed as the baseline.  
It includes:  
- Full LBM simulation (collision + streaming)  
- Configurable 3D mean blur stencil  
  
The sequential version works correctly but exhibits high runtime due to nested loops and cell-wise computations.

# 3. Performance Analysis (Sequential Profiling) : [GPROF Analysis](https://drive.google.com/file/d/1HgjL8y_pVRn5dTQg1mTmT9SHa3BWtpvD/view?usp=sharing)

We identified the following hotspots using gprof :



1)LBMSolver::simulate() function takes 88.3% runtime

• Nested loops updating 3D grid cells. These loops consume lot of time.  
 • Each cell operation is independent .So there is scope for parallelization.  
  
2)meanBlurStencil() function takes 9.06% runtime

• It includes Stencil Operation involving averaging of neighbourhood cells.

• Independent per-cell stencil operations are involved.So they are Parallelizable.

Other functions like initializing ,I/O, utility functions are not worth parallelizing.  
  
Conclusion: Both LBM simulation and stencil operations are parallelizable and offer high performance improvement potential.

# 4. Chosen Parallelization Platforms

Based on the profiling using GPROF :   
  
OpenMP (CPU shared-memory parallelization)  
- Ideal for parallelizing nested loops across cores  
- Simple integration with existing C++ code  
- Suitable for quick speedups on multi-core CPUs  
  
CUDA (GPU acceleration)  
- Best suited for massive parallelism in grid-based computations  
- Thread-per-cell execution model fits stencil patterns  
- Optimized memory hierarchy enables significant speedup  
  
Justification: Combining OpenMP and CUDA ensures scalability across CPU platforms, aligning with research findings on stencil optimizations in heterogeneous environments

# 5. Conclusion

We analyzed sequential performance, identified hotspots, and selected OpenMP and CUDA as parallelization platforms. These approaches target both CPU and GPU architectures, ensuring scalability and optimal performance for stencil computations such as LBM and 3D mean blur operations.Final evaluation will include comparative results to demonstrate achieved speedups.