14-3-19

Digital sydems

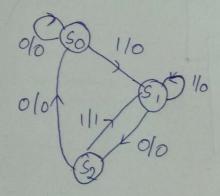
## Sequence Detector:

- -) The FSM doesn't neset
- -) outputs 'i' at the same time, the Last bit of the sequence

Example: "101" occusis, output is "1"

Sample Input: 00100110100 101010
Sample output: 00000000100 001010

## Melay Machine Design:



Present state	next state		output (y)	
	X=0	X=1	X=0	X2
So 00	So 00	S, OI	0	0
5, 01	S2 10	s, 01	0	0
5210	So 00	5, 01	6	1
S <sub>3</sub> 1 1	××	XX		

