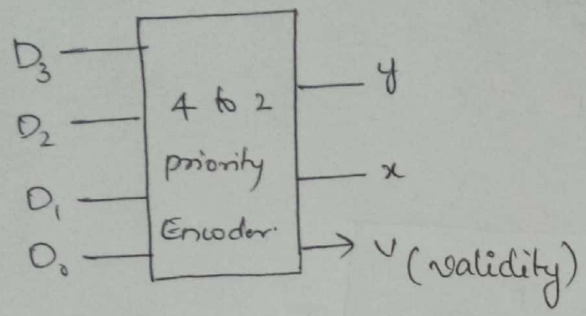
19/02

TUESDAY

DIGITAL SYSTEMS

PRIORITY ENCODER

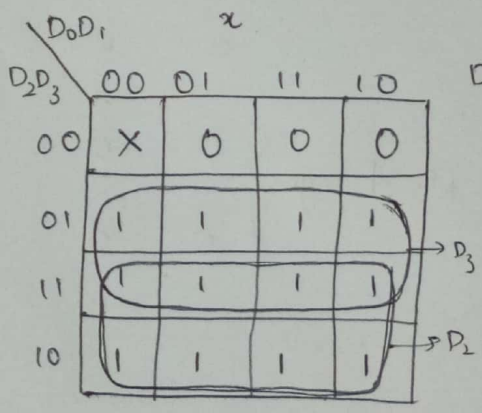


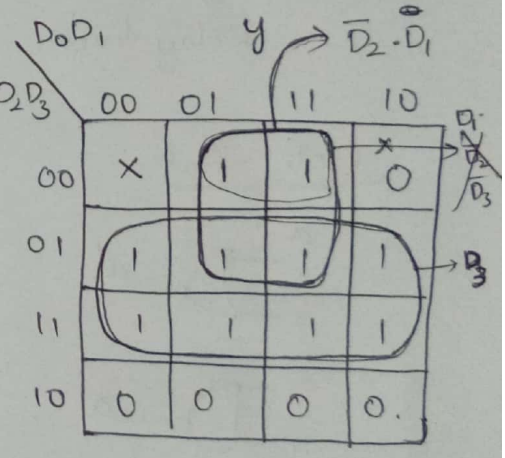
D_0	D_1	D_2	D_3	x	y	V
0	0	0	0	x	x	1
1	0	0	0	0	0	0
x	1	0	0	0	1	0
x	x	1	0	1	0	0
x	x	x	1	1	1	0

$V = \bar{D}_0 \cdot \bar{D}_1 \cdot \bar{D}_2 \cdot \bar{D}_3$

$x = D_3 + D_2$

$y = D_3 + D_1 \cdot \bar{D}_2$





FEED BACK FOR QUIZ-1

Not comfortable with order of questions

TIME NOT SUFFICIENT → < 10 ppl.

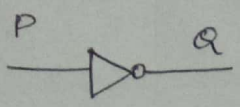
DIFFICULT → At least half the class, (difficult)

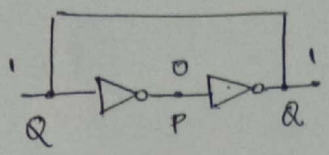
8 → ppl (moderate)

4-5 → ppl (very difficult.

Sequential Circuits

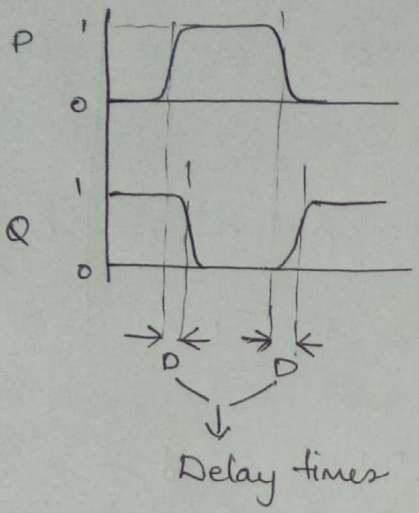
LATCHES & FLIP-FLOPS.

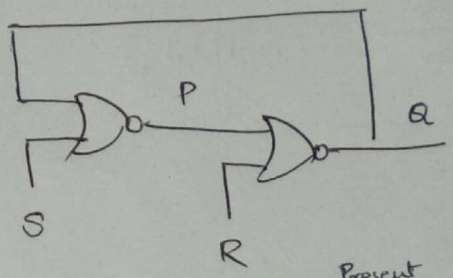




→ Stable state.

0 1 0.





S	R	Present State P(t)	Present State Q(t)	Next state P(t+ϵ)	Next state Q(t+ϵ)
0	0	1	0	1	0
0	0	0	1	0	1

No change

Note that