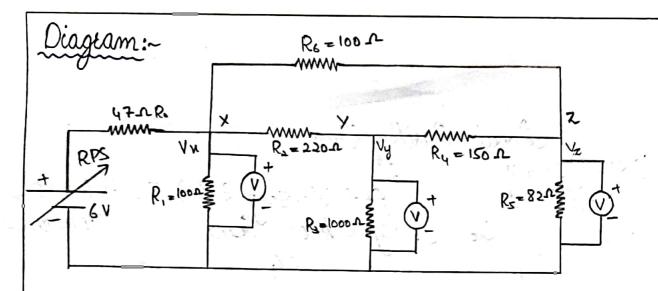


LABORATORY WORK SHEET

		me of the Student :										
	Class (C.S	Class (CSM-A) 1 st Year Semest			er			ımber				
	Course Cod	de : <i>AEE D</i> O	ქ Cou	rse Name : £	EE laborator	y 23	951	A 6.6	01			
	Course Code: AEE DO1 Course Name: EEE (aboutory 23951A6601) Name of the Course Faculty De Rajashekhas Goud Faculty ID: 1ARE 11067											
	Exercise Number:Date:											
	DAY TO DAY EVALUATION:											
	Marks	Alm /	Algorithm	/ Procedure	Source Code		m Execution	Viva	Total			
	Marks	Preparation	Performan	nce in the Lab	Calculations an Graphs		ts and Error Analysis	Voce	iotai			
1	Max. Marl	a milet in	(1) Ai	Voltage &	abot 4V o	Vertag	stady.	h4/19	20			
	Obtained	i (ille)		Velis) X	1) 1/	(illas	Dp.	in Copia	19			
	. Time Hearting Peartinal Weithat Bostical Mesition Rockers											
				'A				Signature	of Faculty			
	START W	RĮTING FROM	HERE:	Node	al Analy	sis:-	Ec	, Ú N	4			
	Ai	m:~	. J.	7.000	······	~	7.0	₹ 3				
	TÃ	e studu.	of no	dal and	alusia is 7	the ob	jectinos	d th	ii enem	is.		
	Spe	cipically	its (M	ago in	musti-	SOUNCO	N (ris con it	Ate	~ .		
	ap	The study of nodal analysis is the objectives of this everis- specifically its usage in multi-source oc circuits. Its application in finding circuit node voltage will be investigated										
			٠٠٠ ل			- vou		N MPS	F. C.	ωe		
	#4	paratus:	house	in 08 2	t diagra	indu	the	swaret	i. G			
	S	Equipme	1.5	Range	11 1	is Ty	1	1Qua	ntity			
sist !	2000 July	Resistor	' 1		V1850 -055,	cal	Gen and	L mal	5			
)		steps.	mi -	150 1, 10	eases	4 radually increases						
	2.	Volt met	N		of verte	Rig	ital 🖟	10	, –			
	3	RPS.	11. 1		V V	Dia	ital .	0/u				
	J. 123	Bread board		with Soldined wi		1000	Rigital 11- 11					
	7		1	- ,		-	-	0	1.0			
			Connecting		<u></u>				SONTINA-	J		
		wire	y /			-			require	L		
	,				1/4							



Observation:

	Applied Voltage	Node V	lottage Vi tr)	Node Ve	ltage (V2)	Node Voltage (Vs)		
	V (Volts)	Theoritical	Practical	Theoritical	Practical	Theoritical	bactical	
	6 V	3.7	3.6	2.5	2.4	1.7	1.5	
-	nt b List	udto ido	A THE	in Marines	i inter	indu of Mi II	3000	0.0

Procedure:

- 1. Connect the circuit diagram as shown in figure.
- 2. Switch on the supply to RPS.
- 3. Apply the voltage (say 15 v) and note the volt miter readings
- 4. Gradually increases the supply voltage in steps.
- 5. Note the readings of volt meter.
- 6. Verify the practical results obtained with theoritical results.

Precautions:~

- To check for proper connections before switching on the power supply. → Make sure of proper colour coding of resistors. → The terminal of the resistance should be properly connected. Calculations: Apply KCL at node 'u'. $\underline{T}_1 + \underline{T}_2 + \underline{T}_3 + \underline{T}_4 = 0$ $\frac{V_{x}-6}{47} + \frac{V_{x}-0}{100} + \frac{V_{x}-V_{y}}{220} + \frac{V_{x}-V_{z}}{100} = 0$ $V_{x}\left[\frac{1}{47} + \frac{1}{100} + \frac{1}{220} + \frac{1}{100}\right] - \frac{6}{47} - \frac{V_{y}}{220} - \frac{V_{z}}{200} \ge 0$ $V_x (0.04) - 0.12 - 0.004 V_y - 0.01 V_z = 0$ 0.04 Vu - 0.004 Vy - 0.01 Vz = 0.12 - 0Apply KCL at mode 'y': $\mathcal{T}_3 + \mathcal{T}_5 + \mathcal{T}_6 = 0$ $\frac{V_{y}-V_{x}}{220}+\frac{V_{y}-V_{z}}{152}+\frac{V_{y}-0}{1000}=0.$ $\frac{V_y}{\sqrt{\frac{1}{220} + \frac{1}{150} + \frac{1}{1000}}} - \frac{V_x}{220} - \frac{V_z}{150} = 0$ 0.01 Vy -0.004 Vx -0.006 Vz =0 -0.004 Vx + 0.01 Vy -0.06 Vz = 0 -8 Apply KCL at mode 'z':- $T_5 + T_4 + T_7 = 0$ $\frac{V_z - V_y}{150} + \frac{V_z - V_x}{100} + \frac{U_z - 0}{20} = 0$ $V_z \left[\frac{1}{150} + \frac{1}{100} + \frac{1}{82} \right] - \frac{V_y}{150} - \frac{V_x}{100} = 0$ 0.03 Vz - 0.06 Vy - 0.01 Vx = 0 -0.01 Vx - 0.006 Vy + 0.03 Vz = 0 - 3By solving equations (0, 2 & 3

1 Vx = 3.7V with the death of the Vy = 2.5V/ 1 Vz = 1,7 V) 1100 Hence nodal analysis is verified both theoritically and practically 是一是一是一点。一是一点 0 16 - 50 10 - 6 10 6 My - 1.01 Vz - 5 are the training it show to my plant. 0 - 6 4 5 4 2 5 5 6 6 5

a i someour points