

LABORATORY WORK SHEET

Name of the Student MADK! SAL CHARAN Class CSM-'C' Semester IST	Roll Number							
Course Code AEEDO3 Course Name Electrical and Electronics En Name of the Course Faculty Ms. M. VARA LAKSHMI	2	3	9 5	0.0	A	6 6	F 2	
Name of the Course Faculty M.S. M. VARA LAKSHMI	970	er-	Fa	La.I culty I	007	ARE	1107	2.
Exercise Number 01 Week Number 01		may.	D	ate : L	3	oct	oby o	90

DAY TO DAY EVALUATION:

	TAKE YOU	Aim /	Algorithm / Procedure	Source Code	Program Execution	Viva -	1000
1	Marks	Preparation	Performance in the Lab	Calculations and Graphs	Results and Error Analysis	Voce	Total
	Max. Marks	4	poll 4 diament	4	4	4	20
	Obtained	4	4	4	4	4	20

Signature of Faculty

START WRITING FROM HERE:

OHM'S LAW

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Aim

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: To verity ohm's law for a given resistive

network

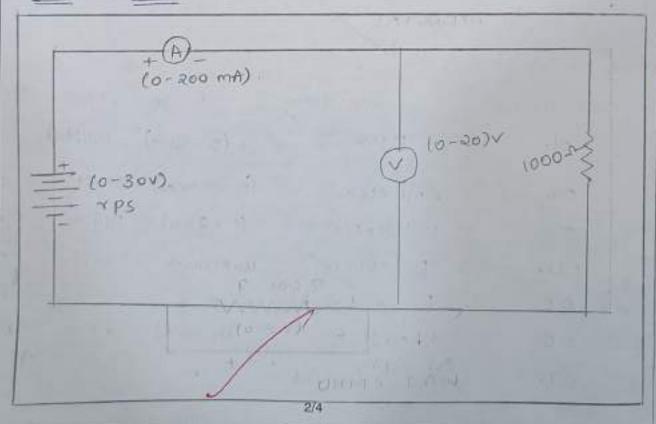
Apparatus :

Serial Number	Apparatus Name	Range	Type	Quantity
ol.	RPS	(0-304)	Digital	01
02.	Ammetex	(6-200mA)	oigital	01
03	Voltmeters	(0-30v)	Digital	01
04.	Resistor	unknown	Carbon	03.
05.	Bread band	-	-	01
0.6	Rheostat	(0-20K)	-	01
·FO	Connecting wires	-		As required

Procedure :

- 10 Make the connections as per circuit diagram.
- O Switch ON the power supply to RPS and apply a voltage (say 10v) and take the reading of voltmeter and ammeter.
- 3) Adjust the theostat in steps and take down the readings of ammeter and voltmeter.
- (4) plot a graph with 'v' along x-axes and 'I' along Y-axis.
- The graph will be a straight line which verifies ohm's law
- @ Determine the slope of the V-I graph, the reciprocal of slope gives resistance of the wire.

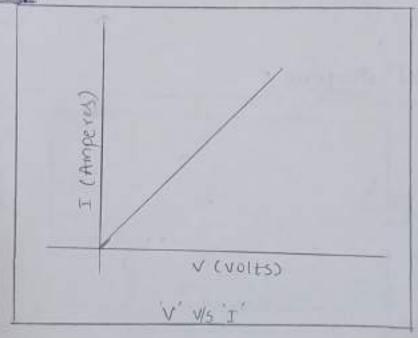
Circuit Diagram:



01	Se	N	va	tio	ms.	- 4
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S.N.0	VSupply	I (Theoritical)	I (Practical)
(1)	2 4	ama	0.3 MA
(2)	AV	AMA	4. 4 ma
(3)	6 /	6-mA	6.5 MA
(4)	8 v	8 ma	8-6 mA
(5)	10	1010A	1027 KNA

Model graph:



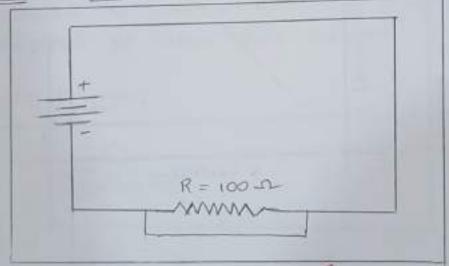
Precautions:

- Take care to connect the ammeter and voltmeter. With their correct polarities.
- @ Make sure of proper color cooling of resistors.
- 1 The terminal of the battery should be properly connected.

Calculations:

Theoretical Diagram:

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Result: Hence, ohm's law is veriffed.

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Class CSM-C Semester I st					Roll Number								
Course Code A t	EE DO3 - Co	urse Name Electrical	and	2	3	9	5	1	A	6	6	F	2
Name of the Course	Faculty M.S. N	ourse Name : Electrical Electron 1. VARALAK St	tcs Er	og i	nee	urii	Fac	La	101	tai A.G	T.	Ži.	72
Exercise Number	01		01				Da	he -	27	0	H	the	1 20

DAY TO DAY EVALUATION:

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Marks Max Marks	Preparation	Performance in the Lab	Calculations and Graphs	Results and Error Analysis	Voce	Total	
Max. Marks	4	4	4	4	4	20	
Obtained							

Signature of Faculty

START WRITING FROM HERE:

Aim: To verify Kirchhoff's voltage law (KVL) and Kirchhoff's current law (KCL) in a passive resistive network.

Statement :

Kirchhoff's voltage law states that "The sum of all voltages (or) potential differences in an electrical circuit loop is Zoro."

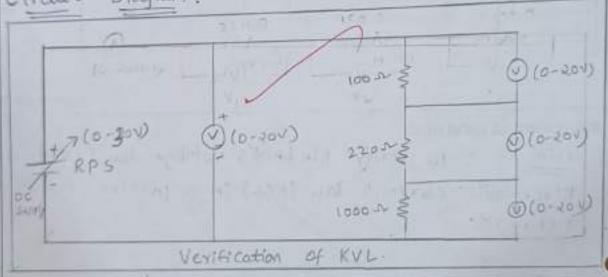
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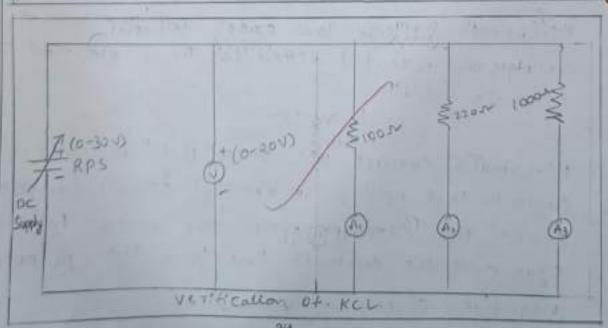
Kirchhoff's current law states that "The sum of all currents that enters an electrical circuit junction is zero." The currents enter the junction have positive sign and the currents that leave the junction have negative sign. $\Sigma_{K} \Sigma_{K} \Sigma_{O}$.

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Apparatus	- 4
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	D. nos	Type	quantity
Apparatus Name		ALL STATE OF THE S	07-
RPS		1	03
Ammeter	-		
voltmeter	(0-30V)	Indiana and a second	03-
Resistors	unknown	Carbon	
	0,5	-	01.
	+		As required
	Ammeter Voltmeter Resistors	RPS (0-30V) Ammeter (0-200mA) Voltmeter (0-30V) Resistors (0-30V) Aread Bookd (Bound)	Apparatus Manneter (0-30V) Digital Ammeter (0-200mA) Digital Voltmeter (0-30V) Digital Resistors (0-30V) Digital Resistors (0-30V) Digital

Diagram: Circuit





Procedure:

To verify KVL:

- O connect the circuit diagram as shown in fig1.
- 2) Switch ON the supply to RPS.
- 3 Apply the voltage (say sv) and note the voltmeter reachings.
- @ Gradually increase the supply of voltage in steps.
- 6) note the readings of voltmeter.
- 6) Sum up the voltmeter readings (voltage drops), '
 that should be equal to applied voltage.
- Thus KUL is verified exactically.

To verify KCL:

- 1 connect the circuit diagram as shown in fig 2.
- @ switch on the supply to RPS.
- (3) Apply the voltage (says v) and note the Ammeter readings.
- @ Gradually increase the supply of voltage in steps.
- 10) Note the readings of anmeter.
- 6 sum up the Ammeter readings (I, and I), that should be equal to total current (I).
- @ Thus KCL is Verified practically.

Applied voltage	V, Cvo	(ts)	V1 (VO	(15)	V3 (V0	V14V14V3		
(volta)	Theoritical	practical	Theoritical	practical	Theoritical	practical		
6 v	0.550/0.500.50	0.47		1+02V	- 61	4-624	5-98v	6-117
	KCL:	·		de en	Prof. Dille	are of the	196)	
Applied voltage	工	(A)	I2 (A	+)	120	43	1,41,	(A)
(volts)	Theoritical	proctical	Theoritical.	practical	Theoritical	Processal		
6v	GOMA	60+9 m.A	27.2 mA	44.52 WH	GMA.		98-2-MA	

Precautions: 1 check for proper connections before switching on the Supply.

- @ Make sure of proper colour cooling of resistors.
- 3) The terminal of the resistance should be properly connected.

= 13202

ROLL NUMBER: 23951A66FL

V2 = IR2 = 0.8045 x 220 = 0-99V.

V3 = IR3 = 0.0045 x1000 = 4.5 V

VI+ VI TUS = 0.45+ 0.99+4.84 = 5.98V

FOT KCL: V= I Reg

Reg = 100 + 220 + 1000 = 15.5 × 10-3 = 15.5 mm

I = VS = 6 = 60 MA

I2 = V5 = 6 = 27.2mm

 $I_3 > \frac{V_3}{E_3} = \frac{6}{1000} = 6 \text{ mA}$

I + I + I = 60 + 27 - 2 + 6 = 93.2 mA.

Result: Hence Kul and Kch are verified.

