

# *Certificate of Completion*

*This is to certify that Saicharan sarla  
successfully completed 7.5 hours of FPGA Design  
and VHDL online course on Jan. 8, 2020*

*Eduvance (Microchip Certified Trainer, AUP Trainer, CUA Trainer)*

Eduvance (Microchip Certified Trainer, AUP Trainer, CUA Trainer), Instructor



Certificate no: UC-4GHZJAKB  
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