Name of the course : Computer Organization & Architecture

Academic Year/ Semester : 2021 – 2022 / II semester

Evaluation component : **Home Assignment** 

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Home Assignment: 1 CO1

- Q1.1 Illustrate the *Direct*, *Register indirect* and *implied addressing* modes with suitable examples.
- Q1.2 Identify which addressing modes do the following instruction belong:
  - 1. MOV AX, [SI]
  - 2. INC [5000H]
  - 3. SUB AX, BX
  - 4. XOR AX, [5000H]
  - 5. XCHG AX, BX
  - 6. PUSH DS
  - 7. OUT 03H, AL
  - 8. INC [BX]
- Q1.3 What is subroutine? How to handle subroutine calls using stack.
- Q1.4 Classify different Instruction Formats (Zero, One, Two and Three Address Instruction) with suitable examples.
- Q1.5 Illustrate the Arithmetic, Logical, Branch and Control instructions with suitable examples.