

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA - 533 003, ANDHRA PRADESH, INDIA

Sl. No. **K 00608520**
PG No. **2023JAN21BT2809**



PROVISIONAL CERTIFICATE

Hall Ticket No. : **198X5A0423**
Institution : **KALLAM HARANADHAREDDY INSTITUTE OF TECHNOLOGY.**
Aadhar No. :

This is to certify that **SAGADABOINA SAI KRISHNA**
son/daughter of Shri. **SAGADABOINA SRINIVASA RAO**
passed **B.TECH (ELECTRONICS & COMMUNICATION ENGINEERING)** degree
examination of this university held in **September 2022** and that
he/she was placed in ******Second Class******

He/She has satisfied all the requirements for the award of the B.Tech
degree of the Jawaharlal Nehru Technological University Kakinada.



21-01-2023

Date :

* Medium of Instructions and Examinations in English

Controller of Examinations

Director of Evaluation

Registrar



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

KAKINADA - 533 003, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT / GRADE SHEET



CMM No. K 00596315

Serial No. 370851

Name: SAGADABOINA SAI KRISHNA

Hall Ticket No. 198X5A0423

Bachelor of Technology in ELECTRONICS & COMMUNICATION ENGINEERING

Name of the College KALLAM HARANADHAREDDY INSTITUTE OF TECHNOLOGY
Name & Year of Final Exam B.Tech September 2022

Year of Admission 2019 - 2020 Class Awarded Second Class

S.No	COURSE TITLE	GRADE	GRADE POINT	CREDITS	S.No	COURSE TITLE	GRADE	GRADE POINT	CREDITS
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I YEAR

DIRECT ADMISSION INTO SECOND YEAR UNDER LATERAL ENTRY SCHEME

II YEAR

1 ELECTRONIC DEVICES AND CIRCUITS	D	5	3	1 ELECTROMAG. WAVES & TRANSMISSION LINES	C	6	3
2 SWITCHING THEORY AND LOGIC DESIGN	C	6	3	2 PULSE AND DIGITAL CIRCUITS	D	5	3
3 SIGNALS AND SYSTEMS	D	5	3	3 MANAGEMENT SCIENCE	D	5	3
4 NETWORK ANALYSIS	D	5	3	4 ELECTRONIC CIRCUIT ANALYSIS	B	7	3
5 RANDOM VARIABLES AND STOCHASTIC PROCESS	C	6	3	5 CONTROL SYSTEMS	C	6	3
6 MANAGERIAL ECO. & FIN. ANALYSIS	C	6	3	6 ANALOG COMMUNICATIONS	C	6	3
7 ELECTRONIC DEVICES AND CIRCUITS LAB	C	6	2	7 ANALOG COMMUNICATIONS LAB	A	8	2
8 NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	9	2	8 ELECTRONIC CIRCUIT ANALYSIS LAB	S	9	2

III YEAR

1 LINEAR I C APPLICATIONS	B	7	3	1 MICRO PROCESSORS & MICRO CONTROLLERS	D	5	3
2 COMPUTER ARCHITECTURE & ORG.	B	7	3	2 DIGITAL SIGNAL PROCESSING	A	8	3
3 DIGITAL I C APPLICATIONS	C	6	3	3 VLSI DESIGN	B	7	3
4 DIGITAL COMMUNICATIONS	D	5	3	4 MICRO WAVE ENGINEERING	C	6	3
5 ANTENNA AND WAVE PROPAGATION	D	5	3	5 BIO-MEDICAL ENGINEERING	C	6	3
6 DIGITAL I C APPLICATIONS LAB	S	9	2	6 MICROPROCESSORS & MICROCONTR. LAB	A	8	2
7 PULSE AND DIGITAL CIRCUITS LAB	O	10	2	7 DIGITAL COMMUNICATIONS LAB	S	9	2
8 LINEAR I C APPLICATIONS LAB	O	10	2	8 VLSI LAB	S	9	2
9 PROF. ETHICS & HUMAN VALUES	CPA	0	0	9 IPR & PATENTS	CPA	0	0

IV YEAR

1 ELECTRONIC SWITCHING SYSTEMS	B	7	3	1 ELECT. MEASUREMENTS & INSTRUM.	C	6	3
2 EMBEDDED SYSTEMS	C	6	3	2 CELLULAR MOBILE COMMUNICATIONS	C	6	3
3 RADAR SYSTEMS	D	5	3	3 SATELLITE COMMUNICATIONS	C	6	3
4 OPTICAL COMMUNICATIONS	D	5	3	4 OPERATING SYSTEMS	C	6	3
5 DIGITAL IMAGE PROCESSING	C	6	3	5 SEMINAR	S	9	2
6 COMPUTER NETWORKS	C	6	3	6 PROJECT	S	9	10
7 MICRO WAVE ENGINEERING & OPTICAL LAB	O	10	2				
8 DIGITAL SIGNAL PROCESSING LAB	S	9	2				

Number of Credits registered for: 132
CGPA Secured: 6.72

Date of Declaration of Result: October 2022
(See overleaf for Instructions)

* CPA - Completed 12/1/2023

* Medium of Instruction and Examinations in English

H. R. K. K. K.
CONTROLLER OF EXAMINATIONS