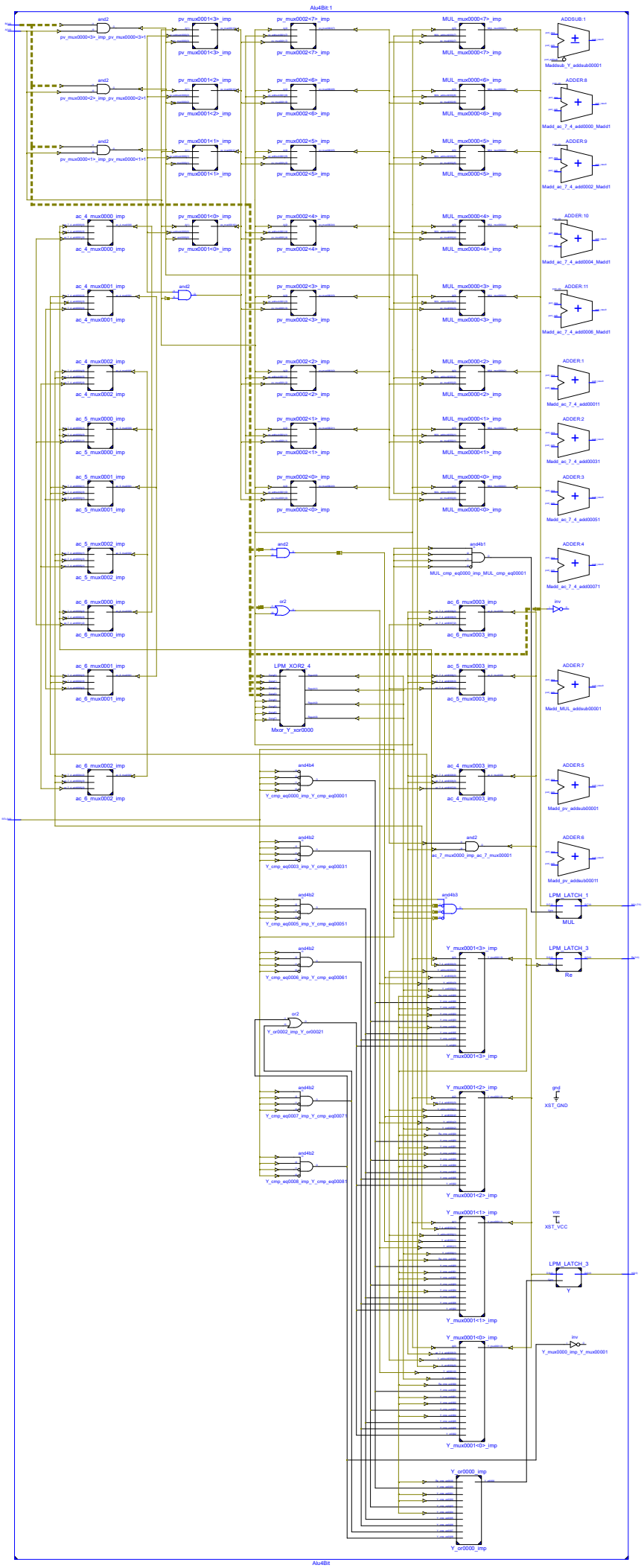
Alu4Bit:1
and2
pv_mux0000<3>_imp_pv_mux0000<3>1
pv_mux0001<3>_imp
pv_mux0002<7>_imp
MUL_mux0000<7>_imp
ADDSUB:1
Maddsub_Y_addsub00001
pv_mux0000<2>_imp_pv_mux0000<2>1
pv_mux0001<2>_imp
pv_mux0002<6>_imp
MUL_mux0000<6>_imp
ADDER:8
Madd_ac_7_4_add0000_Madd1
pv_mux0000<1>_imp_pv_mux0000<1>1
pv_mux0001<1>_imp
pv_mux0002<5>_imp
MUL_mux0000<5>_imp
ADDER:9
Madd_ac_7_4_add0002_Madd1
ac_4_mux0000_imp
pv_mux0001<0>_imp
pv_mux0002<4>_imp
MUL_mux0000<4>_imp
ADDER:10
Madd_ac_7_4_add0004_Madd1
ac_4_mux0001_imp
pv_mux0002<3>_imp
MUL_mux0000<3>_imp
ADDER:11
Madd_ac_7_4_add0006_Madd1
ac_4_mux0002_imp
pv_mux0002<2>_imp
MUL_mux0000<2>_imp
ADDER:1
Madd_ac_7_4_add00011
ac_5_mux0000_imp
pv_mux0002<1>_imp
MUL_mux0000<1>_imp
ADDER:2
Madd_ac_7_4_add00031
ac_5_mux0001_imp
pv_mux0002<0>_imp
MUL_mux0000<0>_imp
ADDER:3
Madd_ac_7_4_add00051
ac_5_mux0002_imp
and4b1
MUL_cmp_eq0000_imp_MUL_cmp_eq00001
ADDER:4
Madd_ac_7_4_add00071
ac_6_mux0000_imp
or2
ac_6_mux0003_imp
inv
LPM_XOR2_4
Mxor_Y_xor0000
ac_5_mux0003_imp
ADDER:7
Madd_MUL_addsub00001
ac_6_mux0001_imp
ac_6_mux0002_imp
and4b4
Y_cmp_eq0000_imp_Y_cmp_eq00001
ac_4_mux0003_imp
ADDER:5
Madd_pv_addsub00001
and4b2
Y_cmp_eq0003_imp_Y_cmp_eq00031
ac_7_mux0000_imp_ac_7_mux00001
ADDER:6
Madd_pv_addsub00011
and4b3
LPM_LATCH_1
MUL
Y_cmp_eq0005_imp_Y_cmp_eq00051
Y_mux0001<3>_imp
LPM_LATCH_3
Re
Y_cmp_eq0006_imp_Y_cmp_eq00061
Y_or0002_imp_Y_or00021
Y_mux0001<2>_imp
gnd
XST_GND
Y_cmp_eq0007_imp_Y_cmp_eq00071
Y_cmp_eq0008_imp_Y_cmp_eq00081
Y_mux0001<1>_imp
vcc
XST_VCC
LPM_LATCH_3
Y
Y_mux0001<0>_imp
inv
Y_mux0000_imp_Y_mux00001
Y_or0000_imp
Alu4Bit