

addiu

Current register/bus values :

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Instruction Count : 7

PC : 0x0040001c

Registers:

R0: 0x00000000  
R1: 0x00000000  
R2: 0x0000000a  
R3: 0x00000000  
R4: 0x00000000  
R5: 0x00000000  
R6: 0x00000000  
R7: 0x00000000  
R8: 0x00000005  
R9: 0x00000131  
R10: 0x000001f4  
R11: 0x00000243  
R12: 0x00000000  
R13: 0x00000000  
R14: 0x00000000  
R15: 0x00000000  
R16: 0x00000000  
R17: 0x00000000  
R18: 0x00000000  
R19: 0x00000000  
R20: 0x00000000  
R21: 0x00000000  
R22: 0x00000000  
R23: 0x00000000  
R24: 0x00000000  
R25: 0x00000000  
R26: 0x00000000  
R27: 0x00000000  
R28: 0x00000000  
R29: 0x00000000  
R30: 0x00000000  
R31: 0x00000000  
HI: 0x00000000  
LO: 0x00000000

Registers		
Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x0000000a
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x00000005
\$t1	9	0x00000131
\$t2	10	0x000001f4
\$t3	11	0x00000243
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000000
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10008000
\$sp	29	0x7fffffc
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x0040001c
hi		0x00000000
lo		0x00000000

arithtest

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Instruction Count : 17
PC                : 0x00400044
Registers:
R0: 0x00000000
R1: 0x00000000
R2: 0x0000000a
R3: 0x00000800
R4: 0x00000c00
R5: 0x000004d2
R6: 0x04d20000
R7: 0x04d2270f
R8: 0x04d2230f
R9: 0x00000400
R10: 0x000004ff
R11: 0x00269000
R12: 0x004d2000
R13: 0x00000000
R14: 0x00000000
R15: 0xffffffffb01
R16: 0x00000000
R17: 0x00640000
R18: 0x00000000
R19: 0x00000000
R20: 0x00000000
R21: 0x00000000
R22: 0x00000000
R23: 0x00000000
R24: 0x00000000
R25: 0x00000000
R26: 0x00000000
R27: 0x00000000
R28: 0x00000000
R29: 0x00000000
R30: 0x00000000
R31: 0x00000000
HI: 0x00000000
LO: 0x00000000
```

Registers		
Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x0000000a
\$v1	3	0x00000800
\$a0	4	0x00000c00
\$a1	5	0x000004d2
\$a2	6	0x04d20000
\$a3	7	0x04d2270f
\$t0	8	0x04d2230f
\$t1	9	0x00000400
\$t2	10	0x000004ff
\$t3	11	0x00269000
\$t4	12	0x004d2000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0xffffffffb01
\$s0	16	0x00000000
\$s1	17	0x00640000
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10008000
\$sp	29	0x7ffefffc
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x00400044
hi		0x00000000
lo		0x00000000

brtest0

```
-----  
Instruction Count : 12  
PC                : 0x0040005c  
Registers:  
R0: 0x00000000  
R1: 0x0000d00d  
R2: 0x0000000a  
R3: 0x00000000  
R4: 0x00000000  
R5: 0x00000001  
R6: 0x00001337  
R7: 0x0000d00d  
R8: 0x00000000  
R9: 0x00000000  
R10: 0x00000000  
R11: 0x00000000  
R12: 0x00000000  
R13: 0x00000000  
R14: 0x00000000  
R15: 0x00000000  
R16: 0x00000000  
R17: 0x00000000  
R18: 0x00000000  
R19: 0x00000000  
R20: 0x00000000  
R21: 0x00000000  
R22: 0x00000000  
R23: 0x00000000  
R24: 0x00000000  
R25: 0x00000000  
R26: 0x00000000  
R27: 0x00000000  
R28: 0x00000000  
R29: 0x00000000  
R30: 0x00000000  
R31: 0x00000000  
HI: 0x00000000  
LO: 0x00000000
```

Registers		
Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x0000d00d
\$v0	2	0x0000000a
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000001
\$a2	6	0x00001337
\$a3	7	0x0000d00d
\$t0	8	0x00000000
\$t1	9	0x00000000
\$t2	10	0x00000000
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000000
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$l0	26	0x00000000
\$l1	27	0x00000000
\$gp	28	0x10008000
\$sp	29	0x7ffefffc
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x0040005c
hi		0x00000000
lo		0x00000000

brtest1

```
-----
Instruction Count : 30
PC                : 0x00400090
Registers:
R0: 0x00000000
R1: 0xbeb0063d
R2: 0x0000000a
R3: 0x00000001
R4: 0xffffffff
R5: 0xbef01a5e
R6: 0x00000000
R7: 0x00000000
R8: 0x00000000
R9: 0x00000000
R10: 0x00000000
R11: 0x00000000
R12: 0x00000000
R13: 0x00000000
R14: 0x00000000
R15: 0x00000000
R16: 0x00000000
R17: 0x00000000
R18: 0x00000000
R19: 0x00000000
R20: 0x00000000
R21: 0x00000000
R22: 0x00000000
R23: 0x00000000
R24: 0x00000000
R25: 0x00000000
R26: 0x00000000
R27: 0x00000000
R28: 0x00000000
R29: 0x00000000
R30: 0x00000000
R31: 0x00400070
HI: 0x00000000
LO: 0x00000000
```

Registers		
Name	Number	Value
\$zero	0	0x00000000
\$at	1	0xbeb0063d
\$v0	2	0x0000000a
\$v1	3	0x00000001
\$a0	4	0xffffffff
\$a1	5	0xbef01a5e
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x00000000
\$t1	9	0x00000000
\$t2	10	0x00000000
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000000
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$l0	26	0x00000000
\$l1	27	0x00000000
\$gp	28	0x10008000
\$sp	29	0x7fffffc
\$fp	30	0x00000000
\$ra	31	0x00400070
pc		0x00400090
hi		0x00000000
lo		0x00000000



## Brtest2

Current register/bus values :

-----  
Instruction Count : 8

PC : 0x0040002c

Registers:

R0: 0x00000000  
R1: 0x0000d00d  
R2: 0x0000000a  
R3: 0x00000000  
R4: 0x00000000  
R5: 0x00000000  
R6: 0x00000000  
R7: 0x0000d00d  
R8: 0x00000000  
R9: 0x00000000  
R10: 0x00000000  
R11: 0x00000000  
R12: 0x00000000  
R13: 0x00000000  
R14: 0x00000000  
R15: 0x00000000  
R16: 0x00000000  
R17: 0x00000000  
R18: 0x00000000  
R19: 0x00000000  
R20: 0x00000000  
R21: 0x00000000  
R22: 0x00000000  
R23: 0x00000000  
R24: 0x00000000  
R25: 0x00000000  
R26: 0x00000000  
R27: 0x00000000  
R28: 0x00000000  
R29: 0x00000000  
R30: 0x00000000  
R31: 0x00000000  
HI: 0x00000000  
LO: 0x00000000

Registers		
Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x0000d00d
\$v0	2	0x0000000a
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x0000d00d
\$t0	8	0x00000000
\$t1	9	0x00000000
\$t2	10	0x00000000
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000000
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10008000
\$sp	29	0x7ffefffc
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x0040002c
hi		0x00000000
lo		0x00000000

## Memtest0

Current register/bus values :

-----  
Instruction Count : 32

PC : 0x00400080

Registers:

R0: 0x00000000  
R1: 0x00000000  
R2: 0x0000000a  
R3: 0x10000004  
R4: 0x00000000  
R5: 0x000000ff  
R6: 0x000001fe  
R7: 0x000003fc  
R8: 0x0000792c  
R9: 0x000000ff  
R10: 0x000001fe  
R11: 0x000003fc  
R12: 0x0000792c  
R13: 0x000000ff  
R14: 0x000000ff  
R15: 0x000001fe  
R16: 0x000003fc  
R17: 0x0000881d  
R18: 0x00000000  
R19: 0x00000000  
R20: 0x00000000  
R21: 0x00000000  
R22: 0x00000000  
R23: 0x00000000  
R24: 0x00000000  
R25: 0x00000000  
R26: 0x00000000  
R27: 0x00000000  
R28: 0x00000000  
R29: 0x00000000  
R30: 0x00000000  
R31: 0x00000000  
HI: 0x00000000  
LO: 0x00000000

Registers		
Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x0000000a
\$v1	3	0x10000004
\$a0	4	0x00000000
\$a1	5	0x000000ff
\$a2	6	0x000001fe
\$a3	7	0x000003fc
\$t0	8	0x0000792c
\$t1	9	0x000000ff
\$t2	10	0x000001fe
\$t3	11	0x000003fc
\$t4	12	0x0000792c
\$t5	13	0x000000ff
\$t6	14	0x000000ff
\$t7	15	0x000001fe
\$s0	16	0x000003fc
\$s1	17	0x0000881d
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$l0	26	0x00000000
\$l1	27	0x00000000
\$gp	28	0x10008000
\$sp	29	0x7fffffc
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x00400080
hi		0x00000000
lo		0x00000000

# Memtest1

Current register/bus values :

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Instruction Count : 40

PC : 0x004000a0

Registers:

R0: 0x00000000  
 R1: 0x0000efbe  
 R2: 0x0000000a  
 R3: 0x10000004  
 R4: 0x00000000  
 R5: 0x0000cafe  
 R6: 0x0000feca  
 R7: 0x0000beef  
 R8: 0x0000efbe  
 R9: 0x000000fe  
 R10: 0x000000ca  
 R11: 0xffffffffef  
 R12: 0xffffffffbe  
 R13: 0x0000cafe  
 R14: 0x0000feca  
 R15: 0xffffffffef  
 R16: 0xffffffffbe  
 R17: 0x0001caea  
 R18: 0x00000000  
 R19: 0x00000000  
 R20: 0x00000000  
 R21: 0x00000000  
 R22: 0x00000000  
 R23: 0x00000000  
 R24: 0x00000000  
 R25: 0x00000000  
 R26: 0x00000000  
 R27: 0x00000000  
 R28: 0x00000000  
 R29: 0x00000000  
 R30: 0x00000000  
 R31: 0x00000000  
 HI: 0x00000000  
 LO: 0x00000000

Registers		
Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x0000efbe
\$v0	2	0x0000000a
\$v1	3	0x10000004
\$a0	4	0x00000000
\$a1	5	0x0000cafe
\$a2	6	0x0000feca
\$a3	7	0x0000beef
\$t0	8	0x0000efbe
\$t1	9	0x000000fe
\$t2	10	0x000000ca
\$t3	11	0xffffffffef
\$t4	12	0xffffffffbe
\$t5	13	0x0000cafe
\$t6	14	0x0000feca
\$t7	15	0xffffbeef
\$s0	16	0xffffefbe
\$s1	17	0x000179ea
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$l0	26	0x00000000
\$l1	27	0x00000000
\$gp	28	0x10008000
\$sp	29	0x7ffefffc
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x004000a0
hi		0x00000000
lo		0x00000000