

EEE 313 Spring 2012

Bilkent University

Department of Electrical and Electronics Engineering EEE 313 Electronic Circuit Design Final Exam 25 May 2012, 15.30 (5 questions, 150 minutes)

- This is a **closed book**, closed notes exam. No cheat sheet allowed.
- All cell-phones should be completely turned off.
- Use a calculator for numerical computations. Carry at least 3 significant digits. Double check your numerical calculations.
- Be sure to write the **units** of all numerical results.
- Show all work clearly. Show all the intermediate steps including formulas. Never, just carry out calculations without formulas and explanation.
- Please put your **final answer** for each part inside a box for easy identification. Do not give multiple answers, they will not be graded.
- Do not remove the staple from the exam sheets or separate pages of the exam. All extra pages must be stamped to your exam.
- You may leave the exam room when you are done. However, please do not leave during the last five minutes of the exam.
- At the end of the exam, please stay seated unitl all exam papers are collected.

Useful constants and formulas:

Boltzmann's constant: 86×10⁻⁶ eV/K° Electron charge: 1.6×10⁻¹⁹ Coulombs

Drain currrent equation for n-channel MOSFET:

$$\begin{split} I_D &= K_n (V_{GS} - V_{TN})^2 & \text{for} & V_{GS} - V_{TN} \leq V_{DS} \\ I_D &= K_n (2(V_{GS} - V_{TN}) V_{DS} - {V_{DS}}^2) & \text{for} & V_{GS} - V_{TN} \geq V_{DS} \end{split}$$

Drain currrent equation for p-channel MOSFET:

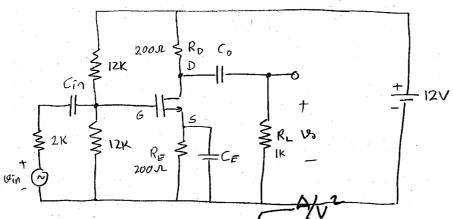
$$\begin{split} I_D &= K_p (V_{SG} + V_{TP})^2 \quad \text{for} \quad V_{SG} + V_{TP} \leq V_{SD} \\ I_D &= K_p (2(V_{SG} + V_{TP})V_{SD} - {V_{SD}}^2) \quad \text{for} \quad V_{SD} + V_{TP} \geq V_{SD} \end{split}$$

Forward active current of BJT is given by:
$$I_C = I_S \left(e^{\frac{V_{BE}}{nV_t}} - 1 \right)$$

Other equations must be deduced by the students.

Please do not write below this line

1. 30 pts.	
2. 30 pts.	
3. 20 pts.	
4. 20 pts.	
Total 100 pts.	



1. **Question:** (30 Pts.) At the circuit given above, the transistor is an n-channel MOSFET with $K_n=2.5\times10^{-3}$ mhes and $V_{TN}=2$ Volts. Answer the following:

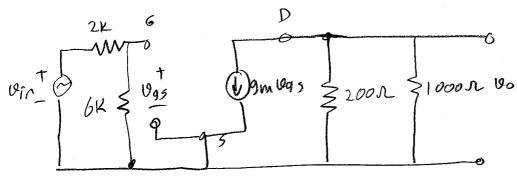
a. 9(6Pts.) Find the DC bias of the transistor.

b.9 (7Pts.) Assuming that the capacitors C_{in} , C_E and C_o are very large and the drain current is equal to 10mA, find the voltage gain of the amplifier defined as v_o/v_{in} .

c. (7 Pts.) Find the output peak to-peak undistorted voltage swing again-assuming that the drain current is equal to 10mA.

d. 12(10 Pts.) The amplifier is to be used to amplify music signals. The BW of the music signal is assumed to between 20Hz and 20kHz. Find limiting values for the capacitors C_{in}, C_E and C_o seperately for each them in order to put the 3dB-cutoff frequencies to the appropriate frequency assuming that the others are very large each time and the drain current is equal to 10mA.

Solution: a-) $V_6 = bV$, assuming sat, $bV = V_{65} + 200I_D = V_{65} + 200 \times 25\pi i \sqrt{65 - 4V_{65}}$ $bV = V_{65} + 200 \times 2.5\pi i \sqrt{65} \left[V_{65} - 2\right]^2 = 0.5 \left[V_{65} - 4V_{65} + 4\right] + V_{65}$ $bV = V_{65} + 0.5 \left[V_{65}^2 - 4V_{65} + 4\right] \Rightarrow b = V_{65} + 0.5 V_{65}^2 - 2V_{65} + 2$ $+0.5 V_{65}^2 - V_{65} - 4 \Rightarrow V_{65}^2 - 2V_{65} - 8 = 0$ $V_{65} = -\frac{b}{2a} \pm \frac{b^2 - 4ac}{2a} = \frac{2}{2} \pm \frac{4 + 32}{2} = 1 \pm 3V$ $V_{65} = -2$ is not feasible as it makes drain current zero. $V_{65} = 4V$ $I_{00} = V_{00} + V_{00} + V_{00} = V_{00} + V_{00}$ b-) First draw the AC equivalent crownit



$$9_{\rm m} = 2 \left(V_{65} - V_{7N} \right) K_{\rm n} = 2 \times 2.5 \times 10^{-3} \left[4 - 2 \right] = 5 \times 10 \times 2 = 10 \text{ m/hos}$$

$$A_V = \frac{v_o}{v_{in}} = 1.25$$

c-) for Cin 8

$$2k$$
 Cin

 $2k$ Cin

 $3k$ Cin

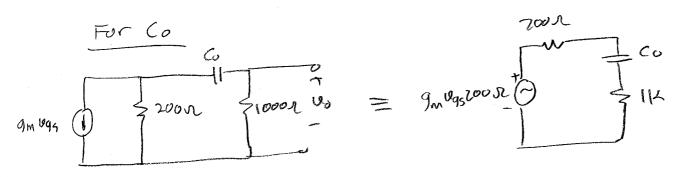
$$W_0 = \frac{1}{8000C} \Rightarrow C = \frac{1}{W_0 C}$$

$$W_0 = \frac{2\pi \times 2017}{a6 + e + ormula is for}$$

$$W_0 = \frac{1}{8000C} \Rightarrow C = \frac{1}{W_0 C}$$

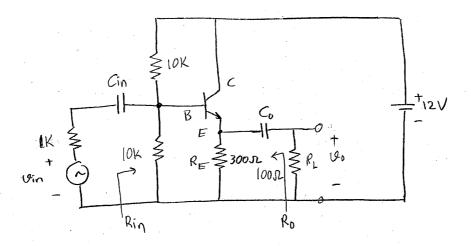
$$W_0 = \frac{1}{8000C} \times 2017$$

$$C_{10} = \frac{1}{2\pi * 20 * 8000} = 9.95 \times 10 P = 10 \times 10 F = 1 \mu F$$



$$\frac{U_{99}}{U_{10}^{2}} = 75 \frac{1+200 \text{ Jw CE}}{300+20000 \text{ Jw CE}} = \frac{75}{300} \frac{1+200 \text{ Jw CE}}{1+\frac{20099}{399} \text{ Jw CE}}$$

The lower (utoff frequency up is given by
$$W_0 = \frac{1}{66.6CE} \Rightarrow C_E = \frac{1}{2\pi \times 20 \times 66.6} = 1.194 \times 10^4 F$$



- 2. **Question:** (30 Pts.) For the circuit given above β =120, V_{CESAT}=0.4Volts and V_{BESAT}=0.7Volts. Answer the following:
 - a. (6 Pts.) Find the DC bias of the transistor.
 - b. (7 Pts.) Find the voltage gain of the amplifier defined as v_o/v_{in} assuming that the Capacitors C_{in} and C_o are very large and the collector current is 15mA.
 - c. (10 Pts.) Find the input and output resistances as shown at the figures assuming that the collector current is 15mA.
 - d. (7 Pts.) Find the output peak-to-peak undistorted voltage swing assuming that the collector current is 15mA.

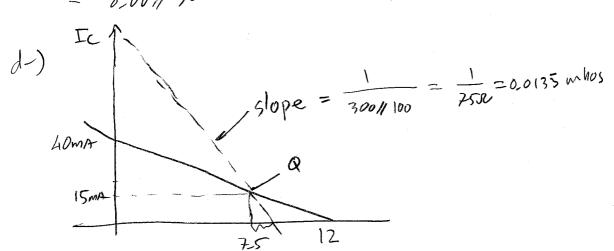
$$\frac{U_0}{U_0} = \frac{(300/100)}{(300/100) + \frac{1}{gm}} \qquad \frac{1}{gm} = \frac{26.6mV}{15mA} = 1.773 L$$

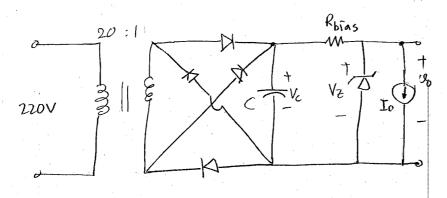
$$\frac{U_0}{U_0} = \frac{75}{75 + 1.773} = 0.56425$$

$$R_0 = \frac{\left(\frac{R_3}{18}, \frac{1}{182}\right)}{\beta + 1} + \frac{1}{9m} \frac{1}{18} R_E = \frac{1000/15000}{121} + 1.773 \frac{1}{300}$$

$$= \left(\frac{833}{121} + 1.773\right) \frac{1}{300} = \left(\frac{6.887 + 1.773}{1200}\right) \frac{1}{300}$$

$$= \frac{8.661/300}{1200} = \frac{8.47}{1200} \approx \frac{8.47}{1200}$$





3. Question: (20 Pts.) At the circuit given above, $V_{\gamma}=1$ Volt, $V_{z}=7$ Volts, $I_{zmin}=20$ mA, $I_{o}=200$ mA, the transformer input is $220V_{rms}$, the transformer is ideal and r_{o} (the output resistance) of the zener diode is equal to 0. Answer the following:

a. (10 Pts.) Find the minimum value of R_{bias} which will work with $2V_{p-p}$ ripple voltage, $V_{(+)}$

b. (10 Pts.) Find the approximate value of the capacitance of the capacitor C which will limit the ripple voltage to $2V_{p-p}$ if the value of R_{bjas} were 15Ω .

Solution

Solution

Vereal = $\frac{220 \times \sqrt{2}}{20}$ - 2 Vy = 13-51V

$$\frac{R_{biasmin}}{\Gamma_{R_{biasmax}}} = \frac{V_{R_{biasmin}}}{220nA} = \frac{4.51V}{220nA} = 20.552$$

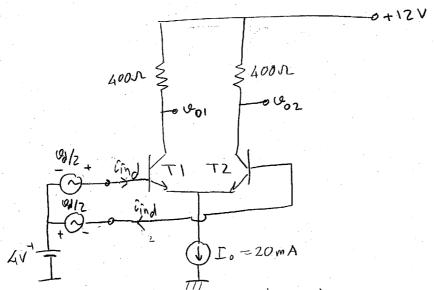
$$F_{av} = \frac{I_{max} + I_{min}}{2} = \frac{13.51 - 7V}{150} + \frac{11.51 - 7}{150} \frac{1}{2}$$

$$= \frac{6.51}{15} + \frac{4.51}{15} \frac{1}{2} = \frac{1102}{2 \times 15} = 0.3673 A$$

$$IA + = CAV \Rightarrow 0.3673 \times 10 = C \times 2V$$

$$C = \frac{0.3673 \times 10^{2}}{2} = 0.001836 F$$

$$C = 1836 \mu F$$



Question: (20 Pts.) For the circuit given above β =100, V_{CESAT} =0.4Volts and V_{BESAT}=0.7Volts. answer the following:

a. (6 Pts.) Find the states of the transistors and transistor currents assuming $v_d = 0$,

b. (7 Pts.) Find the small signal voltage gain defined as $(v_{o1}-v_{o2})/v_{dy}$

c. (7 Pts.) Find the differential input resistance defined as $R_{ind} = v_d / i_{ind}$.

v₀₁, v₀₂, v_d are AC voltages, v_d is the differential input voltage, R_{ind} is the differential input resistance, i_{ind}is the differential input current.

Solution:

$$o-)$$
 $I_{C_1} = I_{C_2} = \frac{20}{2} = 10 \text{ mA}$
 $V_{02} = V_{01} = 12 \text{ V} - 10 \text{ mA} + 400 \Omega = 12 - 4 = 8 \text{ V}$
 $V_{B_1} = V_{B2} = 4 \text{ V} \rightarrow V_{CB} = 8 - 4 = 4 \text{ V} \rightarrow F - A$.
 $b-)$ $V_{01} = -\frac{0d}{2}g_{\text{m}} 400$ $g_{\text{m}} = \frac{10 \text{ mA}}{266 \text{ mV}} = 2376 \text{ m/hos}$
 $V_{02} = -\left(-\frac{0d}{2}g_{\text{m}} 400\right)$
 $V_{03} = \frac{0}{2}g_{\text{m}} 400$ $g_{\text{m}} = \frac{1000}{2}g_{\text{m}} 400 = 000$
 $V_{03} = \frac{0}{2}g_{\text{m}} 400 = 000$