Name:	SOLUTIONS	Section:	
Signature:		EEE 313 Spring 20)10

Bilkent University Department of Electrical and Electronics Engineering EEE 313 Electronic Circuit Design Final Exam

23 May 2010, 09:00 (5 questions, 150 minutes)

- This is a closed book, closed notes exam. No cheat sheet allowed.
- All cell-phones should be completely turned off.
- Use a calculator for numerical computations. Carry at least 4 significant digits during calculations. Your final answer should be at least 3 significant digits.
- Be sure to write the units of all numerical results.
- Show all work clearly.
- Please put your final answer for each part inside a box for easy identification.
 Do not give multiple answers, they will not be graded.
- Do not remove the **staple** from the exam sheets or separate pages of the exam. All extra pages must be stamped to your exam.
- You may leave the exam room when you are done. However, please do not leave during the last five minutes of the exam.
- At the end of the exam, please stay seated unitl all exam papers are collected.

FET equations:

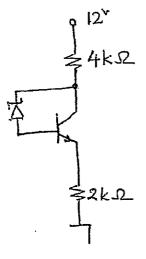
n-channel MOSFET

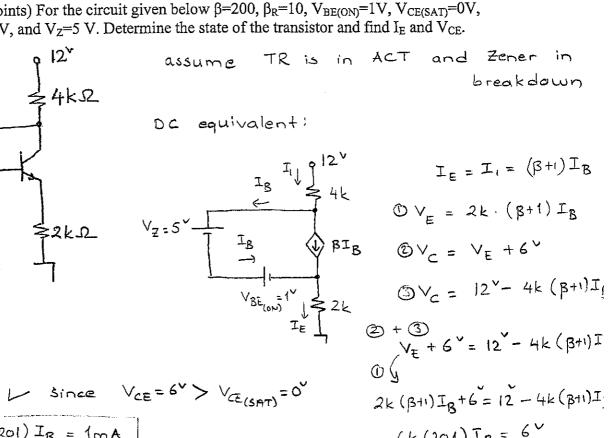
$$\begin{split} i_D &= K_n (v_{GS} - V_{Tn})^2 & \text{SAT} \\ i_D &= K_n \Big[2 (v_{GS} - V_{Tn}) v_{DS} - v_{DS}^2 \Big] & \text{NON-SAT} \\ \textbf{p-channel MOSFET} \\ i_D &= K_p (v_{SG} + V_{Tp})^2 & \text{SAT} \\ i_D &= K_p \Big[2 (v_{SG} + V_{Tp}) v_{SD} - v_{SD}^2 \Big] & \text{NON-SAT} \\ \textbf{n-channel JFET} \\ i_D &= \frac{I_{DSS}}{V_p^2} (v_{GS} - V_P)^2 & \text{SAT} \\ i_D &= \frac{I_{DSS}}{V_p^2} \Big[2 (v_{GS} - V_P) v_{DS} - v_{DS}^2 \Big] & \text{NON-SAT} \end{split}$$

Please do not write below this line

1. 10 pts.		
2. 30 pts.		
3. 20 pts.		
4. 15 pts.		
5. 25 pts.		
Total 100 pts.		

1. (10 points) For the circuit given below $\beta=200$, $\beta_R=10$, $V_{BE(ON)}=1V$, $V_{CE(SAT)}=0V$, $V_{\gamma}=0.8$ V, and $V_{z}=5$ V. Determine the state of the transistor and find I_{E} and V_{CE} .





$$I_{E} = I_{I} = (\beta+i)I_{B}$$

$$O V_{E} = 2k \cdot (\beta+1)I_{B}$$

$$O V_{C} = V_{E} + 6^{\vee}$$

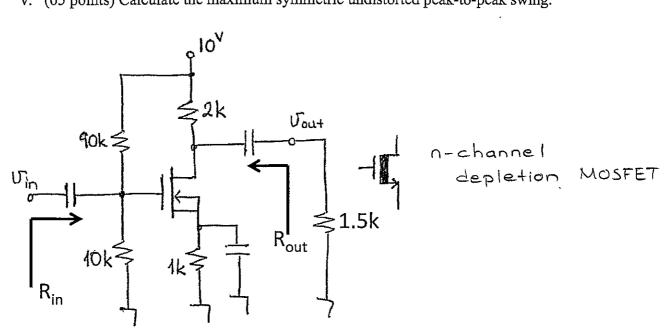
$$O V_{C} = 12^{\vee} - 4k (\beta+i)I_{B}$$

$$V_{CE} = 6^{\circ} | V_{Since} | V_{CE(SAT)} = 0^{\circ}$$

$$| I_{E} = (201) I_{B} = 1 \text{ mA} |$$

$$V_{\xi} + 6^{\circ} = 12 - 4k (\beta + 1)^{2}$$
 $0 \int \int 2k (\beta + 1) I_{B} + 6 = 12 - 4k (\beta + 1) I_{B}$
 $6k (201) I_{B} = 6^{\circ}$
 $I_{B} = 4.975 \mu A$

- 2. (30 points) For the circuit given below V_{TN} =-2V, K_N =1mA/V²
 - i. (05 points) Find the DC drain current.
 - ii. (05 points) Draw the small signal equivalent circuit.
 - iii. (05 points) Determine the voltage gain, v_o/v_{in} .
 - iv. (10 points) Find the input and output impedance.
 - v. (05 points) Calculate the maximum symmetric undistorted peak-to-peak swing.



i)
$$V_G = 1^{\vee}$$
 $V_S = 1 \text{k} \cdot I_D$
assume sAT =) ① $I_D = K_D \left(V_{GS} - V_{Th}\right)^2$ (Transistor relation)
② $10^{\vee} = 3 \text{k} I_D + v_{DS}$ (Load line, DC)

$$I_{D} = 1 \text{ mA} / 2 \left[\left(1 - 1 \text{ k} I_{D} \right) - \left(-2 \right) \right]^{2}$$

$$I_{D} = \left(1 - I_{D} + 2 \right)^{2} = I_{D} = \left(3 - I_{D} \right)^{2}$$

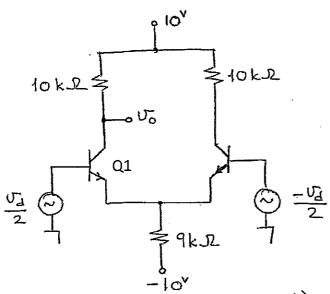
$$I_{D}^{2} - 7 I_{D} + 9 = 0$$

$$I_{D} = \begin{cases} 5.3 \text{ mA} \\ 1.697 \text{ mA} \end{cases} \Rightarrow V_{DS} = \begin{cases} -5.9 \\ 4.91 \end{cases}$$

$$V_{DS} = 4.91^{\circ} > V_{GS} - V_{Th} = (1 - 1.697) + 2$$

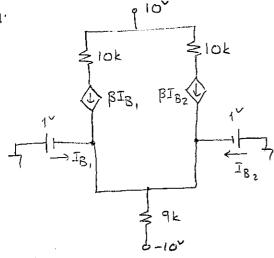
$$V_{DS} = 4.91^{\circ} > 1.303^{\circ}$$

- 3. (20 points) For the circuit given below, the transistors are identical and β =99, $V_{BE(ON)}=1V$, $V_{CESAT}=0V$, $r_o=\infty$.
 - i. (05 points) Determine the DC base current of Q1
 - ii. (05 points) Draw the ac equivalent circuit
 - iii. (10 points) Find the single-sided voltage gain, $\frac{v_o}{v_d}$.



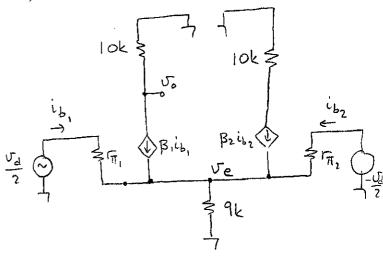
a) TR are in F.A.

Dc eq.



since identical $I_{B_1} = I_{B_2} = I_B$ $I_{E_1} = I_{E_2} = I_E$ $2I_E = \frac{(0-1^{\vee}) - (-10^{\vee})}{9k}$

b) ac eq.

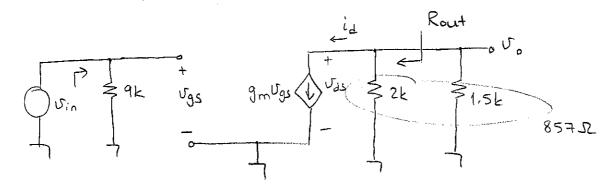


c) $V_0 = -\beta i_{b_1} \cdot 10k = -\beta i_{b_1} \cdot 10k$ emitter node: $(\beta+i) i_{b_1} + (\beta+i) i_{b_2} = \frac{V_e}{9k}$ $\frac{V_d}{2} - \Gamma_{\pi} i_{b_1} = V_e = -\frac{V_d}{2} - \Gamma_{\pi} i_{b_2}$ $\frac{V_d}{2} - \Gamma_{\pi} i_{b_1} = V_e$ $\frac{V_d}{2} + \Gamma_{\pi} i_{b_2} = V_e$ $\frac{V_d}{2} + \Gamma_{\pi} i_{b_2} = V_e$ $\frac{V_d}{2} + \Gamma_{\pi} i_{b_2} = V_e$

$$\frac{\sigma_d}{2} = \pi ib$$

$$\frac{\sigma_0}{2} = \frac{-\beta \cdot 60k}{2} = -95.192$$

$$g_{m} = 2\sqrt{K_{n}I_{D}} = 2\sqrt{I_{mA} \cdot 1.697mA} = 2.606 \text{ mA/V}$$



(iii)
$$V_0 = -g_m v_{gs} (2k//1.5k)$$
 $\int_{V_1}^{\infty} v_{gs} = -g_m \cdot 857 R = [-2.23 = A_0]$

ac load line eq.

in = a vos + b

$$V_{ds} = -i_{d}(2k//1.5k)$$
=> $R_{ac} = 2k//1.5k$

$$Slope = -\frac{1}{Rac}$$

slope of ac load line

$$seg1 = 4.91^{V} - 2.204^{V}$$

1 seg1 = 2.7

$$a = -\frac{1}{Rac}$$
 =) $i_D = -\frac{1}{857} U_{DS} + b$

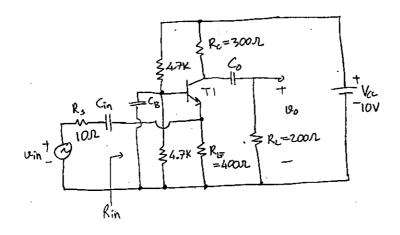
$$i_D = \frac{-V_{DS}}{0.857k} + 7.43 \text{ mA} \quad \text{ac load line}$$

$$V_{DS}^{2} + \frac{V_{DS}}{0.857} - 7.43 = C$$

Swing Vpp=2.91

$$i_D = K_D V_{DS}^2 = I_M A_{/2} V_{DS}^2$$
 parabolic $V_{DS} = \begin{cases} -3.57 \\ 2.204 \end{cases}$

4. (15 points)



At the circuit given above, $\beta=100$, $V_{CESAT}=0.5$ Volt, $V_{BEON}=0.7$ Volt., $R_E=400\Omega$, Rc=300 Ω and R_S=10 Ω . Assume that the transistor is in active mode and Ic=10 mA. Please answer the following:

- a) (5 points) Assuming that all the capacitors are very large, find the input resistance
- b) (5 points) Again assuming that all the capacitors are very large, find the midband gain.
- c) (5 points) Assuming that C_B and C_o are very large, finde the value of C_{in} in order to make the 3-dB lower cut-off frequency equal to 300 Hz.

Assuming that C_B and C_{in} are very large, find the value of C_o in order to make the 3dB lower cut-off frequency equal to 300 Hz.

Solutions & First drow the AC equivalent circuit $g_{\rm m} = \frac{10 \text{mA}}{26 \text{mV}} = 0.3858 \frac{1}{9 \text{m}} = \frac{26 \text{mV}}{10 \text{mA}} = 2.6 \text{ Co}$ The i/p resistance seen at the emitter with base grounded is B = 1 = 2.62 => Rin = 2.62/14002 (CBTS AC)

= 2-6×400 = 2-5832

$$\frac{4-6}{0be} = -uin \frac{2.583}{10.2583} = \frac{2.583}{12.583} uin$$

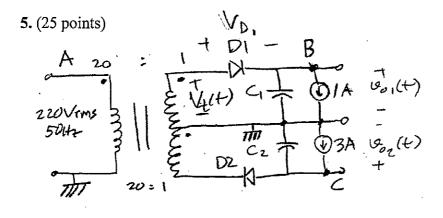
$$\frac{4-6}{0be} = -uin \frac{2.583}{10.2583} = \frac{2.583}{12.583} = \frac{120}{2583}$$

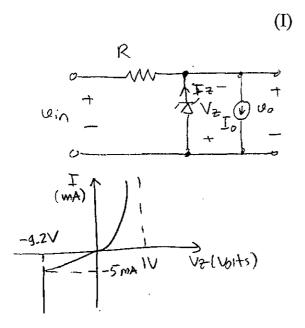
$$\frac{4-6}{000} = \frac{2.583}{12.583} \cdot \frac{120}{2.583} = \frac{120}{12.583} = \frac{120}{2.583}$$

$$\frac{4-6}{12.583} = \frac{120}{12.583} = \frac{120}{12.583} = \frac{120}{2.583}$$

$$\frac{4-6}{12.583} = \frac{120}{12.583} = \frac{120}{12.583} = \frac{120}{2.583}$$

$$\frac{4-6}{12.583} = \frac{120}{12.583} = \frac{120}{12.583} = \frac{120}{2.583} = \frac{120}{2.5$$





At the circuit given at (I), $C1=10000\mu\text{F}$, $V2=15000\mu\text{F}$, $V\gamma=1$ Volts. Each of the secondary winding of the transformer has the transformer ratio of 20:1. Please answer the following:

(II)

- a) (5 points) Find the ripple voltage on $V_{\rm O1}(t)$ and $V_{\rm O2}(t)$.
- b) (5 points) Find the peak reverste voltages on diodes D1 and D2.
- c) (5 points) Plot $V_{O1}(t)$ and $V'_{O2}(t)$ on the same graph showing the voltages and timing properly.
- d) (5 points) Would you get electric shock if you touch the points marked A,B and C. Please answer and explain each one seperately.
- e) (5 points) At the circuit given at (II), a regulator circuit and the V-I characteristics of the zener diode used at the regulator is given. The ranges of v_{in} and Io are given as follows; 12V<v_{in}<20V and 10mA<Io<30mA. Find the value of R which preserves regulation and which minimizes the zener diode power dissipation at the same time.

Solution of Q5

$$A-) \quad LOT = CAV \qquad AV = Vripple, DT = \frac{1}{50Hz} = 20m sec$$

$$Vripple = \frac{LOT}{C} \qquad Opoints for$$

$$A+ u_0 \qquad Vr = \frac{1}{15^2} = 2V \qquad fall wave calculation$$

$$A+ u_0 \qquad Vr = \frac{3A \times 20 \times 10^3}{1.5 \times 10^2} = 4V$$

b-) Peak voltage
$$V_{p}$$
 on the capacitors are
$$V_{p} = \frac{220V_{rms} \times \sqrt{2}}{n} - V_{8} = 14.51 \approx 14.5V$$

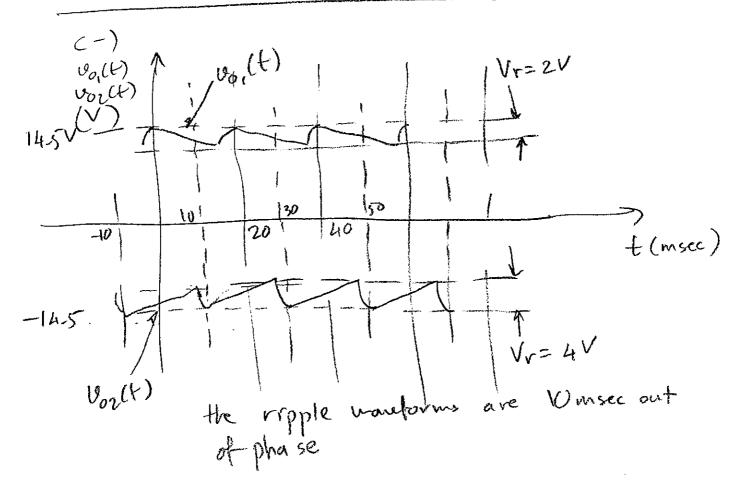
$$V_{D}(t) = (15.5\cos w o t - 14.5)V$$

negative maximum of $V_{D}(t)$ (peak versus voltage)

Requal to

 $-15.5 - 14.5 = -30V$

Solution of Q-5 (Continued)



d) At A, you get electric shock because you touch live phase 220V xVZ peak voltage which induces current in you even if you are not grounded. Iturian body has approximately 100pF capacitance to ground. The drawng is as follows

QJ continued 1 -) cont But it you are grounded you can be exposed to a large current which can kill you. at B&C you are exposed to a small DC vollage which will neither horm you now give you an electoric shock because the transformer isolates you from the mains supply: e-) The minimum input voltage and the maximum o/p current are critical for trinding the value of R -) R = Vinmin -9-2V = 12V-9.2V 30MA+5MA Imax + Izmin because the reso for $R = \frac{2.8V}{35mA} = 80\Omega$ mustalso supply the knoc turvent at least

i