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Signature: _____

EEE 313 Spring 2012

Bilkent University
Department of Electrical and Electronics Engineering
EEE 313 Electronic Circuit Design
Midterm Examination #1
14 March 2012, 17.40
(4 questions, 120 minutes)

- This is a **closed book**, closed notes exam. No cheat sheet allowed.
- All cell-phones should be completely **turned off**.
- Use a calculator for numerical computations. Carry at least **3 significant digits**. Double check your numerical calculations.
- Be sure to write the **units** of all numerical results.
- **Show** all work clearly.
- Please put your **final answer** for each part inside a box for easy identification. Do not give multiple answers, they will not be graded.
- Do not remove the **staple** from the exam sheets or separate pages of the exam. All extra pages must be stamped to your exam.
- You may leave the exam room when you are done. However, please do not leave during the **last five minutes** of the exam.
- At the end of the exam, please stay seated until **all** exam papers are collected.

Useful constants and formulas:

Boltzmann's constant: $86 \times 10^{-6} \text{ eV/K}^\circ$

Electron charge: $1.6 \times 10^{-19} \text{ Coulombs}$

Drain current equation for n-channel MOSFET:

$$I_D = K_n (V_{GS} - V_{TN})^2 \quad \text{for} \quad V_{GS} - V_{TN} \leq V_{DS}$$
$$I_D = K_n (2(V_{GS} - V_{TN})V_{DS} - V_{DS}^2) \quad \text{for} \quad V_{GS} - V_{TN} \geq V_{DS}$$

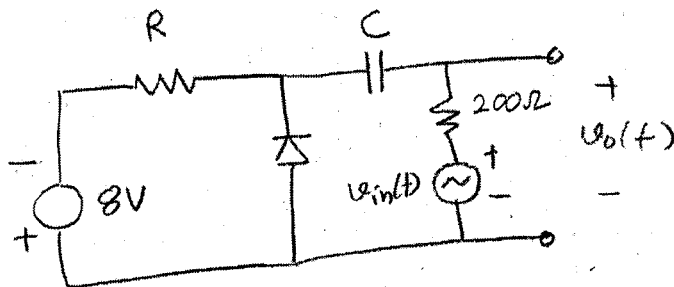
Drain current equation for p-channel MOSFET:

$$I_D = K_p (V_{SG} + V_{TP})^2 \quad \text{for} \quad V_{SG} + V_{TP} \leq V_{SD}$$
$$I_D = K_p (2(V_{SG} + V_{TP})V_{SD} - V_{SD}^2) \quad \text{for} \quad V_{SG} + V_{TP} \geq V_{SD}$$

Other equations must be deduced by the students.

Please do not write below this line

1. 24 pts.	
2. 20 pts.	
3. 20 pts.	
4. 36 pts.	
Total 100 pts.	



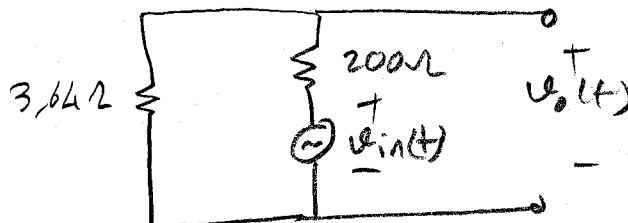
1. (24 points) At the circuit given above, the diode is ideal except $V_T = 0.7\text{V}$. The value of the resistor R is $1\text{k}\Omega$. $V_{in}(t)$ is equal to $0.5\cos(1000t)$. Answer the following:

- (6 points) Find the DC current flowing through the diode,
- (6 points) Draw the AC equivalent circuit assuming C is very large,
- (6 points) Calculate and plot $v_o(t)$
- (6 points) Write a condition on the value of C in order to be assume that it is very large.

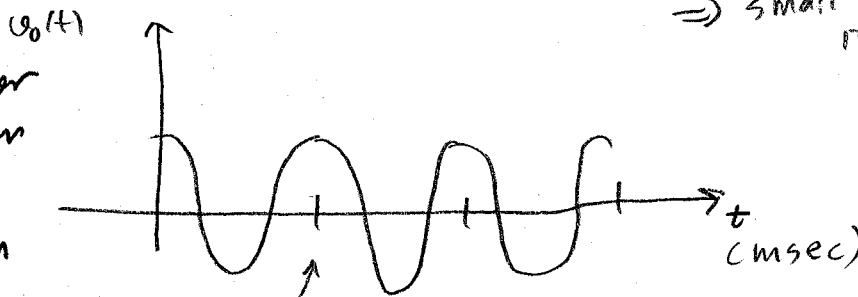
a-) $I_d = \frac{8V - 0.7V}{1000\Omega} = 7.3 \times 10^{-3} \text{ A}$

b-) $r_d = \frac{V_T}{I_d} = \frac{26.6\text{mV}}{7.3\text{mA}} = 3.64\Omega$ assuming small signal appr.

(3) for circuit
(3) for r_d



c-) $v_o(t) = 0.5\cos(1000t) \cdot \frac{3.64 \times 0.5}{200 + 3.64} = 8.94 \times 10^{-3} \text{ V} \ll V_T$
 \Rightarrow small signal approximation is correct



(3) points for calculation
(3) points for graph

$$\frac{2\pi}{1000} \text{ sec} = 159.1 \text{ msec}$$

as $\frac{2\pi}{f} = 1000 \Rightarrow T = \frac{2\pi}{1000} \text{ sec}$

d-) continued

The detailed solution is

$$\vec{V}_o = \vec{V}_in \frac{r_d // R}{r_d // R + 200 + \frac{1}{j\omega C}} \Rightarrow$$

$\left| \frac{1}{j\omega C} \right|$ must small enough in order not to affect the ratio

$$\frac{r_d // R}{r_d // R + 200 + \frac{1}{j\omega C}} \Rightarrow$$

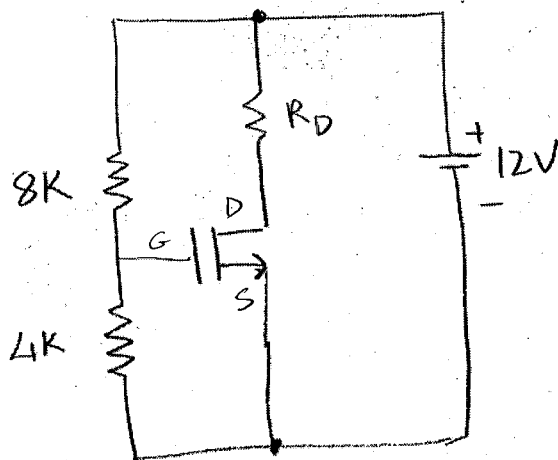
$$\left| \frac{1}{j\omega C} \right| \ll r_d // R + 200 \Rightarrow$$

$$C \gg \frac{1}{\omega (r_d // R + 200)} = \frac{1}{1000 \left(\frac{3.64 \times 1000}{1000 + 3.64} + 200 \right)}$$

$$C \gg \frac{1}{1000 (200 + 3.63)} = \frac{1}{1000 \times 203.63} =$$

$$C \gg 4.91 \times 10^{-6} \text{ F}$$

$$C \gg 4.91 \mu\text{F}$$



2. (20 points) At the circuit given above, the transistor is an n-channel enhancement mode MOSFET with $V_{TN}=2$ Volts and $K_n=1 \times 10^{-3} \text{ A/V}^2$. $R_d=1.5 \text{ k}\Omega$.

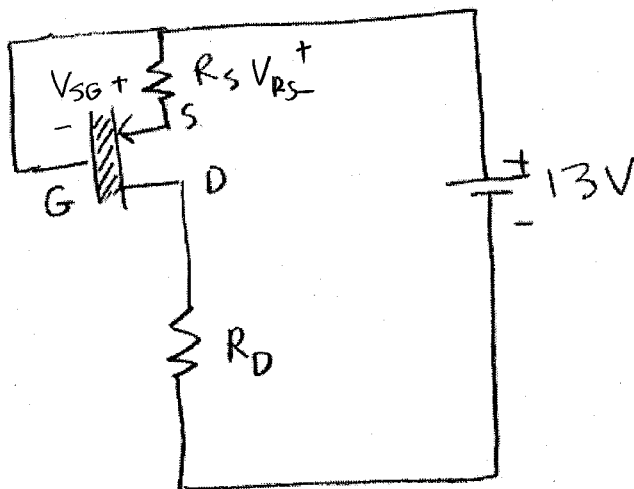
- (15 points) Find the bias (Voltages and currents of the transistor) of the transistor,
- (5 points) Verify the bias of the transistor. ~~state of the transistor~~

$$a-) V_{GS} = 12V \times \frac{4k}{8+4k} = 12V \times \frac{4}{12} = 4V \text{ assume SAT}$$

$$I_d = K_n (V_{GS} - V_{TN})^2 = 10^{-3} \text{ A/V}^2 [4 - 2]^2 = 4 \text{ mA}$$

$$V_{DS} = 12V - I_D R_D = 12 - 4 \times 10^{-3} \times 1.5 \times 10^3 = 12 - 6 = 6V$$

$$b-) 6V = V_{DS} > V_{GS} - V_{TN} = 2V \checkmark \text{ SAT}$$



3. (20 points) At the depletion-mode p-channel MOSFET circuit given above, $V_{TP}=4$ Volts and $K_P=3 \times 10^{-3} \text{ A/V}^2$. $R_d=2\text{k}\Omega$ and $R_S=1\text{k}\Omega$

- (15 points) Find the bias of the transistor,
- (5 points) Verify the solution.

a-) $0\text{V} = V_{SG} + I_D 1000\Omega = V_{SG} + 3 \times 10^{-3} (V_{SG} + (+4))^2 \times 1000$
assuming SAT

$$0 = V_{SG} + 3[V_{SG}^2 + 8V_{SG} + 16] = V_{SD} + 3V_{SG}^2 + 24V_{SG} + 48$$

$$0 = \underset{a}{3V_{SG}^2} + \underset{b}{24V_{SG}} + \underset{c}{48}$$

$$V_{SG1,2} = \frac{-24}{6} \pm \frac{\sqrt{625 - 12 \times 48}}{6} = \frac{-24 \pm 7}{6} = \frac{-18}{6}, \frac{-32}{6}$$

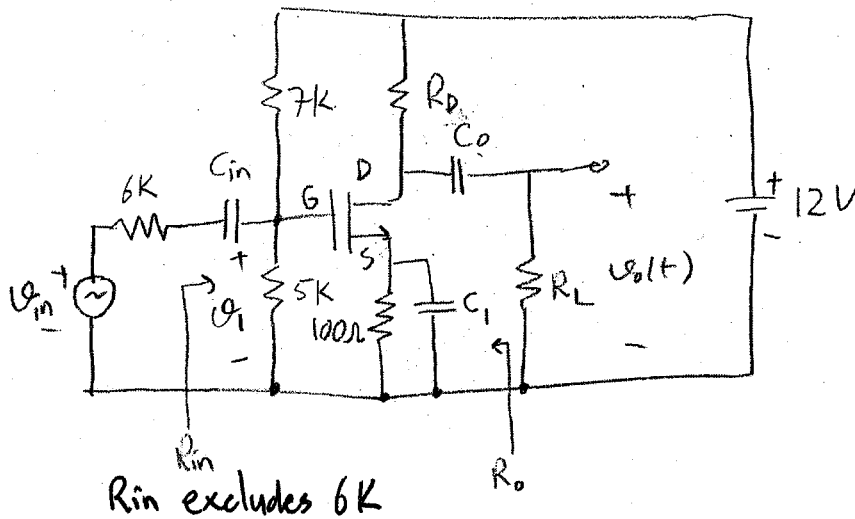
$$= -3, -5\frac{2}{3} \text{, } \left| -5\frac{2}{3} \right| \text{ is larger than } |V_{TP}| \Rightarrow \text{not applicable}$$

$$I_D = 3 \times 10^{-3} (-3 + 4)^2 = 3\text{mA}$$

$$\Rightarrow V_{SG} = -3\text{V}, V_{SD} = 13\text{V} - I_D 2000 - I_D 1000 = 13 - 3 \times 10^{-3} \times 3000 = 13 - 9 = 4\text{V}$$

b-) $I_D = 3\text{mA}, V_{SG} = -3\text{V} \Rightarrow V_{SD} = 4\text{V}, V_{SG} = 0 - \underbrace{3 \times 10^{-3}}_{I_D} \times \underbrace{10^3}_{R_S} = -3\text{V}$

$$V_{SD} = 4\text{V} > V_{SG} + V_{TP} = -3 + 4 = 1\text{V} \checkmark \Rightarrow \text{SAT}$$

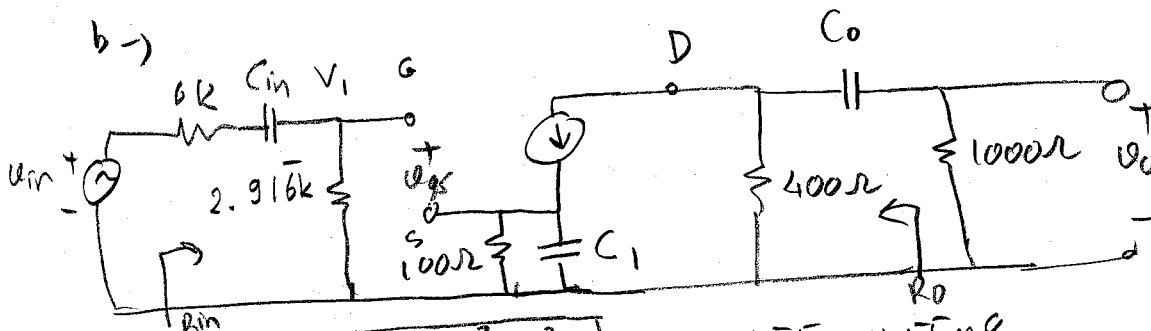


4. (36 points) At the circuit given above, the transistor is an n-channel enhancement mode MOSFET with $V_{TN}=1$ Volts and $K_n=3.333 \times 10^{-3} \text{ A/V}^2$. $R_D=400\Omega$ and $R_L=1000\Omega$.

Answer the following:

- (7) a-) (7 points) Derive the equation for g_m in terms of I_D & K_n at saturation
 (8) b-) (8 points) The drain current turns out to be 10mA and the transistor is at (6) for short AC capacitors
 (7) c-) (7 points) Find the voltage gain of the amplifier defined as V_o/V_{in} assuming that C_{in} , C_o and C_1 are very large.
 (7) d-) (7 points) Find the input and output impedances of the amplifier again assuming that C_{in} , C_o and C_1 are very large.
 (7) e-) (7 points) Find a condition (inequality) which imposes a limit on C_{in} if the frequency of operation is 1000Hz.

$$a-) I_D = K_n (V_{GS} - V_{TN})^2 \Rightarrow \frac{dI_D}{dV_{GS}} = 2K_n (V_{GS} - V_{TN}) = 2K_n \frac{I_D^{1/2}}{K_n^{1/2}} = 2\sqrt{K_n I_D}$$



$$b-) g_m = 2 \sqrt{3.333 \times 10^{-3} \times 10^2} = 0.01155 = 11.55 \text{ mS}$$

$$c-) v_o = -g_m 285.7 v_{gs} = -3.3 v_{gs} = -3.3 \times \frac{2.916}{6 + 2.916} v_{in} = -1.079 v_{in}$$

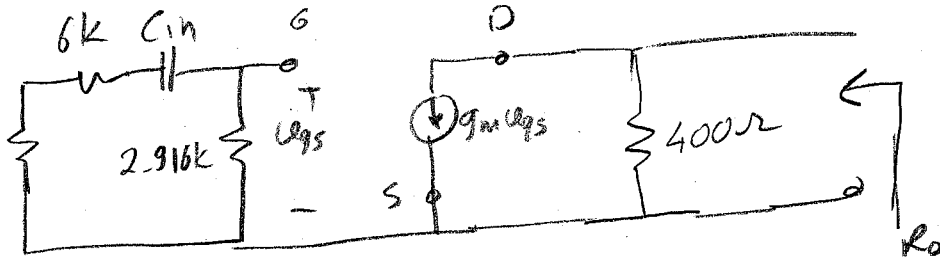
$$\Rightarrow \frac{v_o}{v_{in}} = -1.079$$

d-)

$$R_{in} = 7k \parallel 5k = 2.916k\Omega \text{ as } G_3 \text{ open for DC}$$

$R_o = 400\Omega$ as a current source

controlled by an independent source is open circuit.
or if you start the input source, the circuit becomes



The current source becomes a constant current source with no AC component \Rightarrow open circuit \Rightarrow
 $R_o = 400\Omega$

e-)
$$v_1 = v_{in} \cdot \frac{2916\Omega}{6000 + 2916 + \frac{1}{j\omega C_{in}}}$$
 this function which can also be written as

$$\frac{v_1}{v_{in}} = \frac{2916}{8916 + \frac{1}{j\omega C_{in}}} \text{ settles to } \frac{2916}{8916} \text{ as } \omega \rightarrow \infty$$

which is when $\left| \frac{1}{j\omega C_{in}} \right| \ll 8916\Omega \Rightarrow$

$$\left| \frac{1}{j\omega C_{in}} \right| \ll 8916\Omega$$

$$C_{in} \gg \frac{1}{2\pi \times 1000 \times 8916} = 1.785 \times 10^{-8} F = 17.85 \times 10^{-9} F$$

$$17.85 nF$$