

## Pop Quiz 1

## Section 1 - Time: 50 min

Show your work for full credit. Please read the questions carefully!

**n-channel MOSFET**

$$i_D = K_n (v_{GS} - V_{Th})^2 \quad \text{SAT}$$


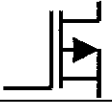
$$i_D = K_n [2(v_{GS} - V_{Th})v_{DS} - v_{DS}^2] \quad \text{NON-SAT}$$

**p-channel MOSFET**

$$i_D = K_p (v_{SG} + V_{Tp})^2 \quad \text{SAT}$$

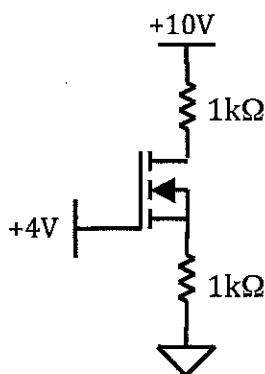
$$i_D = K_p [2(v_{SG} + V_{Tp})v_{SD} - v_{SD}^2] \quad \text{NON-SAT}$$

1. (20 points) Express the type of the transistors shown below. What is the sign of the threshold voltage? What are the conditions for the following operating states?

		
TYPE	n-channel Enhancement	p-channel Depletion
$V_{Th}$	$V_{Th} > 0$	$V_{Th} < 0$
OFF	$V_{GS} \leq V_{Th}$	$V_{SG} \leq -V_{Th}$
SAT	$V_{DS} \geq V_{GS} - V_{Th}$	$V_{SD} \geq V_{SG} + V_{Th}$
NON-SAT	$V_{DS} \leq V_{GS} - V_{Th}$	$V_{SD} \leq V_{SG} + V_{Th}$

(2 points each)

2. (40 points) Find the Q-point (operating point) values of  $I_{DS}$  and  $V_{DS}$  of the transistor shown below.  $V_{Th}=2V$ ,  $K_n=1mA/V^2$ .



(10 points)

$$V_G = 4V$$

$$V_S = I_D \cdot 1k$$

$$V_D = 10V - I_D \cdot 1k$$

$$V_{GS} = 4V - I_D \cdot 1k$$

if  $V_{GS} \geq V_{Th} \Rightarrow$  NOT OFF

Assume SAT check  $V_{DS} \geq V_{GS} - V_{Th}$

$$I_D = \frac{1mA}{V^2} (4V - 1k \cdot I_D - 2)^2$$

if  $I_D$  in mA units:

$$I_D = 1 \cdot (2 - I_D)^2 \Rightarrow I_D = \begin{cases} 1mA \\ 4mA \end{cases} \quad (10 \text{ points})$$

if  $I_D = 1mA$

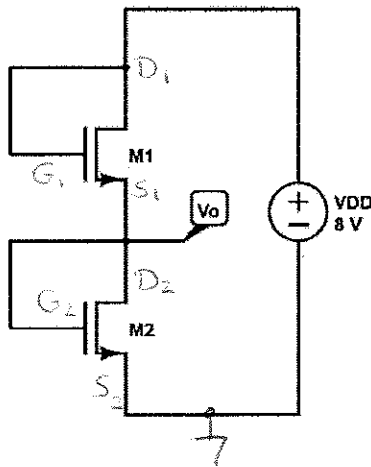
$$\begin{matrix} V_S = 1V \\ V_D = 9V \end{matrix} \Rightarrow V_{DS} = 8V$$

$$V_{DS} \geq V_{GS} - V_{Th}$$

$$8V \geq 3V - 2V \quad \text{SAT OK} \quad (10 \text{ points})$$

$$V_{GS} = 3V \geq V_{Th} = 2V \quad \text{NOT OFF} \quad (10 \text{ points})$$

3. (40 points) In the circuit shown below, the field effect transistors have threshold voltages,  $V_{TH} = 1V$ . The transistors' conductance parameters are  $K_{n1} = 4K_{n2} = 4mA/V^2$ . Determine  $V_{GS1}$ ,  $V_{GS2}$ ,  $V_O$ ,  $I_D$ . Show your work!



(5 points)

$$V_{GS1} = V_{DS1} \quad I_{D1} = I_{D2}$$

$$V_{GS2} = V_{DS2}$$

$$V_{DS1} + V_{DS2} = 8V \quad (*) \quad (5 \text{ points})$$

if T/R conducting they must be in SAT state because  $V_{DS} \geq V_{GS} - V_{TH}$

assume  $M_1$  and  $M_2$  in SAT

$$I_{D1} = I_{D2} \quad (KCL) \quad (5 \text{ points})$$

$$K_{n1} (V_{GS1} - V_{TH})^2 = K_{n2} (V_{GS2} - V_{TH})^2 \quad (5 \text{ points})$$

$$4K_{n2} (V_{DS1} - 1V)^2 = K_{n2} (V_{DS2} - 1)^2$$

$$2(V_{DS1} - 1) = (V_{DS2} - 1)$$

$$2V_{DS1} - 1 = V_{DS2} \quad (5 \text{ points})$$

from (\*)  $V_{DS1} + V_{DS2} = 8V \quad (10 \text{ points})$

$$3V_{DS1} = 9V \Rightarrow V_{DS1} = 3V \Rightarrow V_{DS2} = 5V$$

$$V_{GS1} = 3V \quad V_{GS2} = 5V$$

$$\Rightarrow V_O = 5V$$

$$I_{D1} = I_{D2} = 4 \frac{mA}{V^2} (3V - 1V)^2 = 16mA \quad (5 \text{ points})$$