Name:^	ll Kemal OKYAY	Section:
Signature:	SOLUTIONS	FFF 313 Fall 2013

Bilkent University
Department of Electrical and Electronics Engineering
EEE 313 Electronic Circuit Design

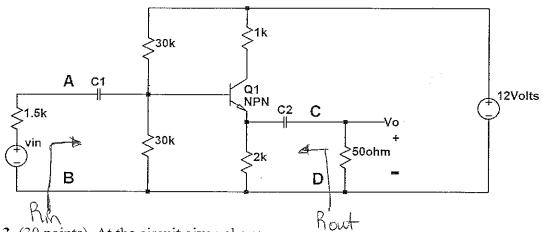
Midterm 2

4 December 2013, 18:00 (4 questions, 150 minutes)

- This is a closed book, closed notes exam. No cheat sheet allowed.
- All cell-phones should be completely turned off.
- Use a calculator for numerical computations. Carry at least 4 significant digits during calculations. Your final answer should be at least 3 significant digits.
- Be sure to write the **units** of all numerical results.
- Show all work clearly.
- Please put your **final answer** for each part inside a box for easy identification. Do not give multiple answers, they will not be graded.
- Do not remove the **staple** from the exam sheets or separate pages of the exam. All extra pages must be stamped to your exam.
- You may leave the exam room when you are done. However, please do not leave during the **last five minutes** of the exam.
- At the end of the exam, please stay seated unit all exam papers are collected.

Please do not write below this line

1. 20 pts.
2. 25 pts.
3. 30 pts.
4. 25 pts.
Total 100 pts.



3. (30 points) At the circuit given above: $\beta=74$, $V_{EE_{GW}}=0.7$ Volts, $V_{CE_{GWSAT}}=0.3$ Volts, C_1 and C_2 are very large

Please answer the following:

- a) (6 points) Find the DC bias of the transistor,
- b) (6 points) Draw the AC equivalent circuit,
- c) (6 points) Find the gain of the circuit defined as Vo/Vin
- d) (6 points) Find the input impedance (between points A and B),
- e) (6 points) Find the output impedance (between points Cand D) by taking the source as only Vin.

a) assume F.ACT
$$I_{B} = \frac{5.3^{\circ}}{165^{\circ}} = 32.4 \text{ p.A.} > 0$$

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$$I_{C} = 2.377 \text{ m.A.}$$

$$V_{C6} = 12^{\circ} - 2.377^{\circ} - 4.215^{\circ} = 4.808^{\circ} > V_{C}$$

$$V_{C1} = 4.808^{\circ}$$

$$V_{C2} = 4.808^{\circ}$$

$$V_{C3} = 4.808^{\circ}$$

$$V_{C4} = 12^{\circ} - 2.377^{\circ} - 4.215^{\circ} = 4.808^{\circ} > V_{C}$$

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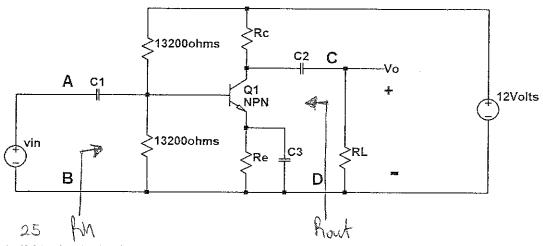
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c)
$$V_0 = \frac{1}{15} (\beta + 1) (2k // 50 \Omega) = 2k // 50 \Omega = 48.78 \Omega$$
 $V_{10} = \frac{2k}{15} (G + (\beta + 1)(2k // 50 \Omega))$
 $V_{10} = \frac{Rm}{R_{10} + 15k} V_{10}$
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 $V_{10} = \frac{Rm}{R_{10} + 15k} V_{10} (2k // 50 \Omega)$
 $V_{10} = \frac{3442.85 \Omega}{3442.85 \Omega} = \frac{3442.85 \Omega}{810 + 75 \cdot 48.78} = 0.5965 \frac{3658.5}{4460.5}$
 $V_{10} = \frac{3442.85 \Omega}{4468.5 \Omega} = \frac{3442.85 \Omega}{13526}$
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4. (20 points) At the BJT circuit given above: $\beta=100$, $V_{BE_{DN}}=0.8$ Volts, $V_{CE_{DNSAT}}=0.2$ Volts, C_1 , C_2 and C_3 are very large:

The desired characteristics of the amplifier are: R_{in} =550 Ω , R_{o} =1000 Ω , Voltage gain A_{v} =100 defined as Vo/Vin Please note that R_{in} is between A and B, R_{o} is between C and D.

Please design the circuit to have the desired characteristics (i.e., find the values of R_c , R_e , and R_L)

$$||f||_{R_0} = 1k.52 \implies ||R_c|| = R_0 = 1k.52 \pmod{5}$$

$$||f||_{R_0} = -\beta i_b (R_c ||R_c)$$

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