

CHAPTER 3 : CIRCUIT ANALYSES TECHNIQUES

NODE ANALYSIS : Variables are node voltages

Basic Steps :

Step 1 : Choose a reference node (Datum = Ground).
Specify reference directions for element currents and hence voltages. Now, we can write each element voltage in terms of node voltages.

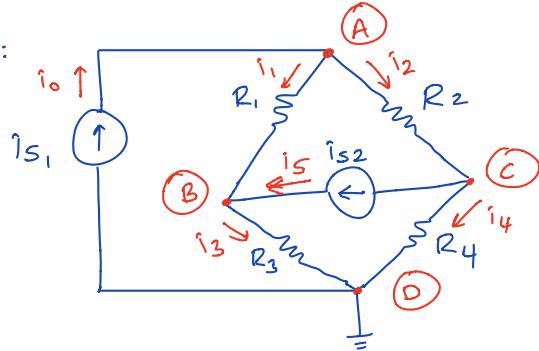
Step 2 : Write KCL equations at all nodes except for the reference node.

Step 3 : Use element relations (e.g., $i = Gv$) to express currents in terms of node voltages.

Step 4 : Substitute these in KCL equations.

Result : We will have $(n-1)$ equations in terms of $(n-1)$ node voltages.

Example :



Step 1 : choose D as the ground node.

$$V_1 = V_A - V_B$$

$$V_3 = V_B$$

$$V_2 = V_A - V_C$$

$$V_4 = V_C$$

Step 2 : KCL at A : $-i_0 + i_1 + i_2 = 0$

KCL at B : $-i_1 + i_3 - i_5 = 0$

KCL at C : $-i_2 + i_4 + i_5 = 0$

Step 3 : $i_0 = i_{s1}$

$$i_3 = G_3 \cdot V_B$$

$$i_1 = G_1 (V_A - V_B)$$

$$i_4 = G_4 \cdot V_C$$

$$i_2 = G_2 (V_A - V_C)$$

$$i_5 = i_{s2}$$

Step 4 : KCL at A : $-i_{S_1} + G_1(V_A - V_B) + G_2(V_A - V_C) = 0$

$$(G_1 + G_2)V_A - G_1V_B - G_2V_C = i_{S_1}$$

KCL at B : $-G_1(V_A - V_B) + G_3 \cdot V_B - i_{S_2} = 0$

$$-G_1V_A + (G_1 + G_3)V_B = i_{S_2}$$

KCL at C : $-G_2(V_A - V_C) + G_4 \cdot V_C + i_{S_2} = 0$

$$-G_2V_A + (G_2 + G_4)V_C = -i_{S_2}$$

* we can write these equations in matrix form as:

$$\underline{G} \underline{V} = \underline{U}$$

/ current source vector. $(n-1) \times 1$

vector of node voltages $(n-1) \times 1$

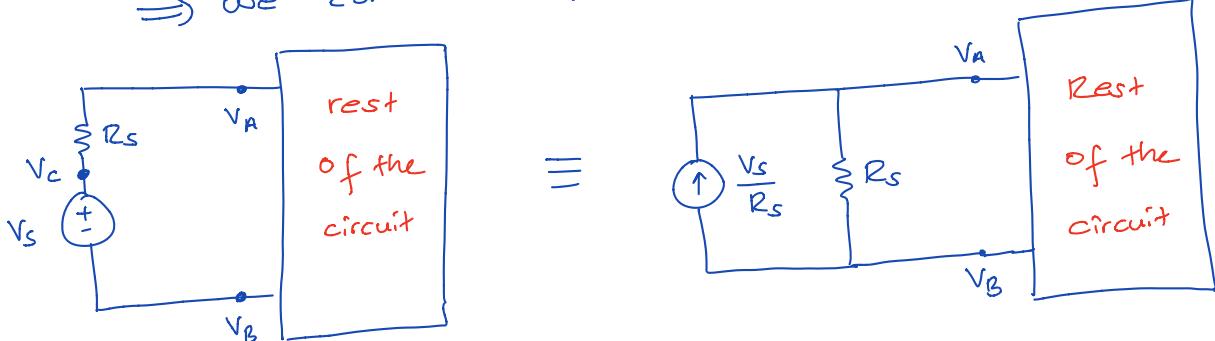
Conductance (admittance) matrix : $(n-1) \times (n-1)$

* For the previous example :

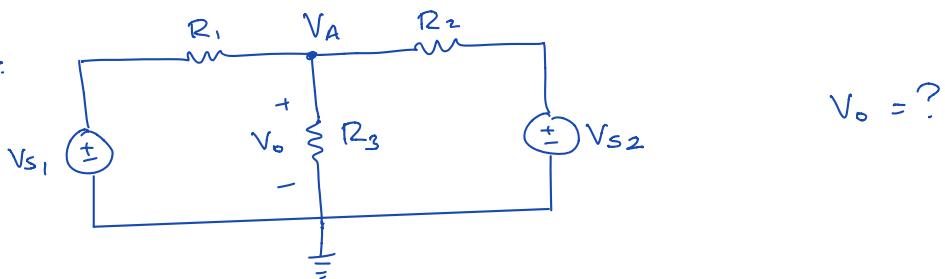
$$\begin{bmatrix} (G_1+G_2) & -G_1 & -G_2 \\ -G_1 & (G_1+G_3) & 0 \\ -G_2 & 0 & (G_2+G_4) \end{bmatrix} \begin{bmatrix} V_A \\ V_B \\ V_C \end{bmatrix} = \begin{bmatrix} i_{S_1} \\ i_{S_2} \\ -i_{S_2} \end{bmatrix}$$

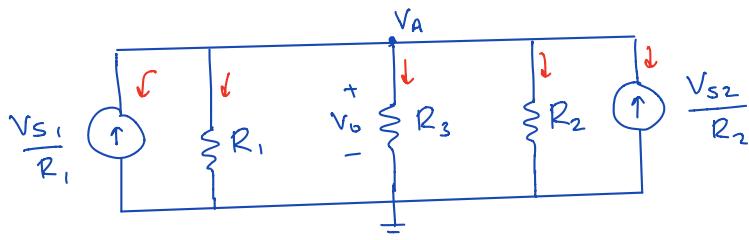
* What happens when we have voltage sources as well?
 ⇒ modify the same idea

case 1 : If a voltage source is in series with a resistor
 ⇒ we can use equivalence (i.e., source transformation)



Example :



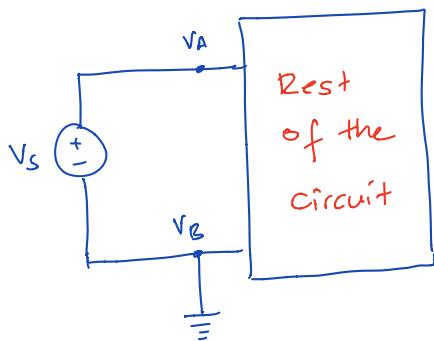


$$\text{KCL at } A : -\frac{V_{S1}}{R_1} + \frac{V_A}{R_1} + \frac{V_A}{R_3} + \frac{V_A}{R_2} - \frac{V_{S2}}{R_2} = 0$$

$$(G_1 + G_2 + G_3)V_A = G_1 \cdot V_{S1} + G_2 \cdot V_{S2}$$

$$V_D = V_A = \frac{G_1 \cdot V_{S1} + G_2 \cdot V_{S2}}{G_1 + G_2 + G_3}$$

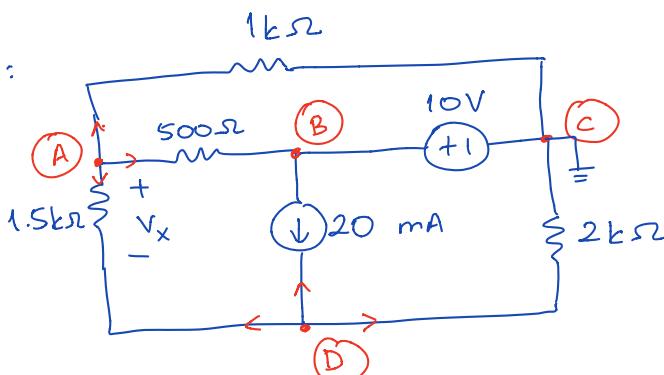
Case 2 : If a voltage source is not in series with a resistor
 \Rightarrow we may choose one end of the voltage source
as the reference node (ground).



- * Do not write node equation at node A.
- * There remains $(n-2)$ nodes, write node equations for these as usual.
- * we get $(n-2)$ equations
 - + $V_A = V_S$

$\underbrace{\quad}_{(n-1) \text{ equations}}$ we can find all the node voltages.

Example :



Find node voltages.
Find V_X

$$V_B = 10V$$

$$\text{KCL at } A : \frac{V_A}{1k\Omega} + \frac{V_A - V_D}{1.5k\Omega} + \frac{V_A - 10}{0.5k\Omega} = 0$$

(3) (2) (6)

$$11V_A - 2V_D = 60$$

$$\underline{\text{KCL at D}} : \frac{V_D - V_A}{1.5k\Omega} - 20mA + \frac{V_D}{2k\Omega} = 0$$

(4) (6k) (3)

4

$$-4V_A + 7V_D = 120$$

* 2 equations , 2 unknowns . Solve together :

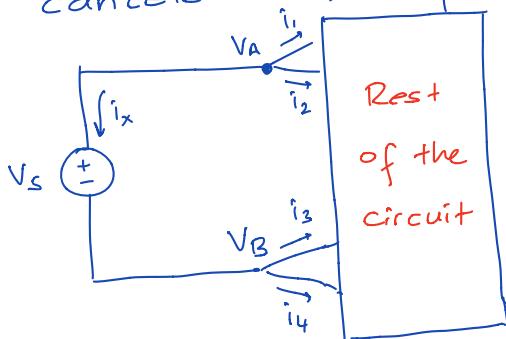
$$V_A \approx 9.6 \text{ V}$$

$$V_B \approx 22.6 \text{ V}$$

* Finally, $v_x = v_A - v_D \approx -13 \text{ V}$

Case 3: If a voltage source is not in series with a resistor and we cannot (or do not want to) choose one end of the voltage source as reference node:
C. Both nodes of the

reference node :
 Sum the KCL equations for both nodes of the voltage source
 voltage source \Rightarrow Current of the voltage source
 cancels \Rightarrow Supernode



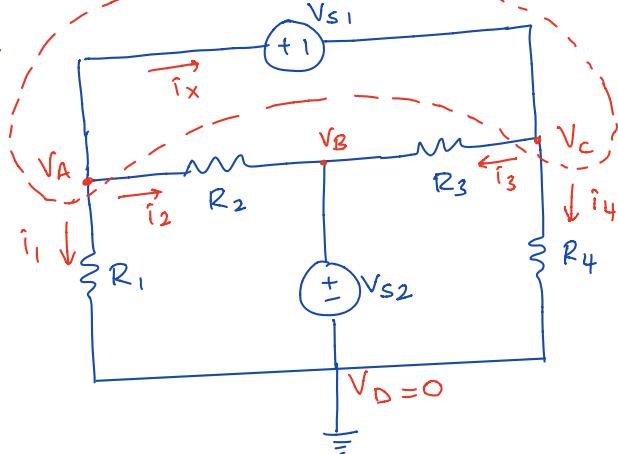
$$KCL \text{ at A} : i_1 + i_2 + i_x = 0$$

$$\text{KCL at B: } -i_1 + i_3 + i_4 = 0$$

$$\underline{i_1 + i_2 + i_3 + i_4 = 0}$$

$$\text{Also, } V_A - V_B = V_s$$

Example



$$V_B = V_{S2}$$

$$KCL \text{ at A} : i_1 + i_2 + i_x = 0$$

$$\text{KCL at C} : -i_x + i_3 + i_4 = 0$$

KCL at supernode : $i_1 + i_2 + i_3 + i_4 = 0 \Rightarrow$ can be obtained by using the Gaussian volume

$$G_1 \cdot V_A + G_2 (V_A - V_B) + G_3 (V_C - V_B) + G_4 V_C = 0$$

Using $V_B = V_{S2}$,

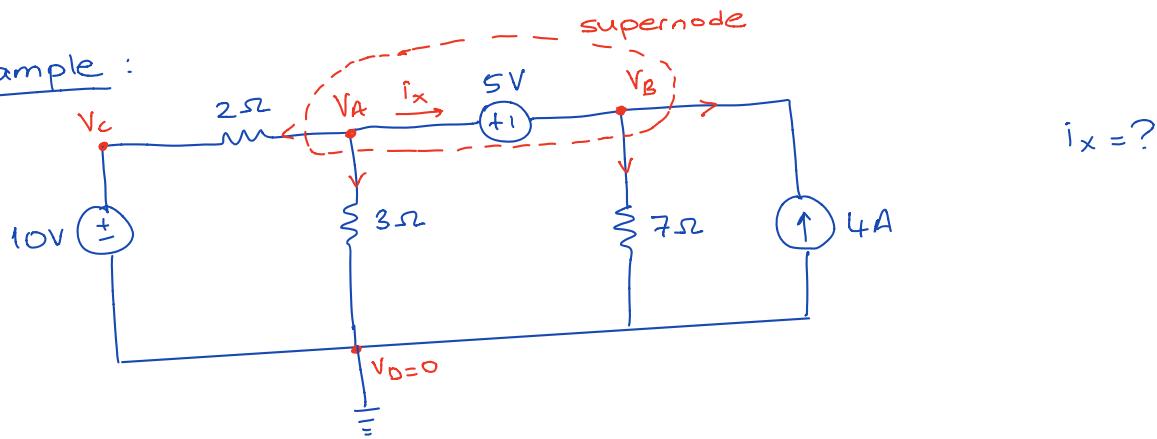
$$(G_1 + G_2) V_A + (G_3 + G_4) V_C = (G_2 + G_3) V_{S2}$$

* Need one more equation:

KVL at A and C : $V_A - V_C = V_{S1}$

* Now, we have two equations and two unknowns (V_A, V_C)

Example :



$$V_C = 10 \text{ V}$$

$$\text{KCL at supernode (A+B)}: \frac{V_A - 10}{2} + \frac{V_A}{3} + \frac{V_B}{7} - 4 = 0$$

$$(21) \quad (14) \quad (6) \quad (42)$$

$$35 V_A + 6 V_B = 210 + 168 = 378$$

$$\text{Also: } V_A - V_B = 5 \text{ V} \quad (\text{KVL at supernode})$$

$$V_B = V_A - 5$$

$$\text{Insert back: } 35 V_A + 6 (V_A - 5) = 378$$

$$V_A = \frac{408}{41} \approx 9.95 \text{ V}$$

$$V_B = V_A - 5 \approx 4.95 \text{ V}$$

* Now, to find i_x , write KCL at A:

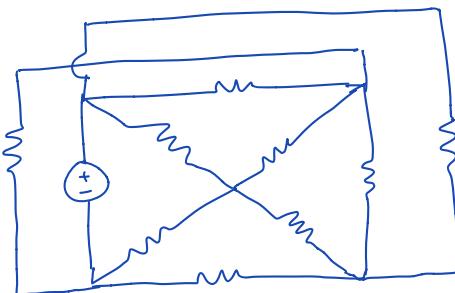
$$\frac{V_A - 10}{2} + \frac{V_A}{3} + i_x = 0$$

$$i_x = -\frac{V_A}{3} + \frac{10 - V_A}{2} = -3.29 \text{ A}$$

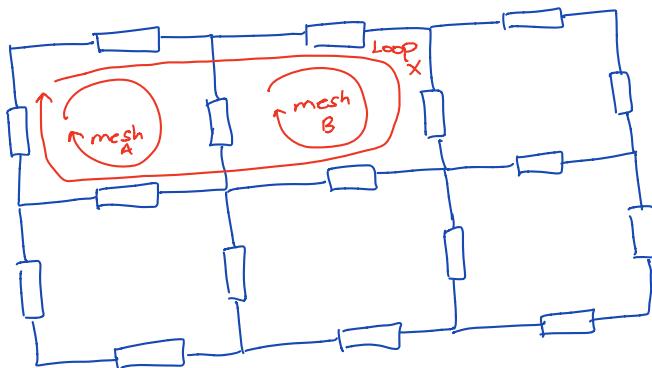
MESH ANALYSES : Variables are mesh currents

Planar Circuit : A circuit that can be drawn on a plane without crossovers.

Counter example :

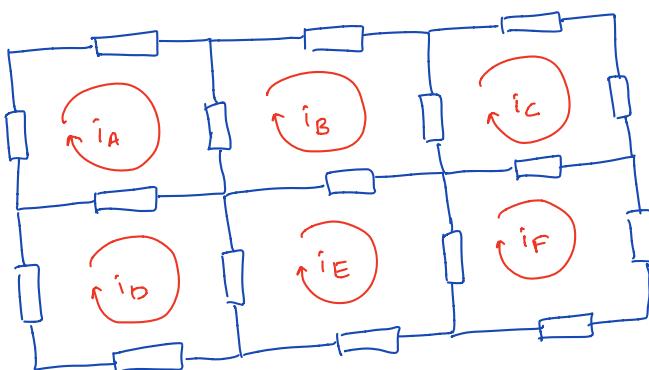


mesh : Given a planar circuit, a mesh is a loop which does not contain any elements inside.



Fact : Given a planar circuit that has b two-terminal elements and n nodes, there are exactly $(b-n+1)$ meshes \Rightarrow same as the number of independent KVL equations

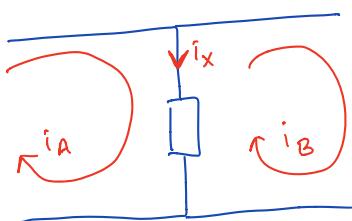
mesh current : In each mesh, define a circulating current called a mesh current.
Necessary for writing KVL equations.



** mesh currents are conceptually the duals of node voltages. However, they are not as natural. Node voltages can be measured easily, whereas mesh current is an abstract variable that simplifies analysis. They may be impossible to measure directly.

* Convention : Clockwise circulation direction for mesh currents.

* If a branch current i_x is common between mesh A and mesh B :



$$i_x = i_A - i_B$$

* Hence, any element current can be written in terms of mesh currents.

Basic Steps :

Step 1 : Choose meshes. Specify reference directions for element currents and hence voltages. Write each element current in terms of mesh currents.

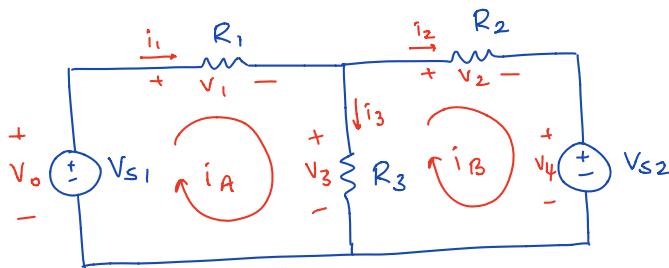
Step 2 : Write KVL equations at all meshes.

Step 3 : Use element relations (e.g., $v = Ri$) to express voltages in terms of mesh currents.

Step 4 : Substitute these in KVL equations.

Result : We will have $(b-n+1)$ equations in terms of $(b-n+1)$ mesh currents.

Example :



Step 1 : $i_1 = i_A$
 $i_2 = i_B$
 $i_3 = i_A - i_B$

Step 2 : KVL at mesh A : $V_1 + V_3 - V_o = 0$

KVL at mesh B : $V_2 + V_4 - V_3 = 0$

Step 3 : Element relations

$$V_o = V_{S1}$$

$$V_1 = R_1 \cdot i_1 = R_1 \cdot i_A$$

$$V_2 = R_2 \cdot i_2 = R_2 \cdot i_B$$

$$V_3 = R_3 \cdot i_3 = R_3 (i_A - i_B)$$

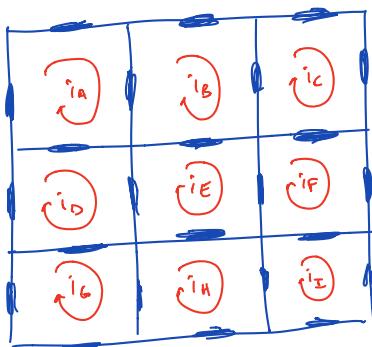
$$V_4 = V_{S2}$$

Step 4 : KVL at mesh A : $R_1 i_A + R_3 (i_A - i_B) - V_{S1} = 0$
 $(R_1 + R_3) i_A - R_3 i_B = V_{S1}$

KVL at mesh B : $R_2 \cdot i_B + V_{S2} - R_3 (i_A - i_B) = 0$
 $-R_3 i_A + (R_2 + R_3) i_B = -V_{S2}$

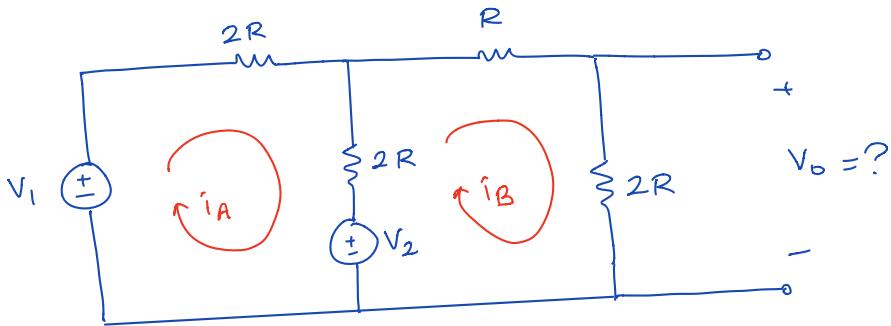
* 2 equations, 2 unknowns (i_A, i_B)

* Side note :



Cannot measure
 i_E directly.
It is an abstract
variable.

Example :



KVL at mesh A : $2R \cdot i_A + 2R(i_A - i_B) + V_2 - V_1 = 0$
 $4R \cdot i_A - 2R i_B = V_1 - V_2$

KVL at mesh B : $R \cdot i_B + 2R \cdot i_B - V_2 + 2R(i_B - i_A) = 0$
 $-2R i_A + 5R \cdot i_B = V_2$

$\times 2$

$-4R i_A + 10R \cdot i_B = 2V_2$
 $+ 4R \cdot i_A - 2R \cdot i_B = V_1 - V_2$

$8R i_B = V_1 + V_2$

$i_B = \frac{V_1 + V_2}{8R}$

Then, $V_o = 2R \cdot i_B = \frac{V_1 + V_2}{4}$

* We can write these equations in matrix form as:
 $R \begin{pmatrix} i \\ i \end{pmatrix} = U$ voltage source vector : $(b-n+1) \times 1$
vector of mesh currents : $(b-n+1) \times 1$
Resistance (impedance) matrix : $(b-n+1) \times (b-n+1)$

* For the previous example:

$$\begin{bmatrix} 4R & -2R \\ -2R & 5R \end{bmatrix} \begin{bmatrix} i_A \\ i_B \end{bmatrix} = \begin{bmatrix} V_1 - V_2 \\ V_2 \end{bmatrix}$$

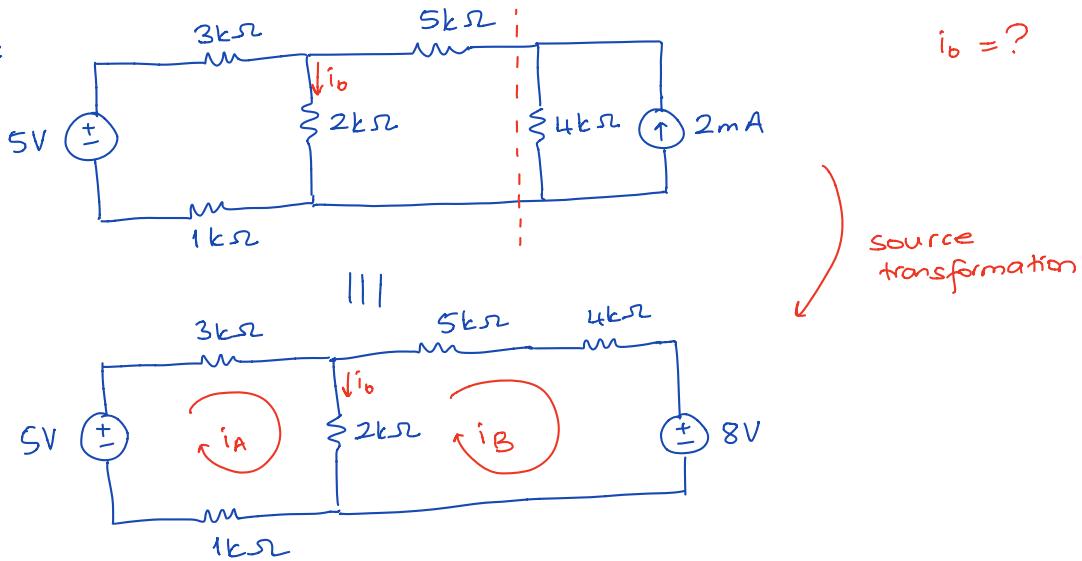
* Simplest case of mesh equations: circuits with independent voltage sources and resistors \Rightarrow easy to write KVL.

mesh Equations with Current Sources:

We have 3 different cases:

- * Case 1: If a current source is in parallel with a resistor
 \Rightarrow apply source transformation, i.e., replace with a voltage source in series with a resistor

Example:



KVL at mesh A: $i_A \cdot 3k + (i_A - i_B) \cdot 2k + i_A \cdot 1k - 5V = 0$

$$6i_A - 2i_B = 5 \quad (i_A, i_B \text{ in mA})$$

KVL at mesh B: $i_B \cdot 5k + i_B \cdot 4k + 8V + (i_B - i_A) \cdot 2k = 0$

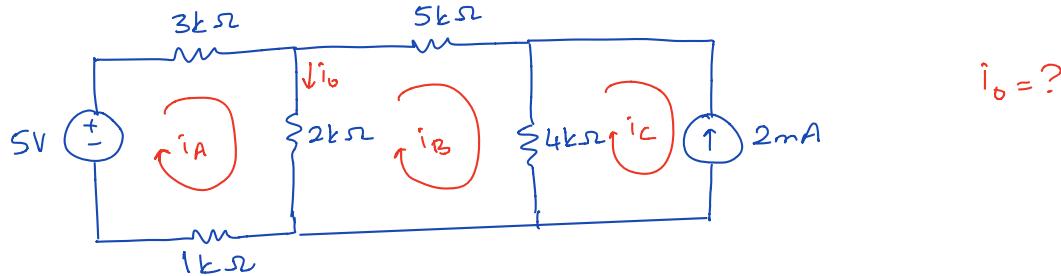
$$\begin{aligned} & -2i_A + 11i_B = -8 \\ & -6i_A + 33i_B = -24 \\ & + 6i_A - 2i_B = 5 \\ \hline & 31i_B = -19 \Rightarrow i_B \approx -0.61 \text{ mA} \end{aligned}$$

$$i_A = \frac{5+2i_B}{6} \approx 0.63 \text{ mA}$$

* Then, $i_o = i_A - i_B \approx 1.24 \text{ mA}$

- * Case 2: If a current source is in only one mesh
 \Rightarrow that mesh current is already known.
 Hence, write the remaining mesh equations.

Example :



$$i_C = -2 \text{ mA}$$

KVL at mesh A : (same as before)

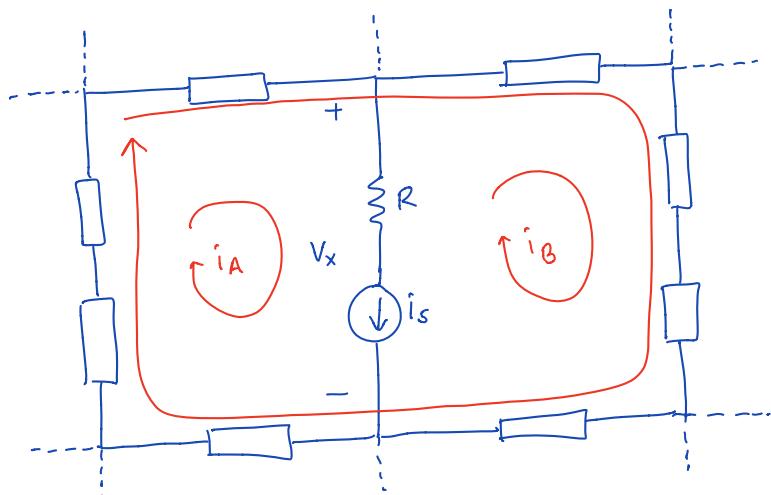
$$6i_A - 2i_B = 5$$

KVL at mesh B : $i_B \cdot 5k + (i_B - i_C) \cdot 4k + (i_B - i_A) \cdot 2k = 0$

$$-2i_A + 11i_B = 4i_C = -8 \Rightarrow \text{some as before.}$$

* Answer will be the same as before.

Case 3 : If a current source is between two meshes
 \Rightarrow we can combine these two mesh equations into a single mesh equation \Rightarrow supermesh



$$\text{mesh A : } \dots + V_x + \dots = 0$$

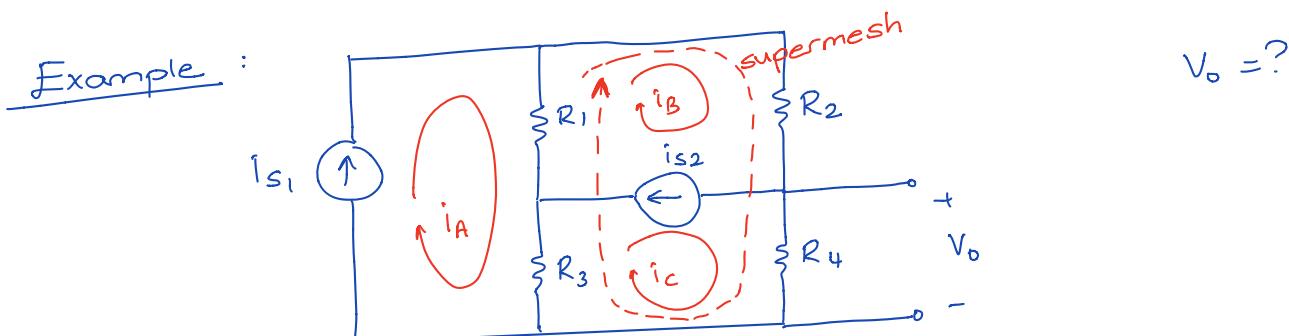
$$\text{mesh B : } \dots - V_x + \dots = 0$$

$$\text{mesh A} + \text{mesh B} : V_x \text{ is eliminated}$$

\Rightarrow supermesh equation

* we need one more equation:
 $i_s = i_A - i_B$

* we again get $(b-n+1)$ equations for $(b-n+1)$ unknowns.



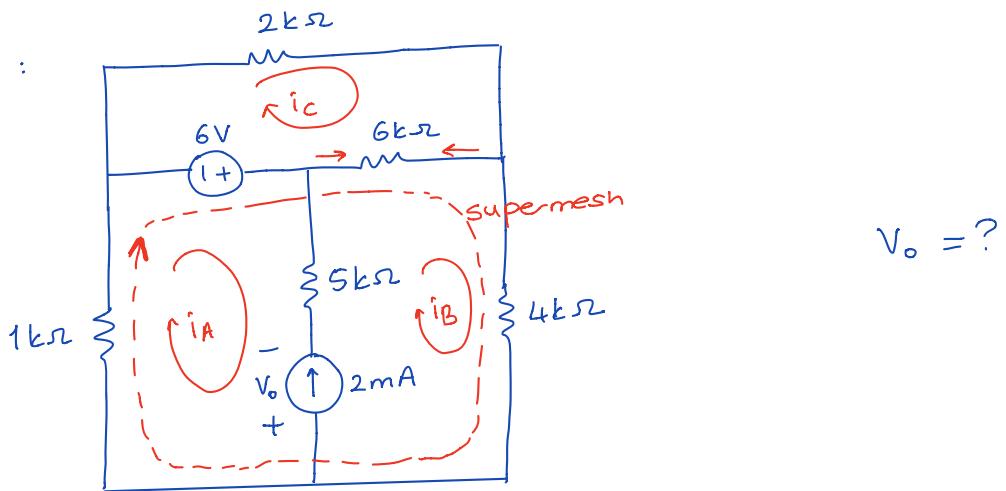
* $i_A = i_{S1}$

* Supermesh (B+C): $R_2 \cdot i_B + R_4 \cdot i_C + R_3(i_C - i_A) + R_1(i_B - i_A) = 0$
 $-(R_1 + R_3)i_A + (R_1 + R_2)i_B + (R_3 + R_4)i_C = 0$
 $(R_1 + R_2)i_B + (R_3 + R_4)i_C = (R_1 + R_3) \cdot i_{S1}$

* Also, from supermesh: $i_B - i_C = i_{S2}$ 2 equations
2 unknowns

* Then, $V_o = R_4 \cdot i_C$

Example :



$$i_B - i_A = 2 \text{ mA} \Rightarrow i_A = i_B - 2$$

Supermesh (A+B): $-6 + 6(i_B - i_C) + 4i_B + 1 \cdot i_A = 0$ (currents in mA)

$$i_A + 10i_B - 6i_C = 6$$

$$(i_B - 2) + 10i_B - 6i_C = 6$$

$$11i_B - 6i_C = 8 \quad \dots \quad (\text{Eqn. 1})$$

mesh C: $2 \cdot i_C + 6(i_C - i_B) + 6 = 0$
 $-6i_B + 8i_C = -6 \quad \dots \quad (\text{Eqn. 2})$

$4 \times \text{Eqn. 1}$: $44i_B - 24i_C = 32$

$3 \times \text{Eqn. 2}$: $-18i_B + 24i_C = -18$

$$\begin{array}{r} 44i_B - 24i_C = 32 \\ -18i_B + 24i_C = -18 \\ \hline 26i_B = 14 \end{array} \Rightarrow i_B = \frac{14}{26} \approx 0.54 \text{ mA}$$

$$i_A = i_B - 2 \approx -1.46 \text{ mA}$$

$$i_C = \frac{6i_B - 6}{8} = -0.35 \text{ mA}$$

Note: $5\text{k}\Omega$ resistor has no effect on currents.

* Now, to find V_o :

$$\text{KVL at mesh B} : 6(i_B - i_C) + 4 \cdot i_B + V_o + 5 \cdot \underbrace{(i_B - i_A)}_{2 \text{ mA}} = 0$$

$$V_o = -10 - 10i_B + 6i_C \\ \approx -17.5 \text{ V}$$

Skew resistor effects V_o .

LINEAR CIRCUITS:

Implicants: Superposition, Thevenin / Norton equivalent circuits if it contains linear elements

* A circuit is called linear if it contains linear elements + independent sources.

* Circuit Equations: Combined constraints

$$\text{KCL} : \sum_i i = 0$$

$$\text{KVL} : \sum_v v = 0$$

$$\text{Element} : Mv + Ni = u$$

$$\left\{ \begin{array}{l} \left[\begin{array}{cc} 0 & A \\ B & 0 \\ M & N \end{array} \right] \left[\begin{array}{c} v \\ i \end{array} \right] = \left[\begin{array}{c} 0 \\ 0 \\ u \end{array} \right] \\ T \quad \omega \quad u_s \end{array} \right.$$

* Resulting equations are linear: $T\omega = u_s$

* b : number of two-terminal elements

* T : $2b \times 2b$ matrix that depends on circuit elements, but not on independent sources.

ω : $2b \times 1$ vector of unknowns

u_s : $2b \times 1$ vector that depends only on the independent sources.

Consequences of Linearity:

Homogeneity: $f(\alpha x) = \alpha f(x)$, $\alpha \in \mathbb{R}$

In our case:

input
sources

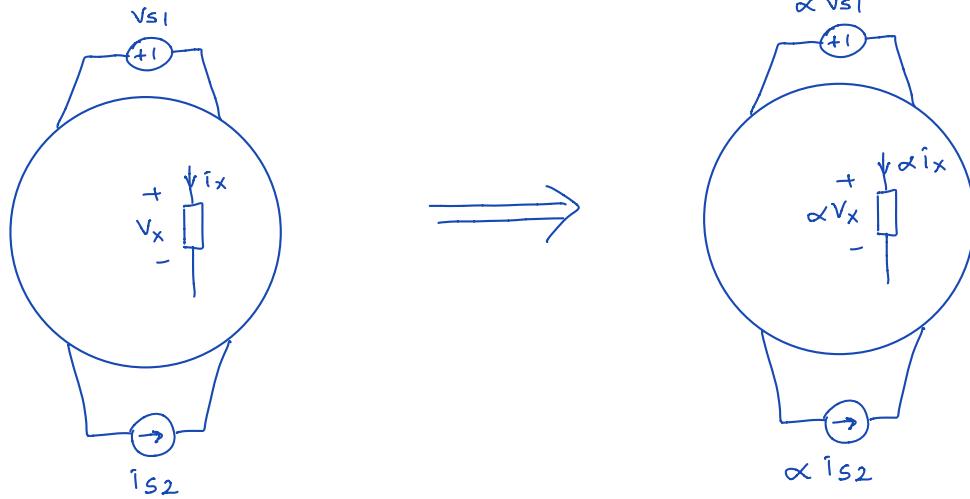
output
element voltages, currents

$$\Rightarrow \frac{u_s}{\alpha u_s} \xrightarrow{\longrightarrow} \frac{\omega}{\alpha \omega}$$

Proof : $T\omega = u_s$

$$\alpha u_s = \alpha T\omega = T(\alpha\omega)$$

Circuit Interpretation :



* Note that all independent sources must be multiplied by the same constant α .

Additivity : $f(x_1 + x_2) = f(x_1) + f(x_2)$

In our case:

input		output
u_{s1}	→	ω_1
u_{s2}	→	ω_2
$\Rightarrow u_{s1} + u_{s2}$	→	$\omega_1 + \omega_2$

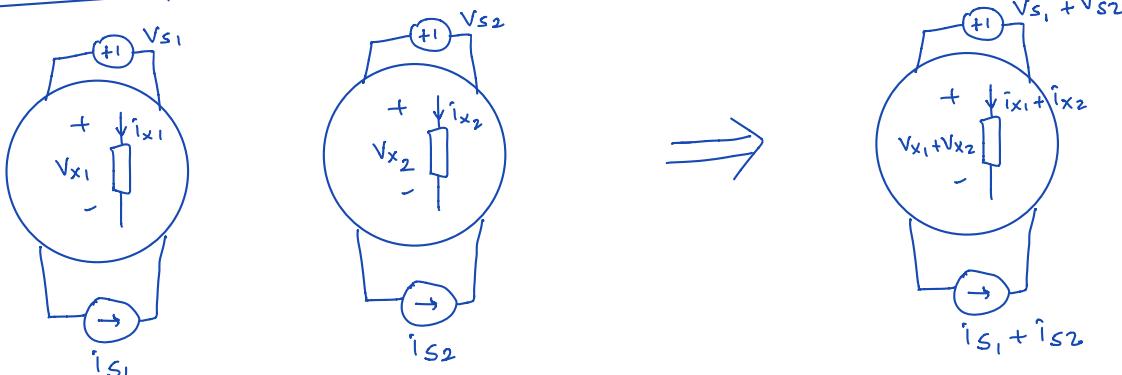
Proof : $T\omega_1 = u_{s1}$, $T\omega_2 = u_{s2}$

$$T\omega_1 + T\omega_2 = u_{s1} + u_{s2}$$

$$T(\omega_1 + \omega_2) = u_{s1} + u_{s2}$$

$$\text{So, } u_{s1} + u_{s2} \rightarrow \omega_1 + \omega_2$$

Circuit Interpretation :



Superposition:

* A consequence of linearity. Assume that the source vector

u_s is given as:

$$u_s = \begin{bmatrix} 0 \\ \vdots \\ 0 \\ v_{s1} \\ i_{s1} \end{bmatrix} = \underbrace{\begin{bmatrix} 0 \\ \vdots \\ 0 \\ v_{s1} \\ 0 \end{bmatrix}}_{u_{s1}} + \underbrace{\begin{bmatrix} 0 \\ \vdots \\ 0 \\ 0 \\ i_{s1} \end{bmatrix}}_{u_{s2}} = u_{s1} + u_{s2}$$

we have only two sources
(one voltage source, one current source)

* Note that for u_{s1} , only v_{s1} is operational.

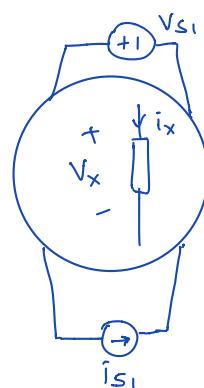
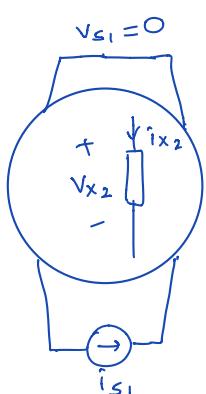
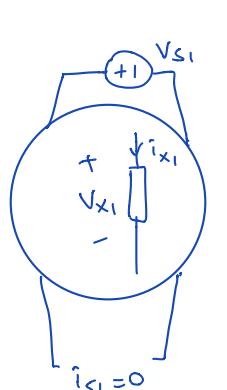
$i_{s1} = 0 \Rightarrow i_{s1}$ current source set to zero
 \Rightarrow set to OPEN CIRCUIT

* Similarly, for u_{s2} , only i_{s1} is operational.

$v_{s1} = 0 \Rightarrow v_{s1}$ voltage source set to zero
 \Rightarrow set to SHORT CIRCUIT.

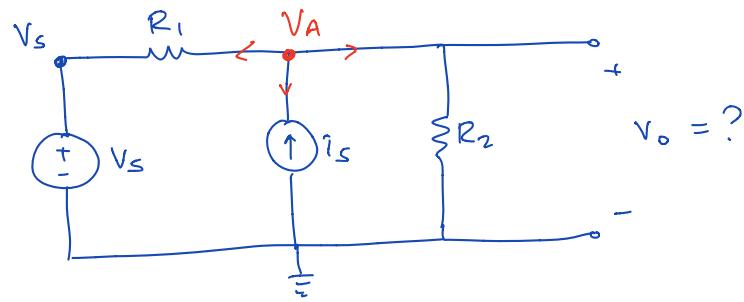
* $u_{s1} \rightarrow w_1$, $u_{s2} \rightarrow w_2$

From linearity: $u_s = u_{s1} + u_{s2} \Rightarrow w = w_1 + w_2$

Circuit interpretation:

$$i_x = i_{x1} + i_{x2}$$

$$v_x = v_{x1} + v_{x2}$$

Example :

Direct way : $V_o = V_A$

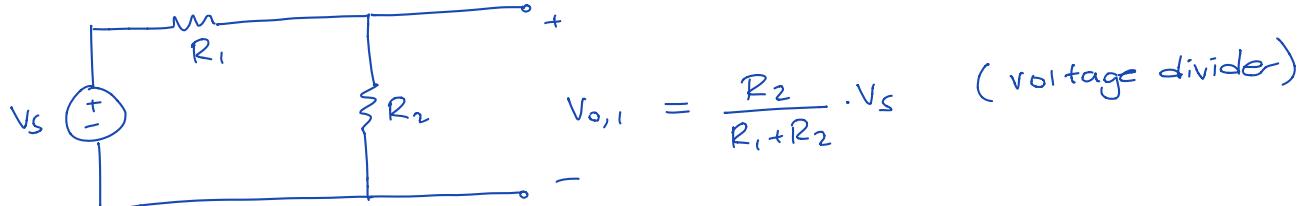
KCL at A : $\frac{V_A - V_s}{R_1} - i_s + \frac{V_A}{R_2} = 0$

$$(R_1 + R_2)V_A = R_2 \cdot V_s + R_1 R_2 i_s$$

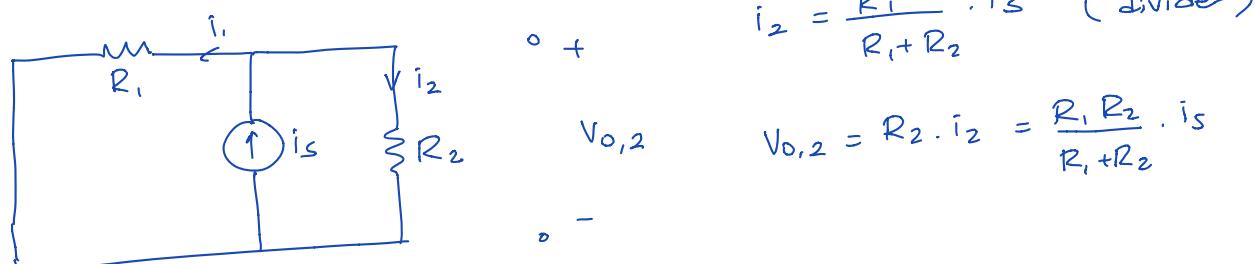
$$V_A = V_o = \frac{R_2}{R_1 + R_2} V_s + \frac{R_1 R_2}{R_1 + R_2} i_s$$

Superposition:

Due to V_s : Set $i_s = 0 \Rightarrow$ OPEN CIRCUIT



Due to i_s : Set $V_s = 0 \Rightarrow$ SHORT CIRCUIT



Add the results : $V_o = V_{o,1} + V_{o,2}$

$$= \underbrace{\frac{R_2}{R_1 + R_2} V_s}_{K_1} + \underbrace{\frac{R_1 R_2}{R_1 + R_2} i_s}_{K_2} = K_1 \cdot V_s + K_2 \cdot i_s$$

In general: If we have a linear circuit with n independent voltage sources V_{S1}, \dots, V_{Sn} , and m independent current sources i_{S1}, \dots, i_{Sm} , and y denotes an arbitrary branch voltage or current:

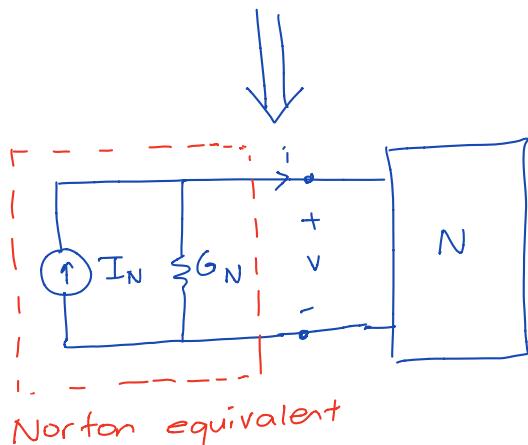
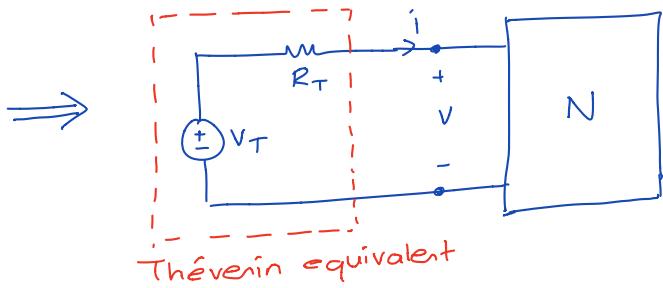
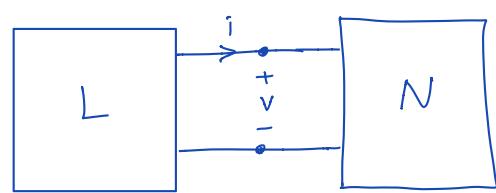
$$y = K_1 \cdot V_{S1} + \dots + K_n \cdot V_{Sn} + H_1 \cdot i_{S1} + \dots + H_m \cdot i_{Sm}$$

* Here, K_i and H_j are constants that depend only on the circuit elements but not on the independent sources themselves.

Thévenin and Norton Equivalent Circuits:

Thévenin and Norton Equivalent Circuits:

* N is any circuit (which may be nonlinear), and L is any linear circuit. The total circuit is assumed to have a unique solution.



V_T : Thévenin eq. voltage source

R_T : " " resistance

I_N : Norton eq. current source

G_N : " " conductance

For Thévenin eq. circuit: $V_T = R_T \cdot i + v$

For Norton eq. circuit: $I_N = \frac{v}{R_N} + i$

} compare

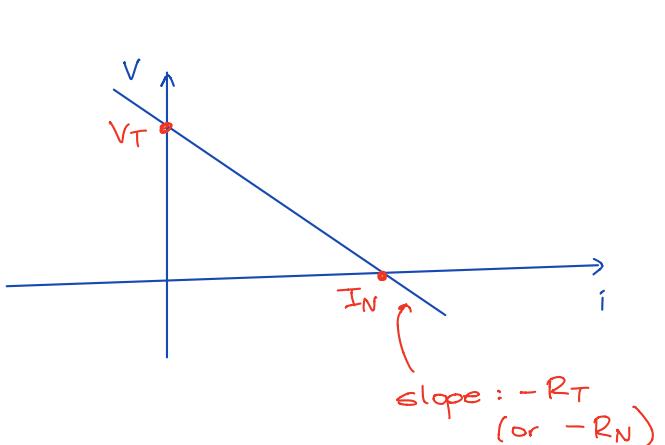
$$i = \frac{V_T}{R_T} - \frac{v}{R_T}$$

$$i = I_N - \frac{v}{R_N}$$

compare

* Hence, Thévenin and Norton equivalent circuits are equal to each other if:

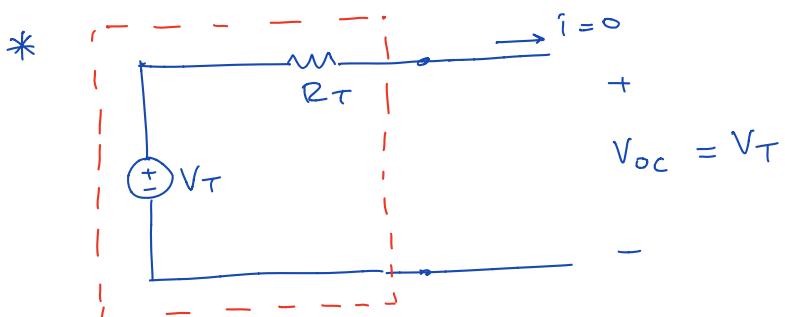
$$R_N = R_T \quad , \quad I_N = \frac{V_T}{R_T}$$



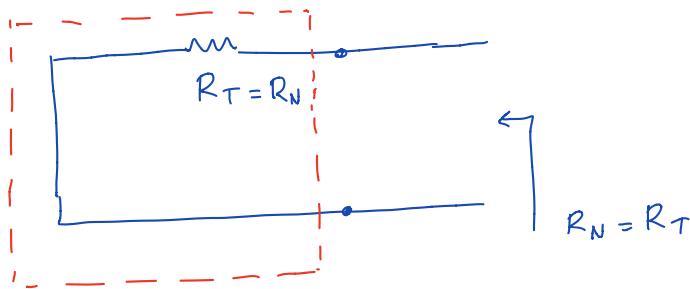
$$V = V_T - i \cdot R_T \quad (\text{Thévenin})$$

OR

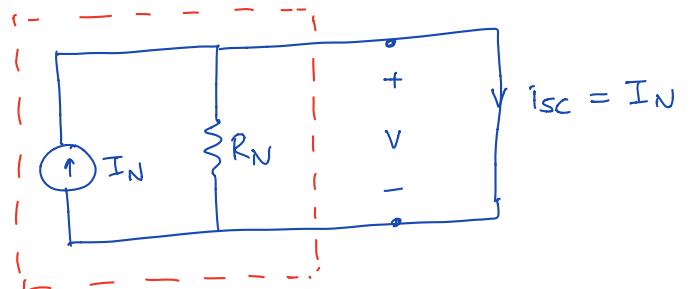
$$i = I_N - \frac{v}{R_N} \quad (\text{Norton})$$



(open circuit voltage is V_T)



Kill all sources inside,
find $R_T = R_N$



(short circuit current is I_N)

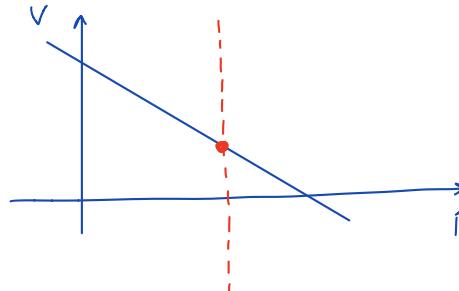
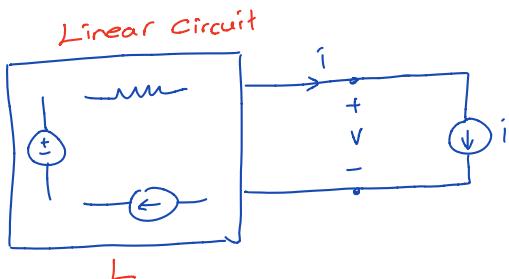
So,

$$V_T = V_{OC}$$

$$I_N = I_{SC}$$

$$R_N = R_T = \frac{V_{OC}}{I_{SC}} = \frac{V_T}{I_N}$$

Proof of Thévenin equivalent circuit:



* Replace N by a current source (this is called substitution).
This circuit is uniquely solvable.

Apply superposition:

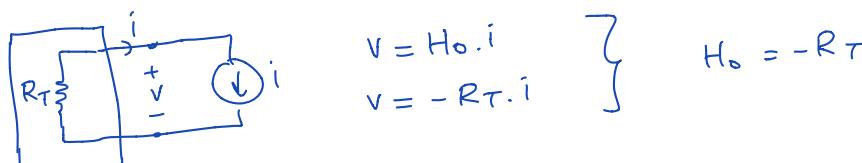
$$v = H_0 \cdot i + \underbrace{K_1 v_{S1} + \dots + K_n v_{Sn} + H_1 \cdot i_{S1} + \dots + H_m \cdot i_{Sm}}_{\text{contribution of sources inside } L}$$

Compare with: $v = -R_T \cdot i + V_T$ (Thévenin eq. circuit)

* $H_0 = -R_T$, $V_T = \text{contribution of sources inside } L$

* How to find R_T : Set $V_T = 0 \Rightarrow$ all sources inside L set to zero.

Then, $v = H_0 \cdot i \Rightarrow H_0 = -R_T = \frac{v}{i}$



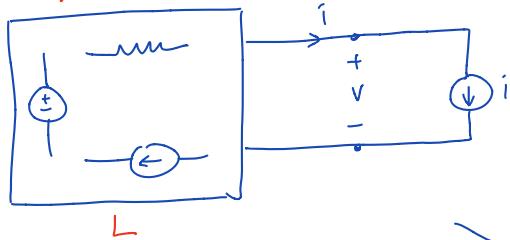
$$\left. \begin{array}{l} v = H_0 \cdot i \\ v = -R_T \cdot i \end{array} \right\} H_0 = -R_T$$

* How to find V_T : Set $i = 0 \Rightarrow$ set right hand side to OPEN CIRCUIT.

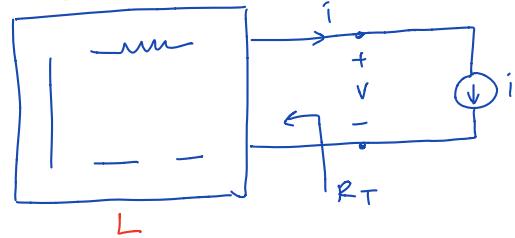
Then, $V_T = V_{OC}$

* Hence, Thévenin equivalent circuit is a direct consequence of superposition and unique solvability.

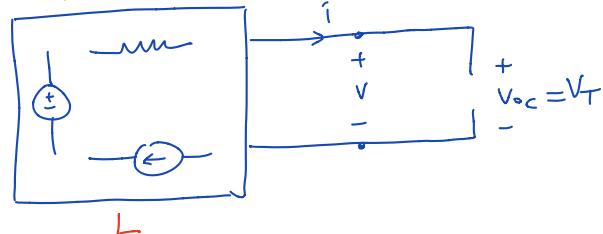
Linear Circuit



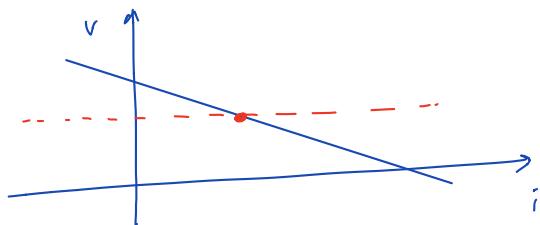
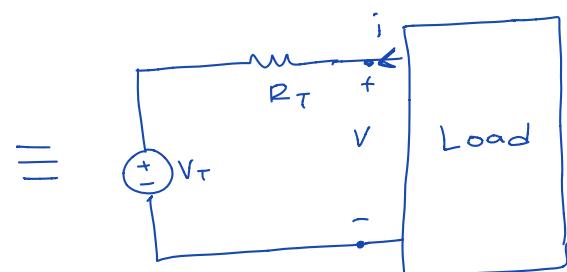
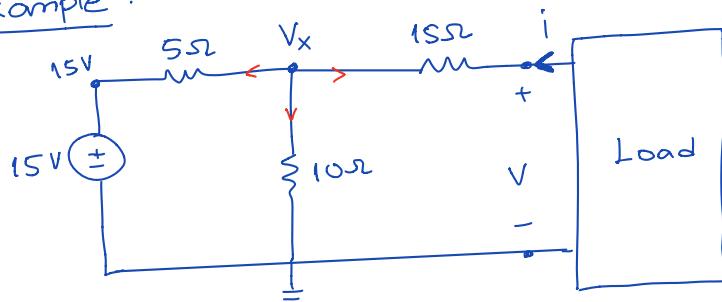
Linear Circuit



Linear Circuit



- * It can be shown similarly that Norton eq. circuit is also a direct consequence of superposition.
(connect a voltage source V to the right-hand side, in place of N).

Example :

$$V = R_T \cdot i + V_T$$

Direct way :

$$\text{KCL at } X : \frac{V_x - 15}{5} + \frac{V_x - V}{15} + \frac{V_x}{10} = 0$$

(1) (2) (3)

$$\text{if } V_x = 90 + 2V \Rightarrow V_x = \frac{2V + 90}{11}$$

$$* \text{Also: } V = 15 \cdot i + V_x$$

$$V = 15 \cdot i + \frac{2V + 90}{11}$$

$$gV = 165 \cdot i + 90$$

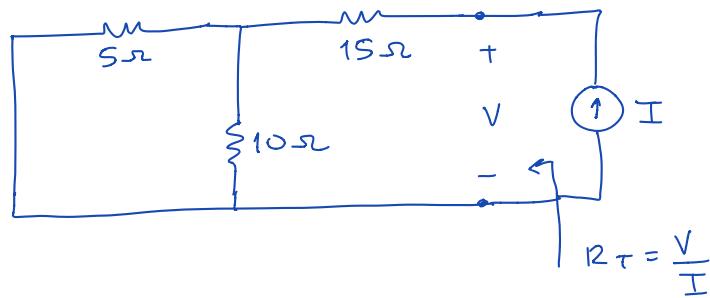
$$\boxed{V = 18.3 \cdot i + 10}$$

compare with $V = R_T \cdot i + V_T$

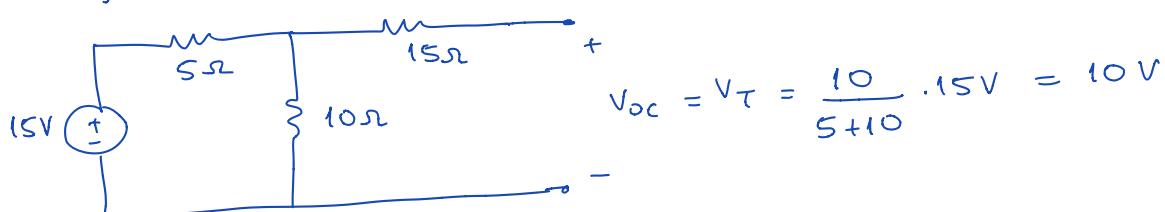
$$\Rightarrow \boxed{R_T = 18.3 \Omega, V_T = 10 V}$$

Indirect way:

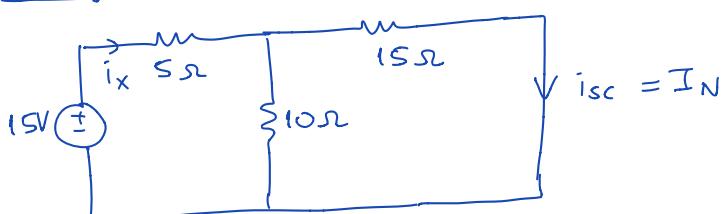
* To find R_T : Kill all sources.



* To find V_T : measure open circuit voltage

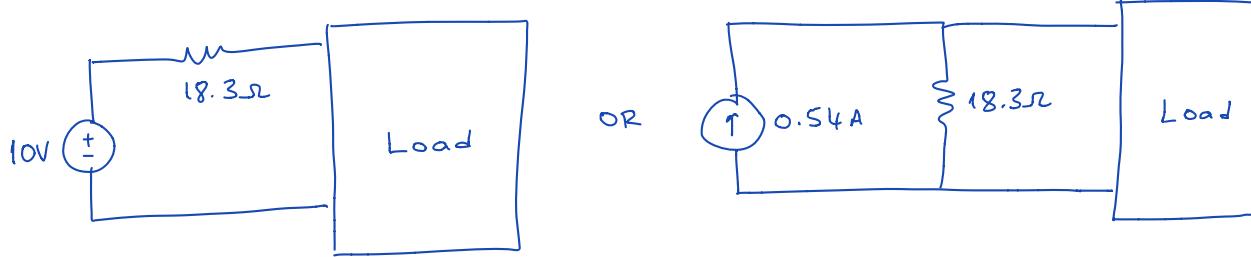


* To find I_N : measure short circuit current

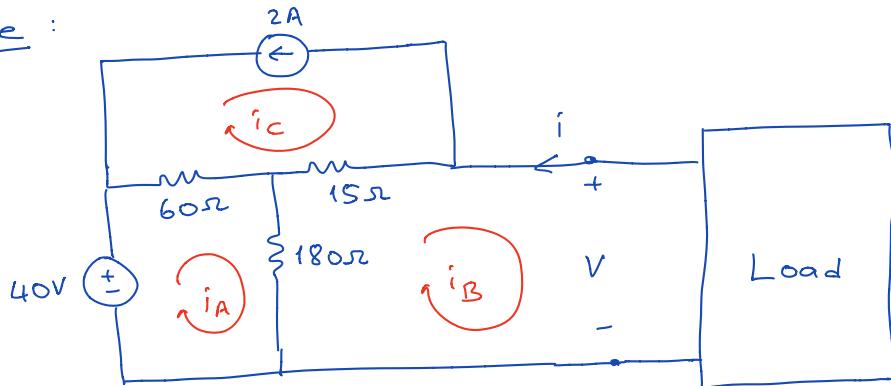


$$i_{sc} = I_N = \frac{10}{10+15} \cdot i_N = \frac{10}{25} \cdot \frac{15}{11} \approx 0.54 A$$

$$\text{Double check: } R_T = \frac{V_T}{I_N} = \frac{10V}{0.54A} \approx 18.3 \Omega$$



Example :



Direct Way :

$$i_c = -2 \text{ A}$$

$$i = -i_B$$

$$\text{mesh A} : 60(i_A - i_c) + 180(i_A - i_B) - 40 = 0$$

$$240i_A - 180i_B = 60i_c + 40 = -80 \quad \dots \text{(Eqn.1)}$$

$$\text{mesh B} : 15(i_B - i_c) + V + 180(i_B - i_A) = 0$$

$$-180i_A + 195i_B = 15i_c - V = -30 - V \quad \dots \text{(Eqn.2)}$$

$$3 \times \text{Eqn.1} : 720i_A - 540i_B = -240$$

$$4 \times \text{Eqn.2} : -720i_A + 780i_B = -120 - 4V$$

$$+ \\ 240i_B = -360 - 4V$$

$$i = -i_B \Rightarrow -240i = -360 - 4V$$

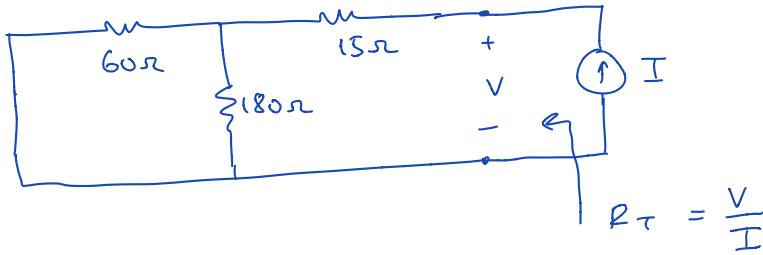
$$V = 60i - 90$$

$$\text{Compare with: } V = R_T \cdot i + V_T$$

$$R_T = 60\Omega, V_T = -90V$$

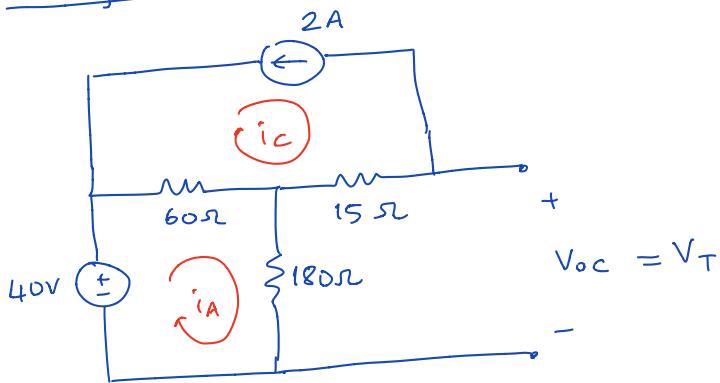
Indirect Way:

* To find R_T : Kill all sources



$$R_T = (60//180) + 15 = 45 + 15 = 60\Omega$$

* To find V_T : Open circuit voltage



$$i_c = -2A$$

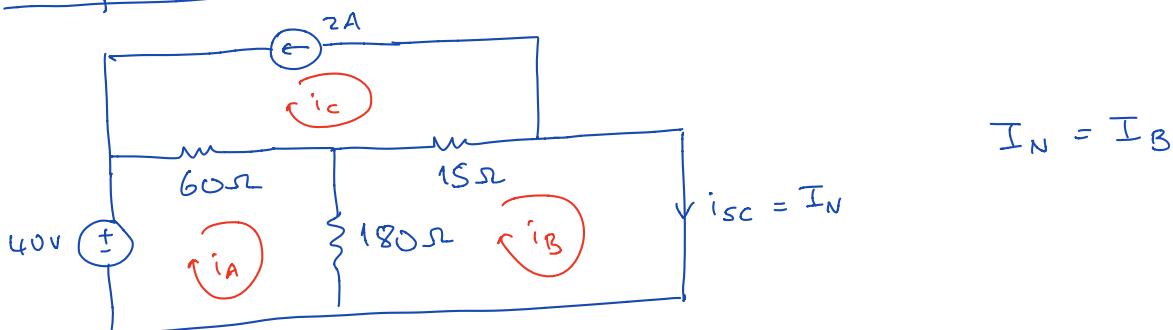
$$\text{mesh A : } 60(i_A - i_c) + 180 \cdot i_A - 40 = 0$$

$$240i_A = 60i_c + 40 = -80$$

$$i_A = -\frac{1}{3}A$$

$$\text{Then, } V_T = 15 \cdot i_c + 180 \cdot i_A = -30 - 60 = -90V$$

* To find I_N : short circuit current

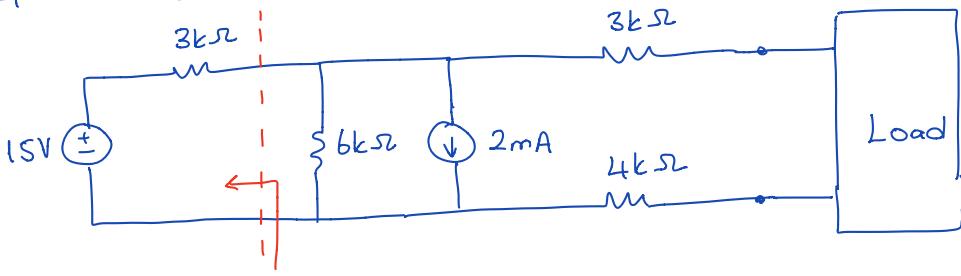


$$I_N = I_B$$

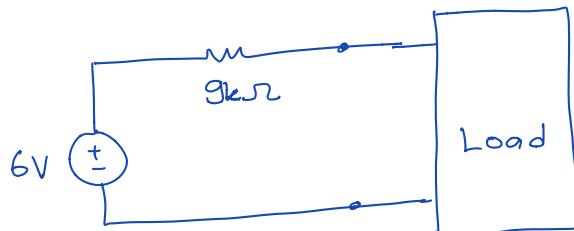
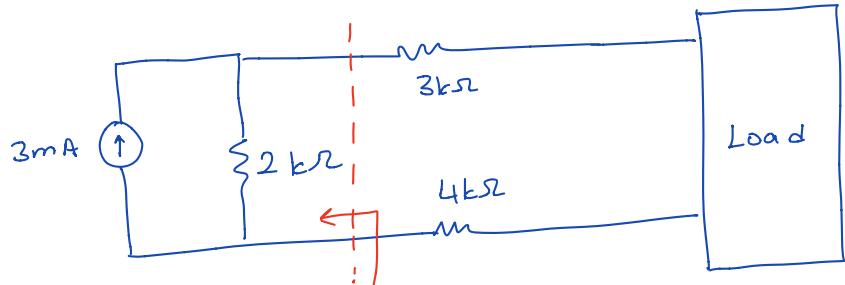
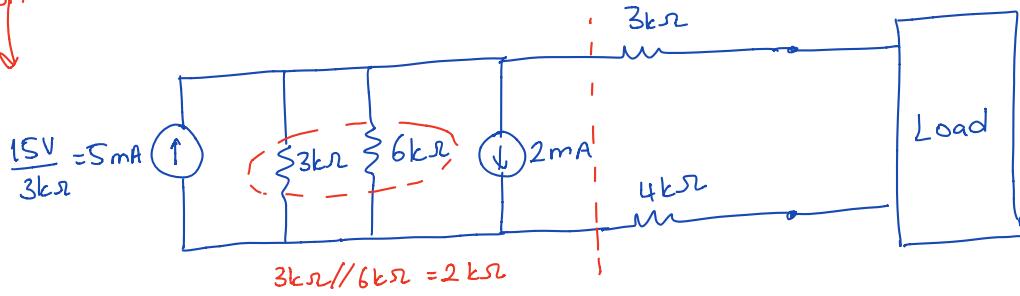
Do mesh analysis. Then, $I_N = I_B$.

$$(\text{exercise}). \quad I_N = \frac{V_T}{R_T} = \frac{-90}{60} = -1.5A$$

Example: By reduction



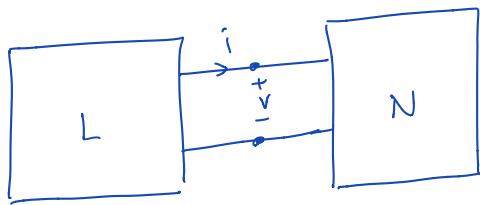
source transformation



$$V_T = 6V, R_T = 9k\Omega, I_N = \frac{6V}{9k\Omega} \approx 0.67 \text{ mA}$$

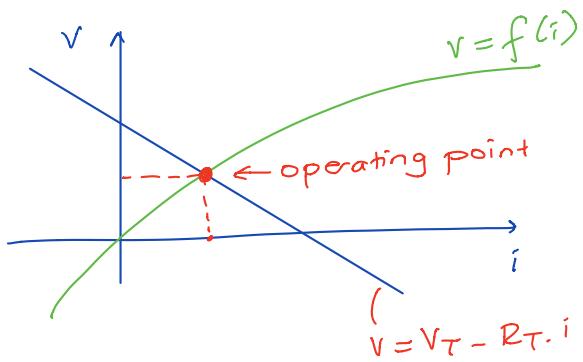
Typical Applications :

- 1) when N is a nonlinear device (e.g., a diode)



$$L : V = V_T - R_T \cdot i$$

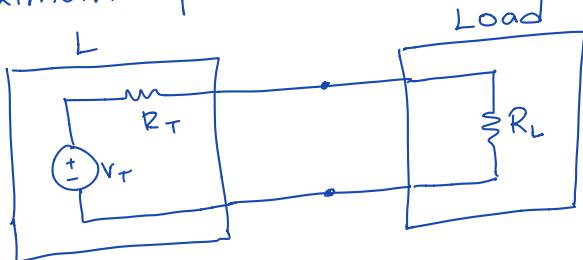
$$N : V = f(i)$$



nonlinear equation : finding the solution
may be difficult.

Thevenin / Norton equivalent circuits make it easier to
find this solution.

- 2) maximum power transfer



maximum power is transferred to the load when $R_L = R_T$.
⇒ called impedance matching