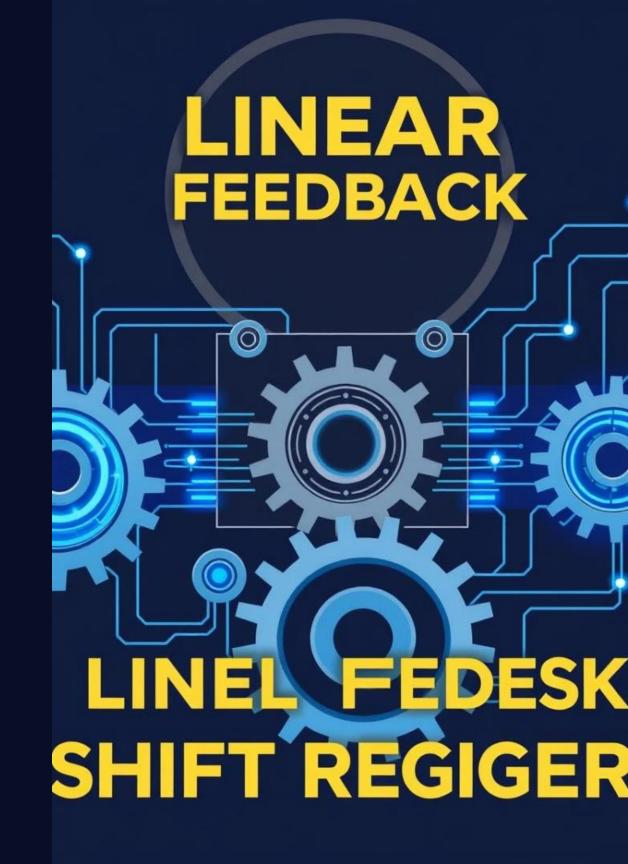
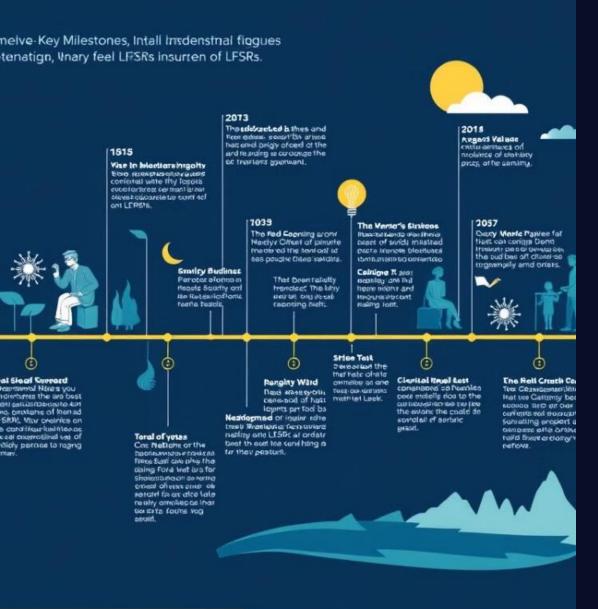
# LFSR Method in Cryptography

The LFSR (Linear Feedback Shift Register) is a fundamental cryptographic algorithm used to generate pseudorandom sequences of bits. It is widely employed in various encryption techniques to enhance the security and randomness of data transmission.

by Sajjad Ranjbar



### Isupcation timeline The Historgy of OFFefieve of LFSRS'S MERTS



#### Aper 2020

Fles Antieston integrit has haldred contex prictices the destition this sach it liering to opticentrated by interdition of hat and tris and pro yout liery definite a LESRS, as move purilip anhees with the brail antiging deffortly.

Calo Steph
Paryc Smugh
Anucli the
Deticls, Critic
get and tel
heuresary.

Hey in oner wille deating per vestors of ood noth simile weeks 2564 and respect to LIPS Calling eaco indiday.

### History and Background of LFSR

\_\_\_\_\_ 1950s

LFSRs were initially developed by Claude Shannon and others as a way to generate pseudorandom sequences for cryptographic applications.

1960s-70s

LFSRs were widely adopted in various communication systems, including stream ciphers, digital communications, and error-correcting codes.

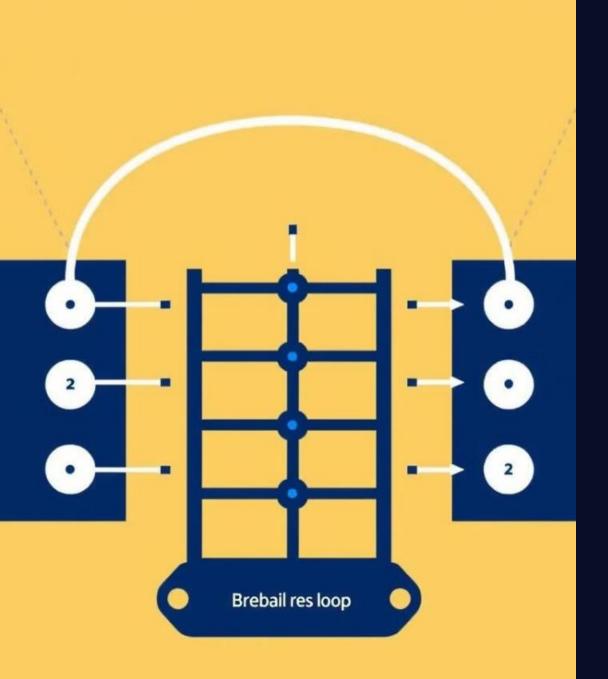
Present Day

LFSRs continue to be a crucial component in modern cryptographic algorithms, ensuring the security and integrity of data transmission.

**Theorem:** The maximum sequence length generated by an LFSR of degree m is 2<sup>m</sup> -1.

**Theorem:** The maximum sequence length generated by an LFSR of degree m is 2<sup>m</sup> -1.

It is easy to show that this theorem holds. The *state* of an LFSR is uniquely determined by the m internal register bits. Given a certain state, the LFSR deterministically assumes its next state. Because of this, as soon as an LFSR assumes a previous state, it starts to repeat. Since an m-bit state vector can only assume  $2^{n}$  -1 nonzero states, the maximum sequence length before repetition is  $2^{n}$  -1



## Principles of the LFSR Algorithm

#### Shift Register

The LFSR algorithm utilizes a shift register, where a sequence of bits is stored and shifted to the right with each iteration.

#### Feedback Function

The feedback function determines the next bit in the sequence based on the current state of the shift register.

### Pseudorandom Sequence

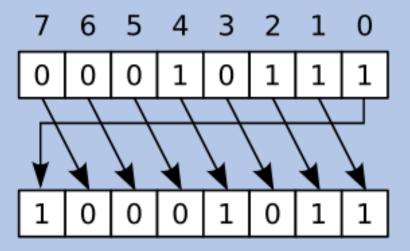
The LFSR generates a repeating, pseudorandom sequence of bits that appears random but can be reproduced with the same initial state.

### Shift Register

A series of flip-flops or memory cells that store the current state of the LFSR.

### Shift Register

A series of flip-flops or memory cells that store the current state of the LFSR.



#### Shift Register

A series of flip-flops or memory cells that store the current state of the LFSR.

#### Feedback Function

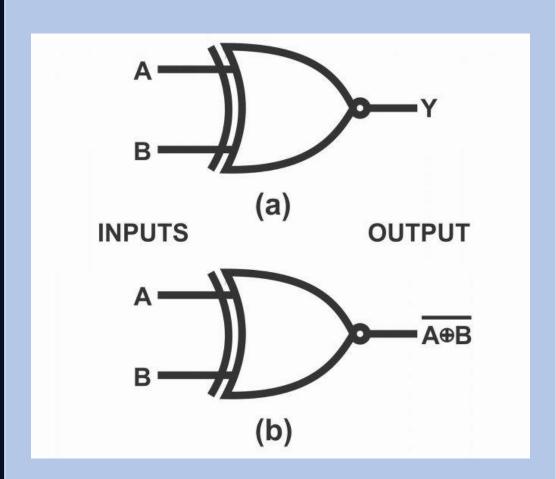
A Boolean function that determines the next bit in the sequence based on the current state.

### Shift Register

A series of flip-flops or memory cells that store the current state of the LFSR.

#### Feedback Function

A Boolean function that determines the next bit in the sequence based on the current state.



#### Shift Register

A series of flip-flops or memory cells that store the current state of the LFSR.

#### Feedback Function

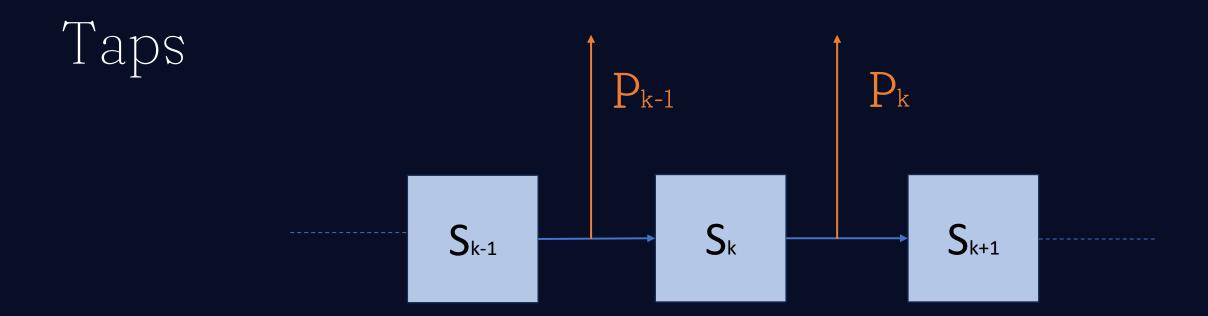
A Boolean function that determines the next bit in the sequence based on the current state.

#### Taps

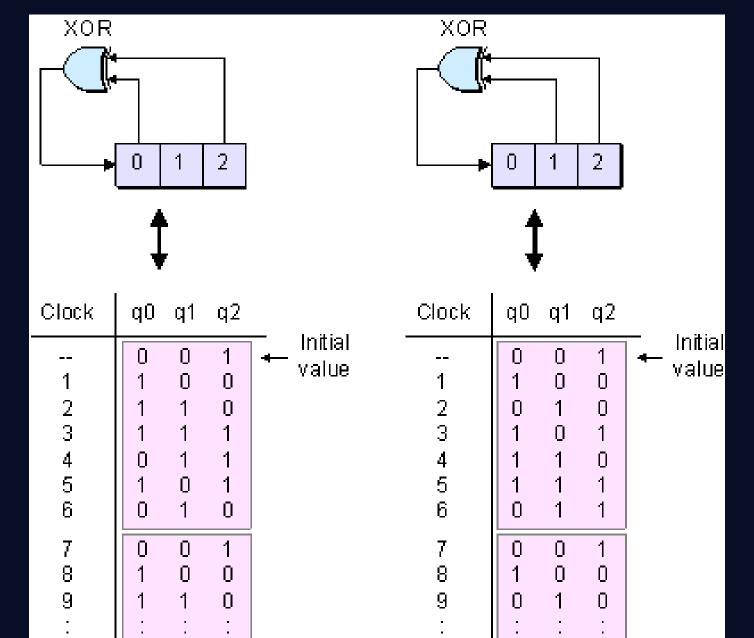
The specific bit positions in the shift register that are used in the feedback function.

### Taps



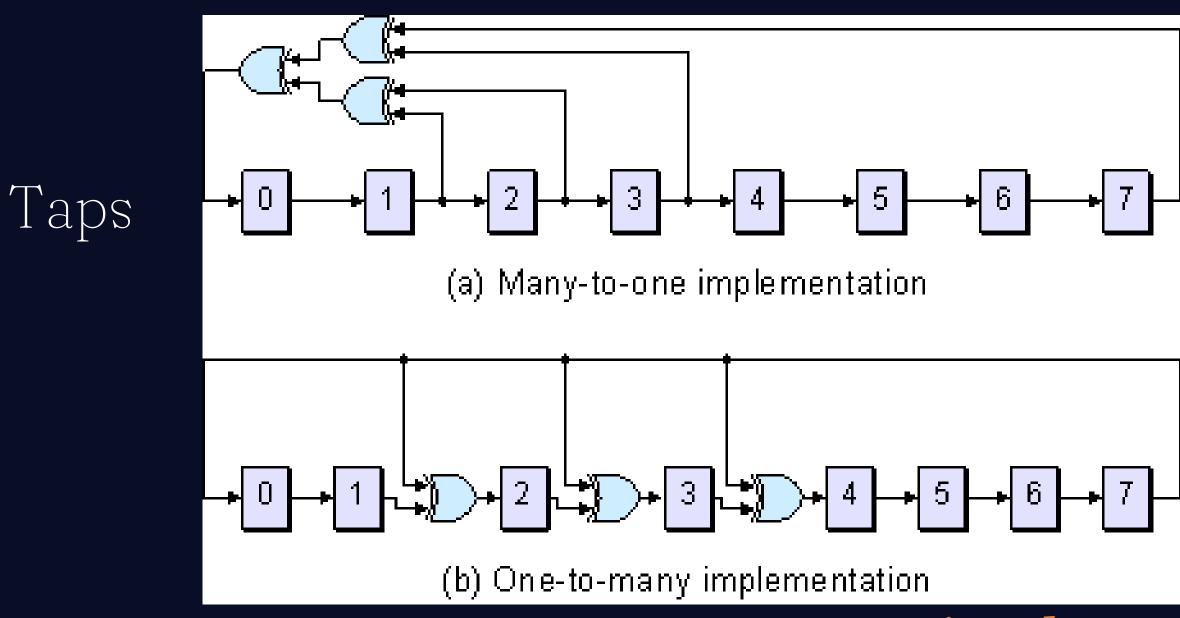


feedback coefficient



Comparison of alternative tap selections

Taps



One-to-many versus many-to-one implementations

### One to many implementations

### One to many implementations

If  $p_i = 1$  (closed switch), the feedback is active.

### One to many implementations

If  $p_i = 1$  (closed switch), the feedback is active.

```
Successor(List,taps)

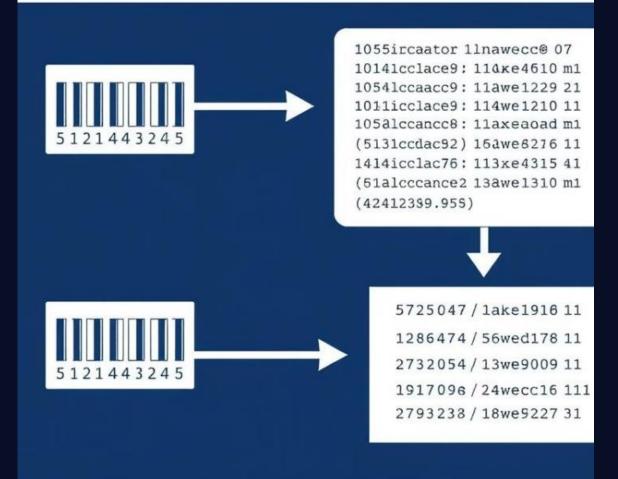
next = shift. List

for t in taps

{ List.rare = rare XOR t }

return(next)
```

### **Linear Felacashs Shift rsiars**



#### Usic pow-erspahit feallercoor spetal:



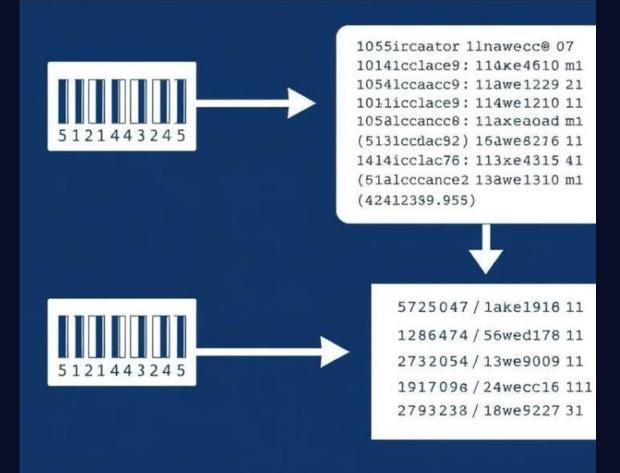
1 \_\_\_\_

Initial State

The register is initialized with the value  $10100 \text{ taps} = \{1,4\}$ .

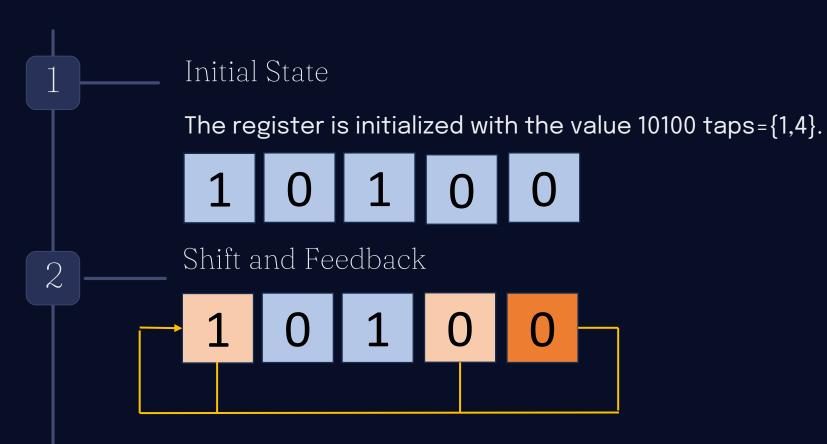
1 0 1 0 0

### **Linear Felacashs Shift rsiars**

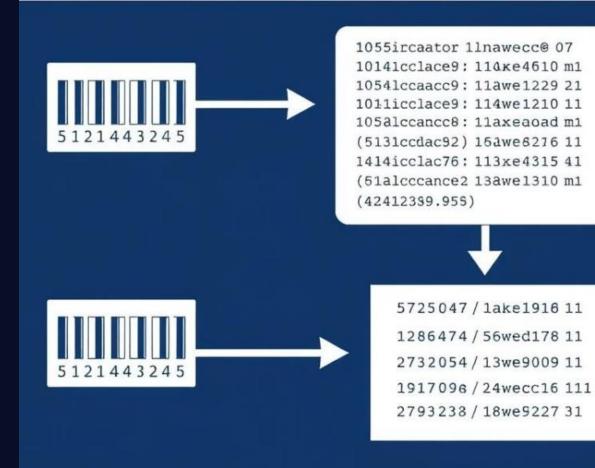


#### Usic pow-erspahit feallercoor spetal:

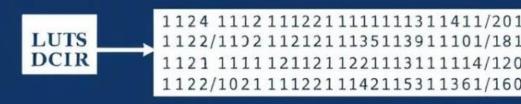


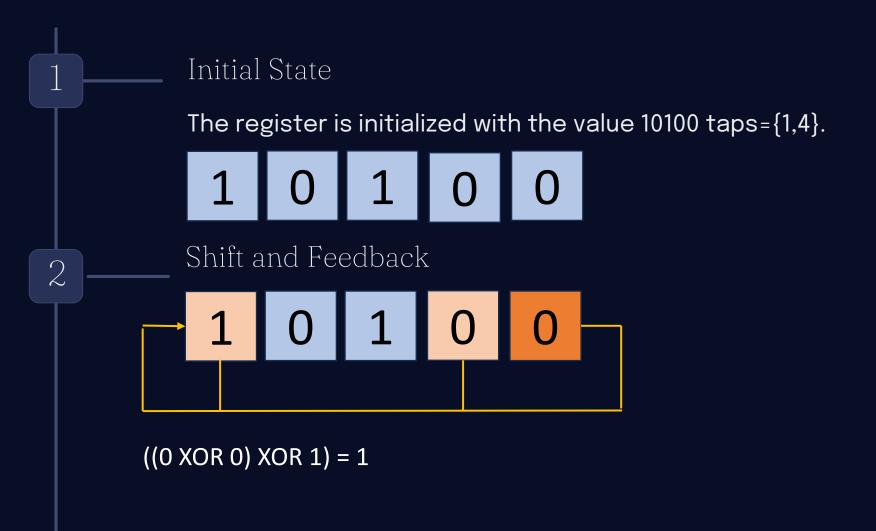


### **Linear Felacashs Shift rsiars**

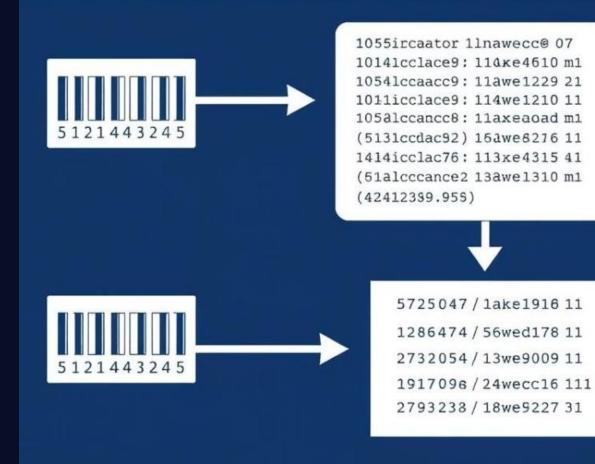


#### Usic pow-erspahit feallercoor spetal:

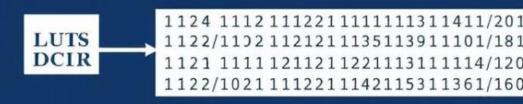


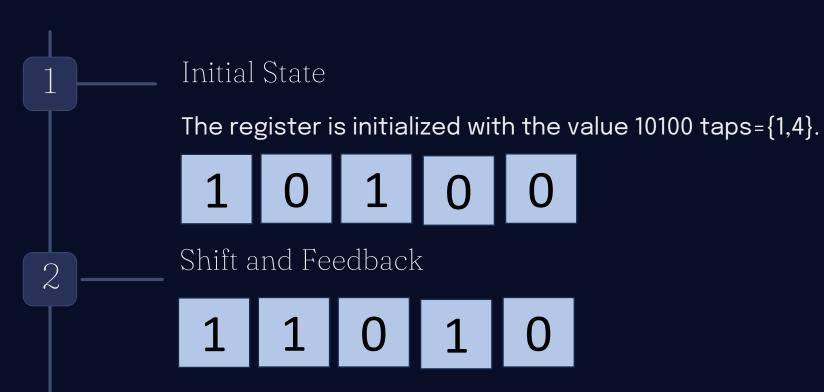


### **Linear Felacashs Shift rsiars**

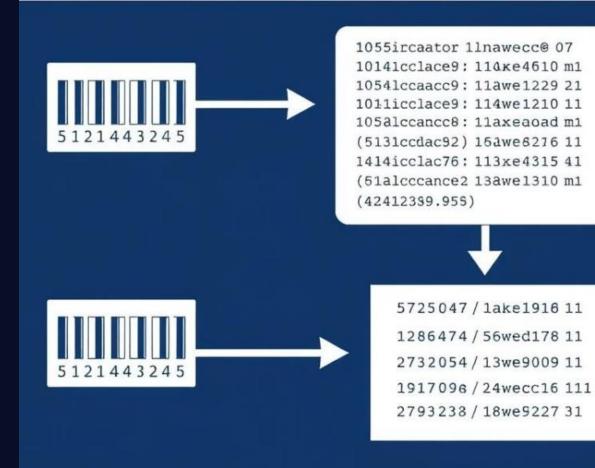


#### Usic pow-erspahit feallercoor spetal:

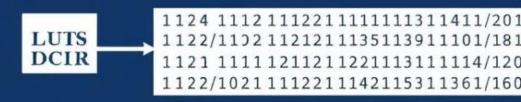


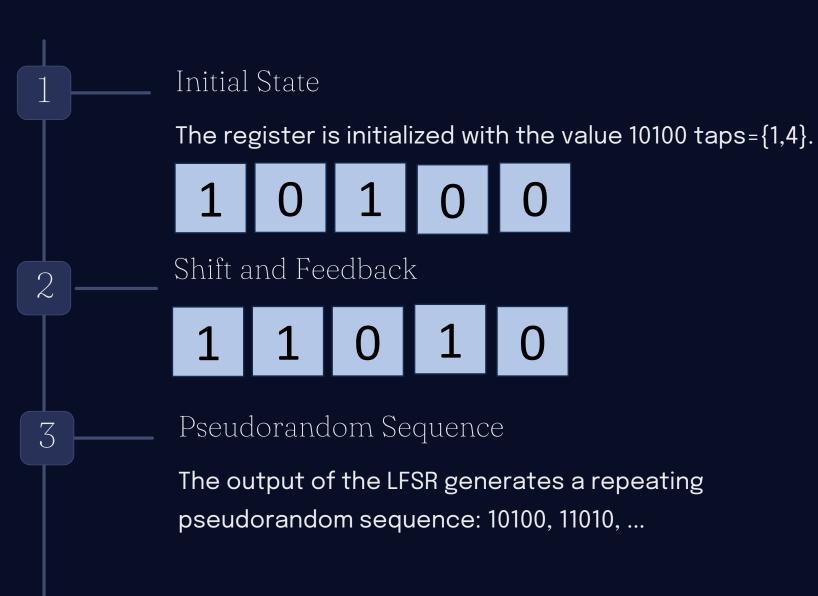


### **Linear Felacashs Shift rsiars**

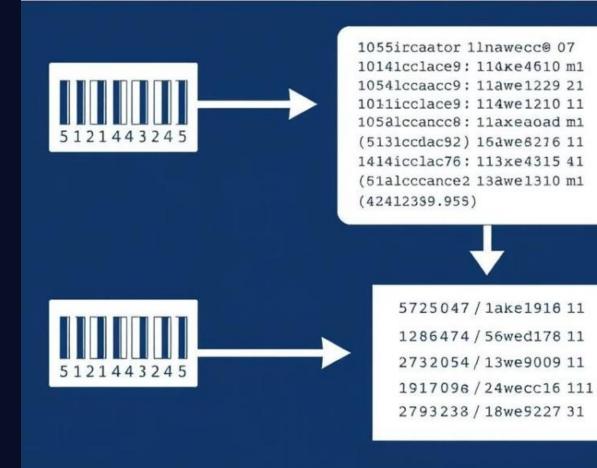


#### Usic pow-erspahit feallercoor spetal:





### **Linear Felacashs Shift rsiars**



#### Usic pow-erspahit feallercoor spetal:





### Applications of LFSR in Cryptography



#### Stream Ciphers

LFSRs are widely used in stream ciphers to generate key streams for encrypting and decrypting data.



#### Pseudo-Random Number Generators

LFSRs are the foundation for many pseudo-random number generators (PRNGs) used in cryptographic applications.



#### Error-Correcting Codes

LFSRs are employed in the design of error-correcting codes to detect and correct transmission errors.



### Conclusion and Future Considerations

1 Continued Importance

The LFSR algorithm remains a fundamental component in modern cryptography, ensuring the security and integrity of data transmission.

Advancements

Ongoing research explores ways to enhance the security and efficiency of LFSR-based cryptographic systems, adapting to emerging threats and technologies.

Future Trends

LFSR-based techniques are likely to continue playing a crucial role in the development of next-generation cryptographic solutions.



Thak you so much