

ICS Answer Sheet #8

Sakar Gopal Gurubacharya
s.gurubacharya@jacobs-university.de

Problem 8.1: combinational digital circuit

- a. What is the Boolean expression implemented by the digital circuit?

We have 3 variables (input) and a single value (output) with many gates in between them (process).

$$\neg (\neg (\neg A \wedge \neg B) \wedge \neg (\neg (A \wedge B) \wedge C))$$

- b. Derive algebraically step-by-step the disjunction (sum) of minterms from the Boolean expression implemented by the digital circuit.

$\neg (\neg (\neg A \wedge \neg B) \wedge \neg (\neg (A \wedge B) \wedge C))$	- 1a
$\neg (\neg \neg (A \vee B) \wedge \neg ((\neg A \vee \neg B) \wedge C))$	- de Morgan's Law (2x)
$\neg ((A \vee B) \wedge \neg ((\neg A \vee \neg B) \wedge C))$	- Double Negation Law
$\neg ((A \vee B) \wedge (\neg (\neg A \vee \neg B) \vee \neg C))$	- de Morgan's Law
$\neg ((A \vee B) \wedge (\neg \neg (A \wedge B) \vee \neg C))$	- de Morgan's Law
$\neg ((A \vee B) \wedge ((A \wedge B) \vee \neg C))$	- Double Negation Law
$\neg (A \vee B) \vee \neg ((A \wedge B) \vee \neg C)$	- de Morgan's Law
$(\neg A \wedge \neg B) \vee (\neg (A \wedge B) \wedge \neg \neg C)$	- de Morgan's Law
$(\neg A \wedge \neg B) \vee (\neg (A \wedge B) \wedge C)$	- Double Negation Law
$(\neg A \wedge \neg B) \vee ((\neg A \wedge C) \vee (\neg B \wedge C))$	- de Morgan's and Distributive Law
$(\neg A \wedge \neg B) \vee (\neg A \wedge C) \vee (\neg B \wedge C)$	- Removing unnecessary brackets.

A	B	C	$(\neg A \wedge \neg B)$	$(\neg A \wedge C)$	$(\neg B \wedge C)$	$(\neg A \wedge \neg B) \vee (\neg A \wedge C) \vee (\neg B \wedge C)$
0	0	0	1	0	0	1
0	0	1	1	1	1	1
0	1	0	0	0	0	0
0	1	1	0	1	0	1
1	0	0	0	0	0	0
1	0	1	0	0	1	1
1	1	0	0	0	0	0
1	1	1	0	0	0	0

Only taking the terms which results in 1, we get:

$$(\neg A \wedge \neg B \wedge \neg C) \vee (\neg A \wedge \neg B \wedge C) \vee (\neg A \wedge B \wedge C) \vee (A \wedge \neg B \wedge C)$$

$$m_0 + m_1 + m_3 + m_5,$$

Therefore, sum of minterms: **$m_0 + m_1 + m_3 + m_5$** ,

Problem 8.2: full adder using different kinds of gates

$$S = (A \vee B) \vee C_{in}$$

$$C_{out} = (A \wedge B) \vee (C_{in} \wedge (A \vee B))$$

Truth Tables

S

A	B	C	$A \vee B$	$S = (A \vee B) \vee C_{in}$
0	0	0	0	0
0	0	1	0	1
0	1	0	1	1
0	1	1	1	0
1	0	0	1	1
1	0	1	1	0
1	1	0	0	0
1	1	1	0	1

C_{out}

A	B	C	$A \wedge B$	$A \vee B$	$(C_{in} \wedge (A \vee B))$	$C_{out} = (A \wedge B) \vee (C_{in} \wedge (A \vee B))$
0	0	0	0	0	0	0
0	0	1	0	0	0	0
0	1	0	0	1	0	0
0	1	1	0	1	1	1
1	0	0	0	1	0	0
1	0	1	0	1	1	1
1	1	0	1	0	0	1
1	1	1	1	0	0	1

a. Write both functions as a disjunction of product terms.

Disjunction of product terms means **sum of product terms**

Sum = OR

Product = AND

i.e., different **AND** expressions joined with **OR**
which is also called **DNF form**.

For DNF, we only take the interpretations that satisfies S and C_{out} , i.e., **result is 1**.

i. For S, that would be: $m_1 \vee m_2 \vee m_4 \vee m_7$.

$$S = (\neg A \wedge \neg B \wedge C) \vee (\neg A \wedge B \wedge \neg C) \vee (A \wedge \neg B \wedge \neg C) \vee (A \wedge B \wedge C)$$

ii. For C_{out} , that would be: $m_3 \vee m_5 \vee m_6 \vee m_7$.

$$C_{out} = (\neg A \wedge B \wedge C) \vee (A \wedge \neg B \wedge C) \vee (A \wedge B \wedge \neg C) \vee (A \wedge B \wedge C)$$

b. Write both functions as a conjunction of sum terms.

Conjunction of sum terms means **product of sum terms**

Sum = OR

Product = AND

i.e., different **OR** expressions joined with **AND**
which is also called **CNF form**.

For CNF, we only take the interpretations that dissatisfies S and C_{out} , i.e., **result is 0**.

i. For S, that would be: $m_0 \wedge m_3 \wedge m_5 \wedge m_6$.

$$S = (\neg A \vee \neg B \vee \neg C) \wedge (\neg A \vee B \vee C) \wedge (A \vee \neg B \vee C) \wedge (A \vee B \vee \neg C)$$

iii. For C_{out} , that would be: $m_0 \wedge m_1 \wedge m_2 \wedge m_4$.

$$C_{out} = (\neg A \vee \neg B \vee \neg C) \wedge (\neg A \vee \neg B \vee C) \wedge (\neg A \vee B \vee \neg C) \wedge (A \vee \neg B \vee \neg C)$$

c. Write both functions using only not (\neg) and not-and (\uparrow) operations.

i. For S,

$$S = (A \vee B) \vee C_{in}$$

For $(A \vee B)$,

$$\begin{aligned} A \vee B &= (A \vee B) \wedge (\neg A \vee \neg B) \\ &= (A \vee B) \wedge \neg (A \wedge B) \end{aligned}$$

We know that,

$$A \uparrow B = \neg (A \wedge B)$$

So,

$$(A \vee B) = ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B))) \quad (\text{From StackExchange})$$

Then,

$$\begin{aligned} S &= (A \vee B) \vee C_{in} \\ &= ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B))) \vee C_{in} \\ &= (((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B))) \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B))) \uparrow C_{in}) \uparrow \\ &\quad (C_{in} \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B))) \uparrow C_{in})) \end{aligned}$$

Therefore,

$$S = (((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B))) \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B))) \uparrow C_{in}) \uparrow (C_{in} \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B))) \uparrow C_{in}))$$

ii. **For C_{out} ,**

$$C_{out} = (A \wedge B) \vee (C_{in} \wedge (A \vee B))$$

- **For $(A \vee B)$,**

$$A \vee B = (A \vee B) \wedge (\neg A \vee \neg B)$$

$$= (A \vee B) \wedge \neg (A \wedge B)$$

We know that,

$$A \uparrow B = \neg (A \wedge B)$$

So,

$$(A \vee B) = ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B))) \quad (\text{From StackExchange})$$

- **For $(A \wedge B)$**

$$(A \wedge B) = ((A \uparrow B) \uparrow (A \uparrow B)) \quad (\text{From Wikipedia})$$

- **For $(A \vee B)$**

$$(A \vee B) = ((A \uparrow A) \uparrow (B \uparrow B)) \quad (\text{From Wikipedia})$$

Now, all operators in C_{out} expression are covered for with NAND gates, we can now express the entire expression with just NAND gates.

$$C_{out} = (A \wedge B) \vee (C_{in} \wedge (A \vee B))$$

$$= (((A \uparrow B) \uparrow (A \uparrow B)) \vee (C_{in} \wedge ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B)))))$$

For now, dealing with the expression on the **right side** of the **OR** operator.

So, we have:

$$= (C_{in} \wedge ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B))))$$

$$= (((C_{in} \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B)))) \uparrow (C_{in} \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B)))))$$

Now, finally combining all and changing the **OR** operator to **NAND**.

So, we have:

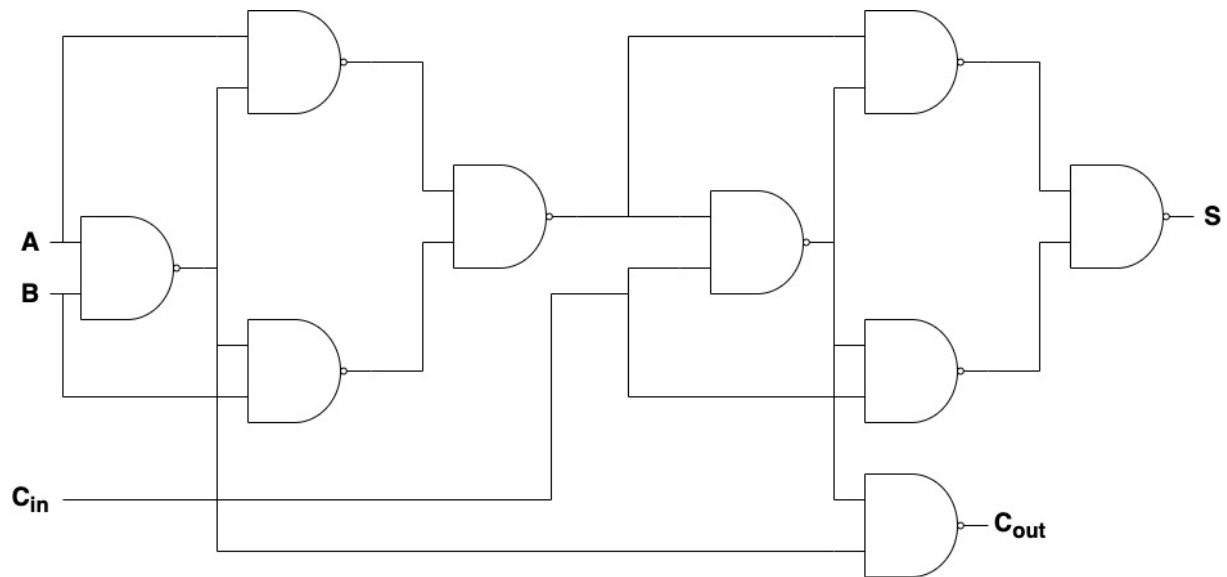
$$= (((A \uparrow B) \uparrow (A \uparrow B)) \vee (((C_{in} \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B)))) \uparrow (C_{in} \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B)))))$$

$$= (((((A \uparrow B) \uparrow (A \uparrow B)) \uparrow ((A \uparrow B) \uparrow (A \uparrow B))) \uparrow (((C_{in} \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B)))) \uparrow (C_{in} \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B))))) \uparrow (((C_{in} \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B)))) \uparrow (C_{in} \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B)))))$$

Therefore,

$$C_{out} = (((((A \uparrow B) \uparrow (A \uparrow B)) \uparrow ((A \uparrow B) \uparrow (A \uparrow B))) \uparrow (((C_{in} \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B)))) \uparrow (C_{in} \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B))))) \uparrow (((C_{in} \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B)))) \uparrow (C_{in} \uparrow ((A \uparrow (A \uparrow B)) \uparrow (B \uparrow (A \uparrow B)))))$$

- d. In a digital circuit, we can easily reuse common terms. Draw a small digital circuit implementing S and C_{out} using NAND gates only.



(Gates Circuit made from draw.io)

Problem 8.3: haskell fizzbuzz

- Write a Haskell function `fizzbuzz :: Integer -> String` that takes a positive integer and returns the number rendered as a string or one of the strings "fizz", "buzz", or "fizzbuzz", following the rules defined above.
- Using `foldr`, write a simple expression that returns the `fizzbuzz` sequence as a list of strings for the numbers in the range 1 to 16. Do not use list comprehensions or other higher order functions or lambda functions.
- Using `foldl`, write a simple expression that returns the `fizzbuzz` sequence as a list of strings for the numbers in the range 1 to 16. Do not use list comprehensions or other higher order functions (and ideally no lambda functions but you may use `flip`).

(fizzbuzz.txt present in the same .zip file)

```
fizzbuzz.hs — Assignment 8

>> fizzbuzz.hs ×
>> fizzbuzz.hs
1  {-
2  Having fizzbuzz as the first condition is necessary,
3  otherwise conditions before it would be satisfied before and end that "iteration"
4  -}
5
6  fizzbuzz :: Int -> String
7  fizzbuzz n =
8
9      if mod n 3 == 0 && mod n 5 == 0 then
10         "fizzbuzz"
11
12     else if mod n 3 == 0 then
13         "fizz"
14
15     else if mod n 5 == 0 then
16         "buzz"
17
18     else
19         show n
20
21  --using foldr.
22  map' f = foldr ((:) . f) [] [1..16]
23  --type "map' fizzbuzz" in ghci
24
25  --using foldl.
26  map'' f = foldl (flip ((:) . f)) [] [16,15,14,13,12,11,10,9,8,7,6,5,4,3,2,1]
27  --type "map'' fizzbuzz" in ghci
```

References

1. *XOR in NAND taken from:* <https://math.stackexchange.com/questions/38473/is-xor-a-combination-of-and-and-not-operators>
2. *Every gate in NAND gates taken from:* https://en.wikipedia.org/wiki/NAND_logic
3. *Digital circuit made from:* <https://app.diagrams.net/>