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Tokamak Magnetic Control Simulation: Applications for JT60-SA and ISTTOK Operation.

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ABSTRACT

Abstract en ingles

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RESUMO

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Parole chiave:

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CONTENTS

1	INTRODUCTION	3
1.1	Tokamak plasma control	3
1.2	Behind the plasma current	3
1.3	Thesis outline	3
2	PLASMA CONTROL SYSTEMS	5
2.1	Overview of control systems	5
2.1.1	DIII-D Plasma Control System	5
2.2	MARTe framework	5
2.2.1	MARTe architecture	6
2.2.2	Hardware containers	6
2.2.3	MARTe 2.0	6
2.3	Equilibrium and control algorithms	6
2.3.1	PID control	6
2.3.2	Multiple-Input Multiple-Output control	6
3	JT60-SA CONTROL DESIGN	7
3.1	Machine description	7
3.2	CREATE tools	7
3.3	Controller designs	7
3.4	QST tools implementation	7
3.5	Simulation results	7
4	ISTTOK	9
4.1	Machine description	9
4.2	Diagnostics and Actuators	9
4.3	ATCA-MIMO-ISOL boards	9
4.3.1	Hardware layout	9
4.3.2	Real-time integration software	9
4.4	Retrieving the contribution of plasma current	9
4.5	Plasma centroid position determination	9
5	ISTTOK RESULTS	11
5.1	General Application Modules implementations	11
5.2	PID control implementation	11
5.3	Multiple-Input Multiple-Output control implementation	11

Contents

6 CONCLUSIONS 13

Bibliography 15

A DEMONSTRATIONS 17

LIST OF FIGURES

Figure 2.1 DIII-D digital PCS in 1991 [3]. 6

LIST OF TABLES

LIST OF ABBREVIATIONS

@TODO: Review variable lists as writing the thesis

- AC - Alternating Current
- ADC - Analog to Digital Converter
- ATCA - Advanced Telecommunications Computing Architecture
- CREATE - Consorzio di Ricerca per l'Energia, l'Automazione e le Tecnologie dell'Elettromagnetismo
- DAC - Digital to Analog Converter
- EO - Electronic Offset
- IST - Istituto Superior Técnico
- LQR - Linear Quadratic Regulator
- MARTe - Multi-threaded Application Real-Time executor
- MIMO - Multiple-Input Multiple-Output
- PCS - Plasma Control System
- PF - Poloidal Field
- PID - Proportional - Integrative - Derivative
- XSC - eXtreme Shape Controller
- WO - Wiring Offset

LIST OF VARIABLES

@TODO: Review variable lists as writing the thesis

VARIABLES:

- I_p - Plasma current
- B_p - Poloidal magnetic field
- μ_0 - Vacuum permeability

INTRODUCTION

1.1 TOKAMAK PLASMA CONTROL

1.2 BEHIND THE PLASMA CURRENT

1.3 THESIS OUTLINE

PLASMA CONTROL SYSTEMS

2.1 OVERVIEW OF CONTROL SYSTEMS

The control of plasma position, shape and current among other parameters is one of the crucial engineering problems for present and future magnetic confinement devices. The Plasma Control Systems (PCS) lead with the overall control of fusion devices being responsible also for the plasma configuration and scenarios algorithms [1, Chapter 8]. Currently different PCS's are use in the tokamaks around the world. In this chapter the "DIII-D-like" PCS and the Multi-threaded Application Real-Time executor (MARTE) will be approach, this last one being of special interest due to its extensive utilization in this work.

2.1.1 *DIII-D Plasma Control System*

Early documentation regarding the PCS in DIII-D¹ reefers to digitalization of analog signals transmitted to a high speed processor executing a shape control algorithm and then writing the result to a digital to analog converter for driving the controlled systems . The real-time computer used allowed to performed operations with vectors and matrices required for the plasma shape control algorithm [3]. Figure 2.1 shows the block diagram of the DIII-D PCS 30 years ago.

2.2 MARTE FRAMEWORK

MARTE was developed in order to standardize general real-time control systems for the execution of control algorithms. MARTE framework is based on a multiplatform C++ library. [2]

¹ DIII-D is a D-shape tokamak operated by General Atomics in San Diego, California.

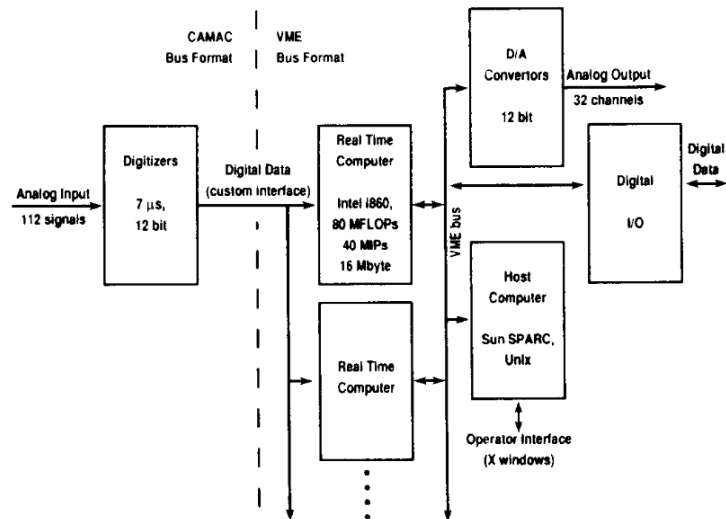


Figure 2.1.: DIII-D digital PCS in 1991 [3].

2.2.1 *MARTe architecture*

2.2.2 *Hardware containers*

2.2.3 *MARTe 2.0*

2.3 EQUILIBRIUM AND CONTROL ALGORITHMS

2.3.1 *PID control*

2.3.2 *Multiple-Input Multiple-Output control*

JT60-SA CONTROL DESIGN

3.1 MACHINE DESCRIPTION

3.2 CREATE TOOLS

3.3 CONTROLLER DESIGNS

3.4 QST TOOLS IMPLEMENTATION

3.5 SIMULATION RESULTS

ISTTOK

4.1 MACHINE DESCRIPTION

4.2 DIAGNOSTICS AND ACTUATORS

4.3 ATCA-MIMO-ISOL BOARDS

4.3.1 *Hardware layout*

4.3.2 *Real-time integration software*

4.4 RETRIEVING THE CONTRIBUTION OF PLASMA CURRENT

The methods of correction of the magnetic error fields due to inaccuracies of tokamak manufacturing and assembly are considered. The problems of the plasma position and shape reconstruction based on magnetic field measurements are discussed.

4.5 PLASMA CENTROID POSITION DETERMINATION

ISTTOK RESULTS

5.1 GENERAL APPLICATION MODULES IMPLEMENTATIONS

5.2 PID CONTROL IMPLEMENTATION

5.3 MULTIPLE-INPUT MULTIPLE-OUTPUT CONTROL IMPLEMENTATION

6

CONCLUSIONS

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DEMONSTRATIONS
