FACULTY PROFILE FORMAT (Format 3)		
Staff Name	:	MEENAKSHI S
Faculty ID	:	
Photo		
scopus id		-
publon id		NMK-5290-2025
google scholar id		-
ORCiD id		https://orcid.org/0009-0004-3892-787X
Designation	:	ASSISTANT PROFESSOR
Qualification	:	M.E.,
Teaching Experience	:	6 YEARS 9 MONTHS
Area of Specialization	:	VLSI DESIGN
Subjects Handled	:	VLSI AND CHIP DESIGN EMBEDDED SYSTEMS AND IOT DESIGN COMMUNICATION SYSTEMS DIGITAL SYSTEMS DESIGN WIRELESS COMMUNICATION COMPUTER ARCHITECTURE AND ORGANIZATION LINEAR INTEGRATED CIRCUITS DIGITAL PRINCIPLES AND COMPUTER ORGANIZATION INDUSTRIAL SAFETY TRANSMISSION LINES AND WAVEGUIDES
Journals Published	:	1.Published paper on **"Fuzzy based Drug Target Interfacing in Wireless Adhoc Molecular Network using Reliable Multicast Routing"**, IJRASET(International Journal for Research in Applied Science & Engineering Technology) Volume 6,Issue II, February 2018, ISSN: 2321-9653, Impact Factor:6.887.
Books/Book chapters published		-
FDP/ Conference /Workshop Attended	:	**CONFERENCE ATTENDED:** 1. Participated and **Won the Best paper Award** entitled '**Efficient VLSI Architecture for Montgomery Modular Multiplier'** in **"International Conference on Emerging Trends in Engineering, Technology, Science and Management"** at Adithya Institute of Technology, Coimbatore. 2. Participated and presented the paper entitled '**Forest Fire monitoring robot based on Zigbee Wireless Sensor Networks'**

in the second national conference on **"Recent Advances in Science, Engineering and Technologies-RASET 2015"** at Jay Shriram group of Institutions, Tirupur.

FDP ATTENDED:
1.Participated in the one day **Cognizant** FDP on "**Robotic Process Automation - UiPath**" at **KCT Tech Park**, Coimbatore.

2. Participated in the three days **AICTE-FDP** on "**Student Induction Programme**" at **PSG Institute of Technology and Applied Research,** Coimbatore.

3.Attended **Nan Mudhalvan** – FDP on "**Embedded System design for IoT application using Raspberry Pi**" at **TANSAM – TIDEL.**

4. Participated in online FDP on "**Exploring the modern circuits, Devices, and systems Design in Nano Technology**" at **VIT,** Chennai from April 1st to April 5th 2024.

5. Attended three days online National level FDP on "**Joy of learning Python**" from **Jerusalem College of Engineering,** Chennai.

6.Participated in Five days Online Hands-on FDP on "**Mastering Cadence Tools for Next Generation Semiconductor Design**" at **CMR Institute of Technology ,** Bengaluru.

WORKSHOP ATTENDED:
1. Participated in the one day Workshop on **"Automotive Embedded Applications"** organized by **Robert Bosch Engineering & Business Solutions Limited**, held at **Government College of Technology, Coimbatore.**

2. Participated in the **National Workshop** on **"Recent research trends in nano scale devices, circuits and systems" organized by school of electrical Sciences, Karunya University, Coimbatore.**

3. Participated in the **two days National Workshop on "VLSI Front End Design – SoC System Design Process"**

organized by Caliber Embedded Technologies held at **Sri Ramakrishna Engineering College, Coimbatore.**

4. Participated in the **five days Hands- on Workshop** on **"IoT Applications Using TI CC3200"** conducted by **Texas Instruments and STEPS Knowledge Services Pvt Ltd.**, at Sri Ramakrishna Engineering College, Coimbatore.

5. Participated in Online National level Workshop on "**MATLAB and its applications in Image Processing and Communication**" at **SRM TRP Engineering College** from **Feb 21st to Feb 25th 2022.**

6. Participated in Five days Online Training Programme on "**Basic Statistical Analysis & its interpretation using SPSS**" organized by **CEDAAR,** Chennai from Apr 7th to 11th 2025.

Patent Details	:	
Consultancy	:	-
Funded/ Sponsored Projects	:	-
Industry Certifications	:	1.Undergone **Internship at Caliber Embedded Technologies**, Coimbatore for the period of **six months**. 2. Has undergone five days in-plant training in **Salzer Electronics limited**, Coimbatore. 3. Has undergone five days in-plant training in **Bharat Sanchar Nigam Limited** Coimbatore.
NPTEL Courses Completed	:	1. Completed the Online Course entitled **"Hardware Modeling Using Verilog"** in NPTEL – AICTE Faculty Development Programme. 2. Completed the Online Course entitled **"Antennas"** with **Elite** in NPTEL – AICTE Faculty Development Programme. 3.Completed the Online Course entitled "**Integrated Circuits, MOSFETs, Op-Amps and their Applications"** in NPTEL – AICTE Faculty Development Programme. 4. Completed the Online Course entitled **"Introduction to Internet of Things"** with **Elite** in NPTEL – AICTE Faculty Development Programme.

Professional Societies	:	1.ISTE LIFE MEMBER SINCE 2019 (M.No: LM 126999) 2. IAENG MEMBERSHIP (No: 349484)