


## FACULTY PROFILE

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<b>Anna University Supervisor Recognition No.</b>	4590010
<b>Designation</b>	Assistant Professor
<b>Qualification</b>	M.E., Ph.D.
<b>Teaching Experience</b>	11 Years
<b>Area of Specialization</b>	VLSI Design, Microcontroller and Application
<b>Subjects Handled</b>	<ul style="list-style-type: none"> <li>✓ VLSI Design</li> <li>✓ ASIC Design</li> <li>✓ Digital Electronics</li> <li>✓ Electronics Device</li> <li>✓ Digital Signal Processing</li> <li>✓ Signals and Systems</li> <li>✓ Analog Electronics</li> <li>✓ Electromagnetic Field</li> <li>✓ Analog and Digital Communication</li> <li>✓ Microprocessor and Microcontroller</li> <li>✓ Wireless Communication</li> <li>✓ Transmission Line and Waveguide</li> <li>✓ Testing for VLSI Circuit</li> </ul>

<b>Journals Published</b>	<ul style="list-style-type: none"> <li>✓ Microcontroller PIC 16F877A standard based on solar cooker using PV- evacuated tubes with an extension of heat integrated energy system, ‘Environmental Science and Pollution Research’, volume 29, issue 11, pp.15863-15875, 2022, <b>Impact Factor 5.4 (Springer)</b>, <a href="https://doi.org/10.1007/s11356-021-16863-2">https://doi.org/10.1007/s11356-021-16863-2</a></li> <li>✓ Experimental investigations on the performance of solar cooker using nichrome heating coil - Photovoltaic with Microcontroller PIC 16F877A, ‘Environmental Progress and Sustainable Energy’, volume42,issue03,2023,<b>ImpactFactor2.824(Wiley)</b>,<a href="https://doi.org/10.1002/ep.14028">https://doi.org/10.1002/ep.14028</a></li> <li>✓ The performance enhancement of solar cooker integrated with photovoltaic module and evacuated tubes using ZnO/AcalyphaIndica leaf extract: response surface study analysis, ‘Environmental Science and Pollution Research’, volume 30, Issue 06, pp.15082-15101,2023, <b>Impact Factor 5.4 (Springer)</b>, <a href="https://doi.org/10.1007/s11356-022-23126-1">https://doi.org/10.1007/s11356-022-23126-1</a></li> <li>✓ Performance of solar cooker with evacuated tubes and photovoltaic panels with phase change materials, ‘Energy Efficiency’, volume16,issue7,2023,<b>ImpactFactor3.0(Springer)</b>,<a href="https://doi.org/10.1007/s12053-023-10151-3">https://doi.org/10.1007/s12053-023-10151-3</a></li> <li>✓ A Review on various CMOS circuit style, ‘Material Today Proceedings’ Volume 30, Part 1, 2020, Pages 104-114 (<b>Elsevier</b>), <a href="https://doi.org/10.1016/j.matpr.2020.04.858">https://doi.org/10.1016/j.matpr.2020.04.858</a></li> </ul>
<b>Conference Published</b>	<p><b>International:</b></p> <ul style="list-style-type: none"> <li>✓ Analysis of Si-Ge based N-channel Tunnel FET with high-k spacer, ‘International Journal of Advanced and Innovative Research’, Volume 3, Issue 4, p.536, 2014, ISSN NO: 2278-7844.</li> <li>✓ Dual-k Spacer Device Architecture for Double-Gate silicon n-Channel Tunnel FETs, ‘International Journal of Engineering Associates’, Volume 3, Issue 4, p.45, 2014, ISSN No: 2320-0804.</li> </ul>
<b>Conference Workshop/FDP Organised</b>	<ul style="list-style-type: none"> <li>✓ Advanced ATAL FDP on “Unlocking the Potential of AI : Journey from Transformer Models to Gen AI, Applications from Industry to Space and the Evolving Threat Landscape”.</li> </ul>
<b>Conference Workshop/FDP Attended</b>	<ul style="list-style-type: none"> <li>✓ Participated the Six Days Faculty Development Program on “Recent Advances, Trends and Challenges in Nanomaterials Characterization and Techniques” (RCNMCT-2024) held from 9<sup>th</sup> to 14<sup>th</sup> September 2024 organized by KL University, Guntur, Andhra Pradesh.</li> <li>✓ Participate IEEE sponsored one week Professional Development Programme on “Accelerating AI-Driven Business Innovation: The</li> </ul>

Role of CPUs, GPUs, TPUs and NPUs” organized by S.A. Engineering College, Chennai in association with IEEE Circuits and Systems Society Madras chapter during 16<sup>th</sup> -22<sup>nd</sup> August 2024

- ✓ Presented a paper entitled “Performance enhancement of solar cooker with Phase Change Material” in the International Conference on Advanced Functional Materials for Sustainable Energy Applications(ICAFMSA-2023) organized by Department of Physics, Koneru Lakshmaiah Education Foundation University, Guntur, Andhra Pradesh, India during 6th -8th December 2023.
- ✓ Presented a paper entitled “An Investigation on Utilization of Solar Energy for Microcontroller based solar cooker” in the National Conference on Information and Communication Technologies (NCICT2022) organized by Department of Electronics and Communication Engineering, Sri Sivasubramaniya Nadar College Engineering, Chennai on 23 February 2022.
- ✓ Participated the National level five days online workshop on “MATLAB and its application in Image Processing and Communication” conducted by the Department of Electronics and Communication Engineering, SRM TRP Engineering College, Trichy during the period from 21 February 2022 to 25 February 2022.
- ✓ Participated the three day International Virtual Conference on Advanced Functional Materials (ICAFM-2021) during 13th -15th December, 2021 and organized by Department of Physics, Koneru Lakshmaiah Education Foundation (KLEF) , Andhra Pradesh.
- ✓ Presented a paper entitled “An Investigation of Various CMOS Circuit Styles @120-nm” in the Virtual National Conference on Advanced Materials and Engineering Applications (NCAMEA-2021) organized by Department of Physics, PERI Institute of Technology, Chennai during 5th -6th August 2021.
- ✓ Presented a paper entitled “An Investigation of Various CMOS Circuit Styles in nano device @180-nm” in the International Conference on Nanoscience and Nanotechnology(ICONN2019) organized by Department of Physics and Nano technology, SRMIST-India during 28-30 January 2019, in association with Shizuoka University, Japan.
- ✓ Presented a paper titled “Investigation of MUX Using Various CMOS Circuit style Under Nanometer Technology” in the National Conference on “Cutting Edge Technologies in Computing and Communication (NCCETCC 19)” organized by the department of Computer Science &Engineering held on 27th February2019.
- ✓ Presented a paper titled “ Design and Performance analysis of Multiplexer using Multiple Logic Family” in the International Conference on “Cutting Edge Technologies in Computing and Communication (NCCETCC 18)” organized by the department of

Computer Science & Engineering held on 10-11 January 2018.

- ✓ Presented a paper titled “Power Analysis of Various CMOS Power Logic Families in the National Conference on NCRTIES2K18 at SVIST, Kolundhalur is held on 28th March 2018.

- ✓ Presented a paper titled “Analysis of Various CMOS Power Logic Families” in Department of Science and technology-Science and Engineering Research Board sponsored International Conference on ‘Innovations in Science, Engineering and Technology for Sustainable Development’ held on 30th&31st March, 2018 at Muthayammal Engineering College in Rasipuram.

Participated the Two-days’ workshop on “Introduction to Robotics” organized by Indian Institute of Technology, Bombay (IIT-Bombay) and conducted on 4<sup>th</sup> and 5<sup>th</sup> December 2017 held at Anna University, Chennai.

- ✓ Presented a paper titled “Role of Nano technology in Sharping the future of mobile and wireless devices” in the National Conference on “Developments in Automobile and Mechanical Engineering-2016”(DAME-2016) organized by Department of Automobile and Mechanical Engineering held on 2nd April 2016.

- ✓ Participated the Faculty Development Training Programme on EC6601-VLSI Design at S.A. Engineering College, Chennai, from 15th December to 22nd December, 2016, Organized by Center for Faculty Development, Anna University, Chennai.

- ✓ Participated the Faculty Development Training Programme on EC6403 Electromagnetic fields in Indira Institute of Engineering and Technology, Tiruvallur, during the period from 03<sup>rd</sup> December to 10<sup>th</sup> December, 2014.

- ✓ Participated the Faculty Development Programme on “Recent Trends in Wireless Communication” held on 27<sup>th</sup> May 2013 to 31<sup>st</sup> May 2013 Organized by Department of Electronics and Communication Engineering, Sri Venkateswara College of Engineering and Technology.

- ✓ Participated three days international conference in SKP Engineering College, Tiruvannamalai on “Emerging Trends In VLSI, Embedded Systems, Nano Electronics And Telecommunication Systems”, On January 7-9, 2013.

- ✓ Presented a Paper Titled “Dual-K Spacer Device Architecture for Double Gate Silicon N-Channel Tunnel FET” in International Conference on Futuristic Trends in Electronics Engineering organized by Thiruvalluvar College of Engineering and Technology, Vandavasi in association with International Society of

Automation (ISA) held on 2nd and 3rd March, 2013.

- ✓ Participated the Three-days' workshop on “ Future of Nano Electronics-Research and Challenges Ahead” organized by IEEE EDS and conducted on 26<sup>th</sup>– 28<sup>th</sup> December 2011 held at SKP Engineering College, Tiruvannamalai.

**Patent Details**

- ✓ Method and system for automated legal research and case analysis –Published on 20-12-2024

**Awards**

I have received Grade B award from Indian Institute of Technology, Bombay (IIT-Bombay) for completing all the assigned tasks in Task Based Training (TBT-2017), during December 11, 2017- March 31, 2018 conducted as a part of Teacher Training through the e-Yantra Lab Setup Initiative (eLSI).