


FACULTY PROFILE FORMAT (Format 3)	
Staff Name	: Divya Shree M
Faculty ID	: -
Photo	
scopus id	-
publon id	-
LinkedIn ID	www.linkedin.com/in/divya-shree-m-046972234 .
google scholar id	https://scholar.google.com/citations?view_op=new_profile&hl=en&authuser=2 .
ORCiD id	https://orcid.org/0009-0002-7391-5511 .
Designation	: Assistant Professor
Qualification	: B.E., M.E.
Teaching Experience	: Fresher
Area of Specialization	: VLSI DESIGN
Subjects Handled	: -
Journals Published	: -
Books/Book chapters published	-
FDP/ Conference /Workshop Attended	: <ol style="list-style-type: none"> 1) M. Divya shree, B. Elizabeth Caroline, M. Margarat & S. Mohamed Nizar, "Unified 32-channel SS- DWDM-PON Transmission System for Remote Monitoring", in ICECAA Conference 2022 indexed by IEEE. 2) M. Divya shree, P. T. Vanathi, J. Ramesh, "Low Power Approximate Full Adders using GNRFT", in Computational Innovations and Engineering Sustainability, organized by PSG College of Technology, Coimbatore during April 24-26, 2025. <p>Workshop Attended:</p> <ol style="list-style-type: none"> 1) Completed online workshop on 'Modern VLSI Design and EDA Tools Hands-on', organized by NIT Delhi during 2nd to 8 th November, 2023.
Patent Details	: -
Consultancy	: -
Funded/ Sponsored Projects	: -

Industry Certifications	:	-
NPTEL Courses Completed	:	-
Professional Societies	:	-