ECE 375 Lab 4

Large Number Arithmetic

Lab Time: Wednesday 5-7

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1 Introduction

Adding and multiplying numbers greater than 1 eight bit byte.

2 What We did and Why

We went through the given code for add16 and mul16 to understand what they are doing. To do that we got on the AVR studio and ran the code in debug mode to see what each line was doing. Once the loop functions were identified we were able to make it perform mul24 by increasing the inner and outer loop iterations by one and adjusting Z by an additional location after the loop ceases.

To implement add16 we followed the instructions laid out in the pre-lab. We also drew out what the code would look like before we coded it.

3 Difficulties

We forgot to initialize the stack pointer. When we simulated the execution of our code, we noticed that it was returning from functions to odd places. We could not figure out what was wrong. Once we realized that we had forgotten to initialize the stack pointer things become more clear.

4 Conclusion

We have more respect for calculators.

5 Source Code

```
; ********************
  Enter Name of file here
;*
;*
;*
  Enter the description of the program here
;*
  This is the skeleton file Lab 4 of ECE 375
;*
; *********************
;*
;*
  Author: Jacques Uber, Riley Hickman
    Date: 2/1/2012
;*
.include "m128def.inc"
                   ; Include definition file
```

```
;* Internal Register Definitions and Constants
;*********************
    mpr = r16
.def
                       ; Multipurpose register
.def
    rlo = r0
                       ; Low byte of MUL result
.def rhi = r1
                      ; High byte of MUL result
.def zero = r2
                      ; Zero register, set to zero in INIT, useful for calcula
A = r3
                      ; An operand
.def B = r4
                       ; Another operand
.def
    oloop = r17
                      ; Outer Loop Counter
.def iloop = r18
                      ; Inner Loop Counter
                       ; Beginning Address of Operand B data
.equ addrAS = $0100
.equ addrBS = $0106
.equ addrAM = $010C
                       ; Beginning Address of Operand B data
.equ addrBM = $010C
.equ
    LAddrP = $011C
                       ; Beginning Address of Product Result
.equ HAddrP = $011E
                       ; End Address of Product Result
.equ LAddrS = $010C
.equ HAddrS = $010E
;.equ LAddrS = $0112
; .equ 	 HAddrS = $0114
;* Start of Code Segment
; Beginning of code segment
.cseg
;-----
; Interrupt Vectors
;-----
     $0000
                      ; Beginning of IVs
.org
                     ; Reset interrupt
     rjmp INIT
     $0046
                      ; End of Interrupt Vectors
.org
:-----
; Program Initialization
;-----
INIT:
                       ; The initialization routine
     ; Initialize Stack Pointer
     ; TODO
                       ; Init the 2 stack pointer registers
     ldi
           mpr, High(RAMEND)
           sph, mpr
     out
```

```
mpr, Low(RAMEND)
      ldi
      out
           spl, mpr
                        ; Set the zero register to zero, maintain
      clr
           zero
                        ; these semantics, meaning, don't load anything
                        ; to it.
:------
; Main Program
;-----
MAIN:
                        ; The Main program
      ; Setup the add funtion
      ; Add the two 16-bit numbers
      rcall ADD16
                        ; Call the add function
      ; Setup the multiply function
      ; Multiply two 24-bit numbers
      rcall MUL24
                        ; Call the multiply function
      ;rcall MUL16
                        ; Call the multiply function
DONE:
      rjmp DONE
                        ; Create an infinite while loop to signify the
                        ; end of the program.
; **********************
;* Functions and Subroutines
:-----
; Func: ADD16
; Desc: Adds two 16-bit numbers and generates a 24-bit number
      where the high byte of the result contains the carry
      out bit.
;-----
ADD16:
      ; Save variable by pushing them to the stack
      ; Execute the function here
      ; Restore variable by popping them from the stack in reverse order\
                       ; Save A register
      push
            Α
      push
           В
                        ; Save B register
                       ; Save rhi register
      push rhi
                       ; Save rlo register
      push rlo
                      ; Save zero register
      push zero
```

; Save X-ptr

push XH

```
YΗ
        push
                               ; Save Y-ptr
                YL
        push
        push
                ZH
                                ; Save Z-ptr
                ZL
        push
        push
               oloop
                                ; Save counters
                iloop
        push
        clr
                                ; Maintain zero semantics
                zero
        ; Set Y to beginning address of B
                YL, low(addrBS); Load low byte
                YH, high(addrBS) ; Load high byte
        ldi
        ; Set Z to begginning address of resulting Product
                ZL, low(LAddrS); Load low byte
        ldi
        ldi
                ZH, high(LAddrS); Load high byte
        ; Begin outer for loop
                oloop, 1
                                ; Load counter
; ADD16_OLOOP:
        ; Set X to beginning address of A
                XL, low(addrAS); Load low byte
        ldi
        ldi
                XH, high(addrAS); Load high byte
        ; Begin inner for loop
        ldi
                iloop, 2
                                ; Load counter
ADD16_ILOOP:
                A, X+
                                ; Get byte of A operand
        ld
                                ; Get byte of B operand
        ld
                B, Y+
                                ; Multiply A and B
        adc
                A,B
                Z+, A
        st
                                ; Get byte of A operand
                A, X+
        ld
        ld
                B, Y+
                                ; Get byte of B operand
                                ; Multiply A and B
        adc
                A,B
                Z+, A
        st
        clr
                В
        adc
                B, zero
                                ; Add carry to A
                Z+, B
        st
                iloop
                                ; Restore all registers in reverves order
        pop
                oloop
        pop
                ZL
        pop
                ZH
        pop
```

XL

push

```
pop
              YL
       pop
              YΗ
       pop
              XL
              XH
       pop
       pop
              zero
       pop
              rlo
              rhi
       pop
              В
       pop
       pop
              Α
                              ; End a function with RET
       ret
                                 ; End a function with RET
       ;ret
;-----
; Func: MUL24
; Desc: Multiplies two 24-bit numbers and generates a 48-bit
       result.
MUL24:
       ; Save variable by pushing them to the stack
       ; Execute the function here
       ; Restore variable by popping them from the stack in reverse order\
       push
                             ; Save A register
       push
              В
                              ; Save B register
       push
             rhi
                             ; Save rhi register
       push rlo
                            ; Save rlo register
       push zero
                            ; Save zero register
              XH
                             ; Save X-ptr
       push
       push
             XL
       push
             YΗ
                             ; Save Y-ptr
             YL
       push
       push
             ZH
                             ; Save Z-ptr
       push
             ZL
            oloop
                             ; Save counters
       push
       push
             iloop
       clr
                              ; Maintain zero semantics
              zero
       ; Set Y to beginning address of B
       ldi
               YL, low(addrBM); Load low byte
       ldi
               YH, high(addrBM); Load high byte
       ; Set Z to begginning address of resulting Product
               ZL, low(LAddrP); Load low byte
       ldi
       ldi
               ZH, high(LAddrP); Load high byte
```

```
; Begin outer for loop
                oloop, 3
        ldi
                                 ; Load counter
MUL24_OLOOP:
        ; Set X to beginning address of A
                XL, low(addrAM); Load low byte
        ldi
                XH, high(addrAM)
                                     ; Load high byte
        ; Begin inner for loop
        ldi
                iloop, 3
                                 ; Load counter
MUL24_ILOOP:
        ld
                A, X+
                                 ; Get byte of A operand
                В, У
                                 ; Get byte of B operand
        ld
        mul
                A,B
                                 ; Multiply A and B
        ld
                A, Z+
                                 ; Get a result byte from memory
        ld
                B, Z+
                                 ; Get the next result byte from memory
                                 ; rlo <= rlo + A
        add
                rlo, A
                rhi, B
                                 ; rhi <= rhi + B + carry
        adc
        ld
                A, Z
                                 ; Get a third byte from the result
                A, zero
        adc
                                 ; Add carry to A
                Z, A
                                 ; Store third byte to memory
        st
                                 ; Store second byte to memory
        st
                -Z, rhi
                -Z, rlo
                                 ; Store third byte to memory
        st
                                 ; Z \le Z + 1
        adiw
                ZH:ZL, 1
        dec
                                 ; Decrement counter
                iloop
        brne
                MUL24_ILOOP
                                 ; Loop if iLoop != 0
        ; End inner for loop
        sbiw
                ZH:ZL, 2
                                 ; Z \le Z - 1
                                 ; Y \leftarrow Y + 1
        adiw
                YH:YL, 1
        dec
                oloop
                                 ; Decrement counter
                MUL24_OLOOP
                                 ; Loop if oLoop != 0
        brne
        ; End outer for loop
                                 ; Restore all registers in reverves order
                iloop
        pop
                oloop
        pop
                ZL
        pop
                ZH
        pop
                YL
        pop
                YΗ
        pop
                XL
        pop
                XH
        pop
                zero
        pop
                rlo
        pop
                rhi
        pop
                В
        pop
```

```
pop
              Α
                            ; End a function with RET
       ret
            _____
; Desc: An example function that multiplies two 16-bit numbers
          A - Operand A is gathered from address $0101:$0100
          B - Operand B is gathered from address $0103:$0102
          Res - Result is stored in address
                  $0107:$0106:$0105:$0104
      You will need to make sure that Res is cleared before
       calling this function.
;-----
MUL16:
                            ; Save A register
       push
             Α
                            ; Save B register
       push
             В
                           ; Save rhi register
       push rhi
                           ; Save rlo register
       push rlo
                           ; Save zero register
       push zero
             XH
                           ; Save X-ptr
       push
       push XL
             YΗ
                            ; Save Y-ptr
       push
       push
             YL
             ZH
                            ; Save Z-ptr
       push
             ZL
       push
       push oloop
                            ; Save counters
       push
             iloop
       clr
                            ; Maintain zero semantics
              zero
       ; Set Y to beginning address of B
              YL, low(addrBM); Load low byte
       ldi
       ldi
              YH, high(addrBM); Load high byte
       ; Set Z to begginning address of resulting Product
              ZL, low(LAddrP); Load low byte
       ldi
       ldi
              ZH, high(LAddrP); Load high byte
       ; Begin outer for loop
       ldi
              oloop, 2
                            ; Load counter
MUL16_OLOOP:
       ; Set X to beginning address of A
              XL, low(addrAM); Load low byte
       ldi
              XH, high(addrAM); Load high byte
       ldi
       ; Begin inner for loop
```

```
iloop, 2
       ldi
                             ; Load counter
MUL16_ILOOP:
       ld
              A, X+
                            ; Get byte of A operand
       ld
              В, Y
                             ; Get byte of B operand
              A,B
                            ; Multiply A and B
       mul
       ld
              A, Z+
                            ; Get a result byte from memory
       ld
              B, Z+
                            ; Get the next result byte from memory
       add
              rlo, A
                            ; rlo <= rlo + A
       adc
              rhi, B
                            ; rhi <= rhi + B + carry
              A, Z
       ld
                            ; Get a third byte from the result
              A, zero
                            ; Add carry to A
       adc
              Z, A
                            ; Store third byte to memory
       st
              -Z, rhi
                            ; Store second byte to memory
                            ; Store third byte to memory
       st
              -Z, rlo
                             ; Z \le Z + 1
       adiw
              ZH:ZL, 1
       dec
                             ; Decrement counter
              iloop
              MUL16_ILOOP
                             ; Loop if iLoop != 0
       brne
       ; End inner for loop
                            ; Z \le Z - 1
       sbiw
              ZH:ZL, 1
       adiw
                             ; Y \le Y + 1
              YH:YL, 1
       dec
              oloop
                             ; Decrement counter
              MUL16_OLOOP
                             ; Loop if oLoop != 0
       brne
       ; End outer for loop
       pop
              iloop
                             ; Restore all registers in reverves order
       pop
              oloop
              ZL
       pop
              ZH
       pop
              YL
       pop
              YΗ
       pop
              XL
       pop
              XH
       pop
       pop
              zero
             rlo
       pop
              rhi
       pop
              В
       pop
              Α
       pop
                             ; End a function with RET
       ret
:-----
; Func: Template function header
; Desc: Cut and paste this and fill in the info at the
  beginning of your functions
;-----
```

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; Begin a function with a label

FUNC:

- ; Save variable by pushing them to the stack
- ; Execute the function here
- ; Restore variable by popping them from the stack in reverse order\ ret $\,$; End a function with RET $\,$

; Enter any stored data you might need here

;********************

; There are no additional file includes for this program $% \left(1\right) =\left(1\right) \left(1\right)$