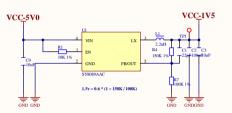
Power Scheme

Sakura Pi RK3308B

SY8089A

DRAM 1.5v/2A OUTPUT

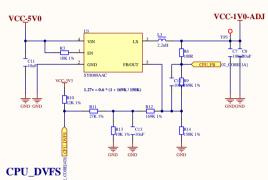


SY8089A

CORE-LOGIC 1.0v/2A OUTPUT

SY8089A

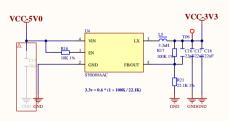
CPU-CORE 1.0-1.35v/2A OUTPUT



Using CPU PWM output for interfering with the feedback cause the output voltage can be kept in the range of 1.0v to 1.35v

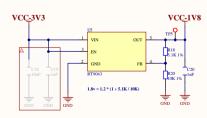
SY8089A

3V3-IO 3.3v/2A OUTPUT



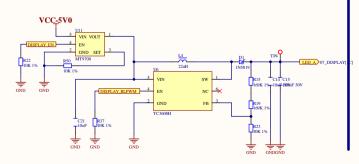
TMI6030/RT9043

1V8-IO 1.8v/300mA OUTPUT



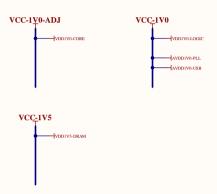
TC3608H

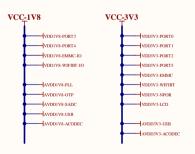
DISPLAY-BG 21v/20mA OUTPUT



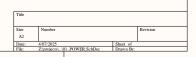
Power Tree











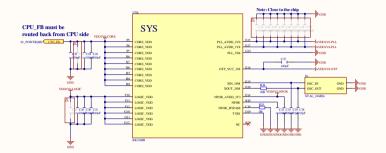
CPU Core Sakura Pi RK3308B -

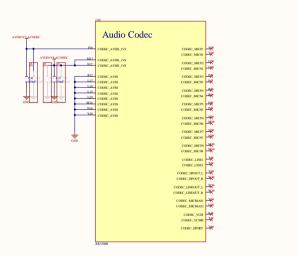
Speed

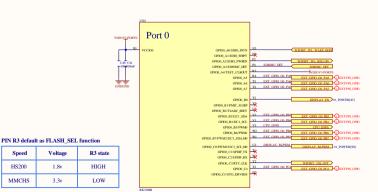
HS200

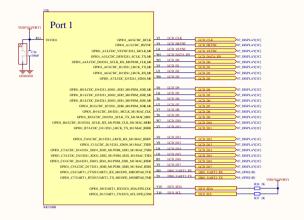
MMCHS

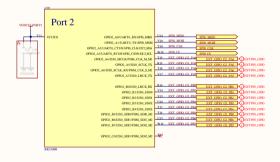
Voltage



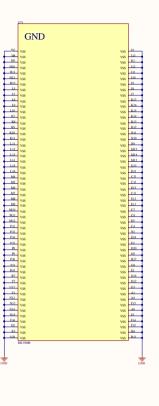














DRAM 1 x 16Bit 512MB

Sakura Pi RK3308B -

